Unit: mm

TOSHIBA High Efficiency Rectifier Silicon Epitaxial Type

# **CMH07**

### Switching Mode Power Supply Applications

Repetitive peak reverse voltage:  $V_{RRM} = 200 \text{ V}$ 

Average forward current: IF (AV) = 2.0 A

Low forward voltage:  $V_{FM} = 0.98 \text{ V}$ 

Very Fast Reverse-Recovery Time: trr = 35 ns (Max)

Suitable for compact assembly due to small surface-mount package "M-FLAT $^{\mathrm{TM}}$ " (Toshiba package name)

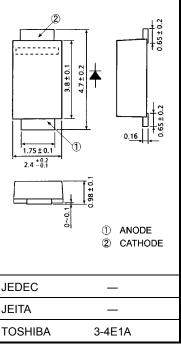
## Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$	200	V
Average forward current	I <sub>F (AV)</sub>	2.0(Note)	Α
Peak one cycle surge forward current (non-repetitive)	I <sub>FSM</sub>	40 (50 Hz)	Α
Junction temperature	Tj	− <b>40~150</b>	°C
Storage temperature range	T <sub>stg</sub>	-40~150	°C

Ta=35°C Note:

> Device mounted on a ceramic board board size: 50 mm  $\times$  50 mm soldering land: 2mm ×2mm

> glass-epoxy board thickness 0.64t



Weight: 0.023 g (typ.)

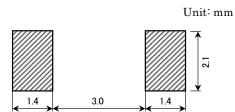
# **Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
	V <sub>FM (1)</sub>	I <sub>FM</sub> = 0.1 A (pulse test)	_	0.68	_		
Peak forward voltage	V <sub>FM (2)</sub>	I <sub>FM</sub> = 1.0 A (pulse test)	_	0.83	_	V	
	V <sub>FM (3)</sub>	I <sub>FM</sub> = 2.0 A (pulse test)	_	0.91	0.98		
Peak repetitive reverse current	I <sub>RRM</sub>	V <sub>RRM</sub> = 200 V (pulse test)	_	_	10	μA	
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 1 A, di/dt = -30 A/μs	_	_	35	ns	
Forward recovery time	t <sub>fr</sub>	I <sub>F</sub> = 1 A	_	_	100	ns	
Thermal resistance (junction to ambient)	R <sub>th (j-a)</sub>	Device mounted on a ceramic board (board size: 50 mm × 50 mm) (soldering land: 2 mm × 2 mm) (board thickness: 0.64 t)	_	_	60	pF	
		Device mounted on a glass-epoxy board (board size: 50 mm × 50 mm) (soldering land: 6 mm × 6 mm) (board thickness: 1.6 t)	_	_	135	°C/W	
		Device mounted on a glass-epoxy board (board size: 50 mm × 50 mm) (soldering land: 2.1 mm × 1.4 mm) (board thickness: 1.6 t)	_	_	210		
Thermal resistance (junction to lead)	R <sub>th (j-l)</sub>	_	_	_	16	°C/W	

#### Marking

Abbreviation Code	Part No.		
H7	CMH07		

#### **Standard Soldering Pad**



#### **Handling Precaution**

The maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

VRRM: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the maximum rating of VRRM for a DC circuit and be no greater than 50% of that of VRRM for an AC circuit.

VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.

IF(AV): We recommend that the worst case current be no greater than 80% of the maximum rating of IF(AV). Carry out adequate heat design. If you can't design a circuit with excellent heat radiation, set the margin by using an allowable Tamax-IF (AV) curve.

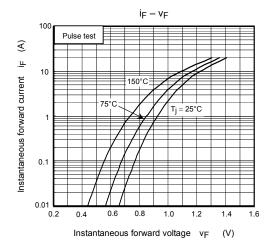
This rating specifies the non-repetitive peak current in one cycle of a 50-Hz sine wave, condition angle 180. Therefore, this is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.

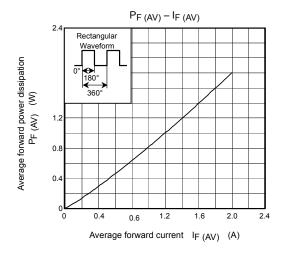
We recommend that a device be used at a Tj of below 120°C under the worst load and heat radiation conditions.

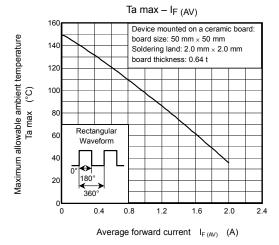
Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.

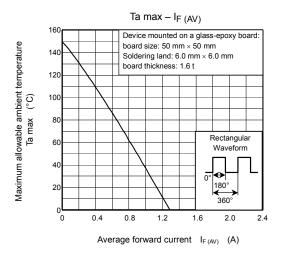
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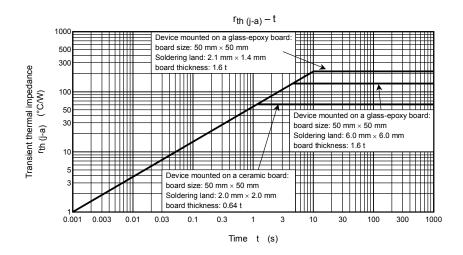
Please refer to the Rectifiers databook for further information.



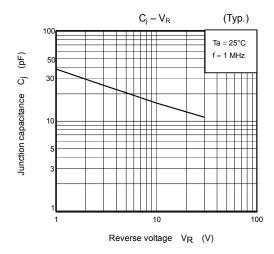


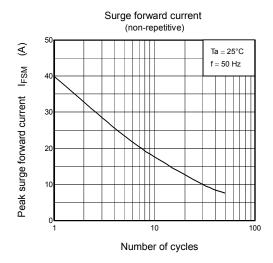






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