

### FGP50B thru FGP50D

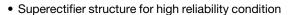
### Vishay General Semiconductor

# **Glass Passivated Ultrafast Rectifier**



| PRIMARY CHARACTERISTICS |                |  |  |  |  |
|-------------------------|----------------|--|--|--|--|
| I <sub>F(AV)</sub>      | 5.0 A          |  |  |  |  |
| $V_{RRM}$               | 100 V to 200 V |  |  |  |  |
| I <sub>FSM</sub>        | 135 A          |  |  |  |  |
| t <sub>rr</sub>         | 35 ns          |  |  |  |  |
| $V_{F}$                 | 0.95 V         |  |  |  |  |
| I <sub>R</sub>          | 5.0 μA         |  |  |  |  |
| T <sub>J</sub> max.     | 175 °C         |  |  |  |  |

#### **FEATURES**





- · Cavity-free glass-passivated junction
- , , ,
- Ultrafast reverse recovery time
- · Low forward voltage drop
- Low leakage current
- · Low switching losses, high efficiency
- High forward surge capability
- Meets environmental standard MIL-S-19500
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

#### **TYPICAL APPLICATIONS**

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer, computer, automotive and telecommunication.

#### **MECHANICAL DATA**

**Case:** GP20, molded epoxy over glass body Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade Base P/NHE3 - RoHS compliant, AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

| MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)                    |                                   |               |        |        |      |
|--|-----------------------------------|---------------|--------|--------|------|
| PARAMETER  | SYMBOL                            | FGP50B        | FGP50C | FGP50D | UNIT |
| Maximum repetitive peak reverse voltage  | $V_{RRM}$                         | 100           | 150    | 200    | V    |
| Maximum RMS voltage  | V <sub>RMS</sub>                  | 70            | 105    | 140    | V    |
| Maximum DC blocking voltage  | $V_{DC}$                          | 100           | 150    | 200    | V    |
| Maximum average forward rectified current 0.375" (9.5 mm) lead length (fig. 1)     | I <sub>F(AV)</sub>                | 5.0           |        |        | Α    |
| Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load | I <sub>FSM</sub>                  | 135           |        |        | А    |
| Operating junction and storage temperature range                                   | T <sub>J</sub> , T <sub>STG</sub> | - 65 to + 175 |        |        | °C   |

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# FGP50B thru FGP50D

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| <b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted) |   |                         |                                    |        |        |        |      |
|---|---|-------------------------|------------------------------------|--------|--------|--------|------|
| PARAMETER   | TEST CONDITIONS   |                         | SYMBOL                             | FGP50B | FGP50C | FGP50D | UNIT |
| Maximum instantaneous forward voltage   | 5.0 A   |                         | V <sub>F</sub> <sup>(1)</sup> 0.95 |        | 0.95   |        | V    |
| Maximum DC reverse current at rated DC blocking voltage                           |   | T <sub>A</sub> = 25 °C  | L_                                 | 5.0    |        |        | - μΑ |
|   |   | T <sub>A</sub> = 100 °C | I <sub>R</sub>                     | 50     |        |        |      |
| Maximum reverse recovery time   | I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A,<br>I <sub>rr</sub> = 0.25 A |                         | t <sub>rr</sub>                    | 35     |        | ns     |      |
| Typical junction capacitance  | 4.0 V, 1  | MHz                     | C <sub>J</sub> 100                 |        |        | pF     |      |

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

| THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted) |                      |        |        |        |      |
|---|----------------------|--------|--------|--------|------|
| PARAMETER   | SYMBOL               | FGP50B | FGP50C | FGP50D | UNIT |
| Typical thermal resistance  | R <sub>0JA</sub> (1) | 60     |        |        | °C/W |
|   | R <sub>0JL</sub> (2) |        | 20     |        | C/VV |

#### Notes

- (1) Thermal resistance from junction to lead at 0.375" (9.5 mm) lead length with both leads attached to heatsinks
- (2) Thermal resistance from junction to ambient at 0.375" (9.5 mm) lead length and mounted on PCB

| ORDERING INFORMATION (Example) |                 |                        |               |                                  |  |  |
|--------------------------------|-----------------|------------------------|---------------|----------------------------------|--|--|
| PREFERRED P/N                  | UNIT WEIGHT (g) | PREFERRED PACKAGE CODE | BASE QUANTITY | DELIVERY MODE                    |  |  |
| FGP50D-E3/54                   | 1.01            | 54                     | 1400          | 13" diameter paper tape and reel |  |  |
| FGP50D-E3/73                   | 1.01            | 73                     | 2000          | Ammo pack packaging              |  |  |
| FGP50DHE3/54 <sup>(1)</sup>    | 1.01            | 54                     | 1400          | 13" diameter paper tape and reel |  |  |
| FGP50DHE3/73 <sup>(1)</sup>    | 1.01            | 73                     | 2000          | Ammo pack packaging              |  |  |

#### Note

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

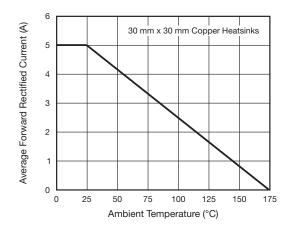


Fig. 1 - Maximum Forward Current Derating Curve

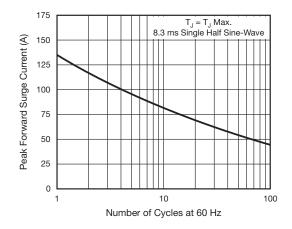


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

<sup>(1)</sup> AEC-Q101 qualified



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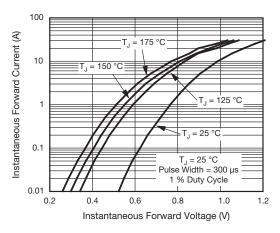


Fig. 3 - Typical Instantaneous Forward Characteristics

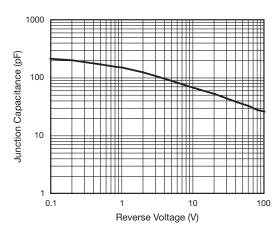


Fig. 5 - Typical Junction Capacitance

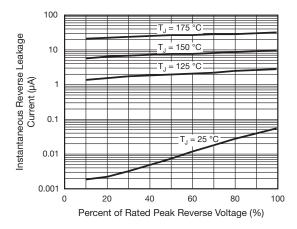
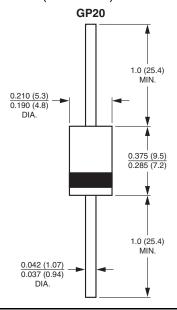


Fig. 4 - Typical Reverse Leakage Characteristics

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



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For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

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