**HALOGEN** 



### Vishay General Semiconductor

# **High Current Density Surface Mount** Trench MOS Barrier Schottky Rectifier

Ultra Low  $V_F = 0.51 \text{ V}$  at  $I_F = 6 \text{ A}$ 

### eSMP<sup>™</sup> Series TMBS<sup>®</sup>



#### TO-277A (SMPC)



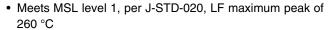
PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	12 A		
V <sub>RRM</sub>	120 V		
I <sub>FSM</sub>	150 A		
E <sub>AS</sub>	100 mJ		
V <sub>F</sub> at I <sub>F</sub> = 12 A	0.63 V		
T <sub>J</sub> max.	150 °C		

#### **TYPICAL APPLICATIONS**

For use in low voltage high frequency inverters, freewheeling, dc-to-dc converters and polarity protection applications.

#### **FEATURES**

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- · Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation



- · AEC-Q101 qualified
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition
- Find out more about Vishay's Automotive Grade Product requirements at: www.vishay.com/applications

#### **MECHANICAL DATA**

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free and RoHS compliant,

commercial grade

Base P/NHM3 halogen-free and RoHS compliant,

automotive grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test. HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V12P12	UNIT	
Device marking code		V1212		
Maximum repetitive peak reverse voltage	$V_{RRM}$	120	V	
Maximum average forward rectified current (fig. 1)	I <sub>F(AV)</sub>	12	Α	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	150	А	
Non-repetitive avalanche energy at $I_{AS} = 2.0 \text{ A}$ , $L = 50 \text{ mH}$ , $T_J = 25 ^{\circ}\text{C}$	E <sub>AS</sub>	100	mJ	
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz, $T_J$ = 38 °C ± 2 °C	I <sub>RRM</sub> 0.5		А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 40 to + 150	°C	

# V12P12

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Breakdown voltage	I <sub>R</sub> = 1.0 mA	T <sub>A</sub> = 25 °C	$V_{BR}$	120 (minimum)	-	V
Instantaneous forward voltage	I <sub>F</sub> = 6 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.57	-	V
	I <sub>F</sub> = 12 A			0.72	0.80	
	I <sub>F</sub> = 6 A	T <sub>A</sub> = 125 °C		0.51	-	
	I <sub>F</sub> = 12 A			0.63	0.70	
Reverse current	V <sub>R</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	13	-	μΑ
		T <sub>A</sub> = 125 °C		7	-	mA
	V <sub>R</sub> = 120 V	T <sub>A</sub> = 25 °C		50	500	μΑ
		T <sub>A</sub> = 125 °C		16	50	mA

#### **Notes**

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V12P12	UNIT	
Typical thermal registance	R <sub>θJA</sub> <sup>(1)</sup>	60	°C/W	
Typical thermal resistance	$R_{ heta JL}$	4		

#### Note

(1) Units mounted on recommended P.C.B. 1 oz. pad layout

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V12P12-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V12P12-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	
V12P12HM3/86A (1)	0.10	86A	1500	7" diameter plastic tape and reel	
V12P12HM3/87A (1)	0.10	87A	6500	13" diameter plastic tape and reel	

#### Note

(1) AEC-Q101 qualified



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#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

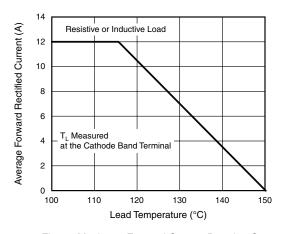


Fig. 1 - Maximum Forward Current Derating Curve

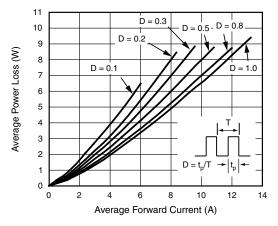


Fig. 2 - Forward Power Loss Characteristics

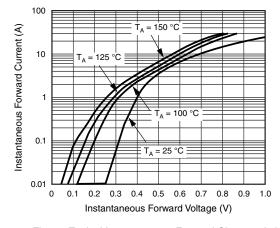


Fig. 3 - Typical Instantaneous Forward Characteristics

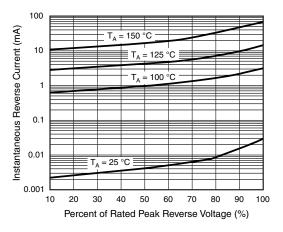


Fig. 4 - Typical Reverse Characteristics

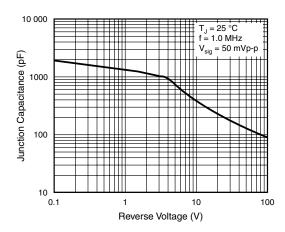


Fig. 5 - Typical Junction Capacitance

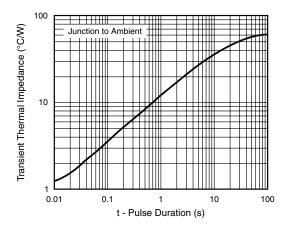
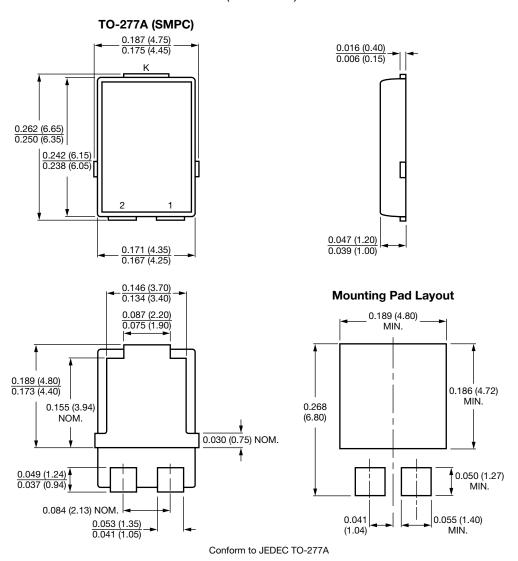


Fig. 6 - Typical Transient Thermal Impedance

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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



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