

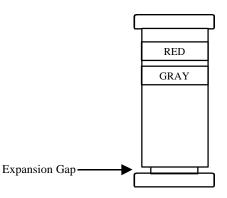
SEMICONDUCTOR IM

# FDLL485B

### **General Description:**

A General Purpose diode that couples high forward conductance fast switching speed and high blocking voltages in a glass leadless LL-34 Surface Mount package.

Placement of the Expansion Gap has no relationship to the location of the Cathode Terminal which is indicated by the first color band.



# High Conductance, Low Leakage Diode Absolute Maximum Ratings\* TA = 25°C unless otherwise noted

Sym	Parameter	Value	Units
T <sub>stg</sub>	Storage Temperature	-65 to +200	°C
TJ	Operating Junction Temperature	-65 to +200	О <sup>о</sup>
P <sub>D</sub>	Total Power Dissipation at $T_A = 25^{\circ}C$	500	mW
	Linear Derating Factor from $T_A = 25^{\circ}C$	3.33	mW/ <sup>o</sup> C
R <sub>OJA</sub>	Thermal Resistance Junction-to-Ambient	350	°C/W
W <sub>iv</sub>	Working Inverse Voltage	180	V
I <sub>o</sub>	Average Rectified Current	200	mA
I <sub>F</sub>	DC Forward Current (IF)	500	mA
i <sub>f</sub>	Recurrent Peak Forward Current	600	mA
i <sub>F(surge)</sub>	Peak Forward Surge Current (IFSM) Pulse Width = 1.0 second	1.0	Amp
. 57	Pulse Width = 1.0 microsecond	4.0	Amp

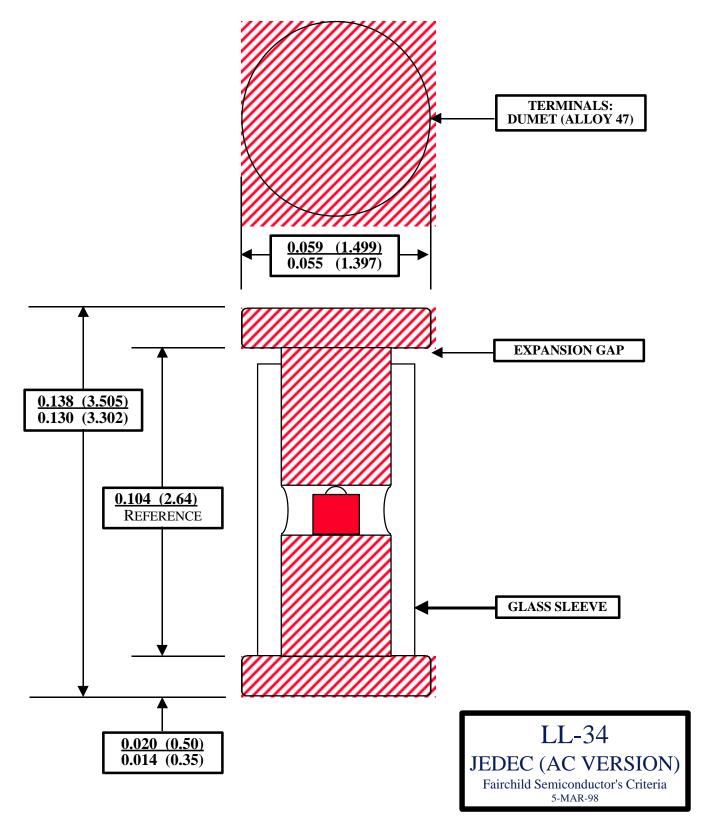
\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired

## **Electrical Characteristics** TA = 25<sup>o</sup>C unless otherwise noted

SYM CH	IARACTERISTICS	MIN	МАХ	UNITS	TEST CONDITIONS
B <sub>v</sub> Breako	lown Voltage	200		V	I <sub>R</sub> = 100 uA
I <sub>R</sub> Revers	se Leakage		25 5.0	nA uA	$V_{R} = 180 V$ $V_{R} = 180 V T_{A} = 150^{\circ}C$
V <sub>F</sub> Forwar	d Voltage		1.00	V	I <sub>F</sub> = 100 mA
C <sub>T</sub> Capac	itance		6.0	pF	$V_{R} = 0.0 V, f = 1.0 MHz$
997 Fairchild Semiconductor Corporation					



THE PLACEMENT OF THE EXPANSION GAP HAS NO RELATIONSHIP TO THE LOCATION OF THE CATHODE TERMINAL OF THE DEVICE. THE EXPANSION GAP & CATHODE BAND CAN BE ON THE SAME TERMINAL OR AT OPPOSITE TERMINALS OF THE DIODE.



#### TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx<sup>TM</sup> Bottomless<sup>TM</sup> CoolFET<sup>TM</sup>  $CROSSVOLT^{TM}$ DOME<sup>TM</sup> E<sup>2</sup>CMOS<sup>TM</sup> EnSigna<sup>TM</sup> FACT<sup>TM</sup> FACT Quiet Series<sup>TM</sup> FAST ® FASTr<sup>™</sup> GlobalOptoisolator<sup>™</sup> GTO<sup>™</sup> HiSeC<sup>™</sup> ISOPLANAR<sup>™</sup> MICROWIRE<sup>™</sup> OPTOLOGIC<sup>™</sup> OPTOPLANAR<sup>™</sup> PACMAN<sup>™</sup> POP<sup>™</sup> PowerTrench® QFET™ QS™ QT Optoelectronics™ Quiet Series™ SILENT SWITCHER® SMART START™ SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SyncFET™ TinyLogic™ UHC™ VCX™

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user. 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### PRODUCT STATUS DEFINITIONS

Definition of Terms

Product Status	Definition
Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.
	Formative or In Design First Production Full Production