TOSHIBA High Efficiency Rectifier Silicon Epitaxial Type

# CMH08A

### Switching Mode Power Supply Applications

• Repetitive peak reverse voltage:  $V_{RRM} = 400 \text{ V}$ 

• Average forward current: IF (AV) = 2.0 A

• Low forward voltage: V<sub>FM</sub>=1.8 V(Max.)

• Very Fast Reverse-Recovery Time: trr =35ns(Max.)

• Suitable for compact assembly due to small surface mount package "M-FLAT  $^{TM}$ " (Toshiba package name)

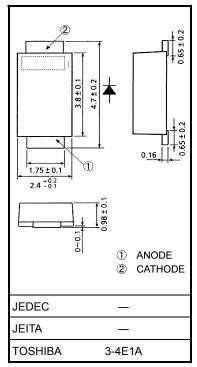
## Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	$V_{RRM}$	400	V
Average forward current	I <sub>F (AV)</sub>	2.0(Note)	Α
Peak one cycle surge forward current (non-repetitive)	I <sub>FSM</sub>	20 (50 Hz)	А
Junction temperature	Tj	-40~150	°C
Storage temperature range	T <sub>stg</sub>	-40~150	°C

Note: Tℓ=99°C

Device mounted on a seramic board board size:  $50~\text{mm} \times 50~\text{mm}$  soldering land:  $2~\text{mm} \times 2~\text{mm}$  board thickness:0.64t

Unit: mm



Weight: 0.023 g (typ.)

## **Electrical Characteristics (Ta = 25°C)**

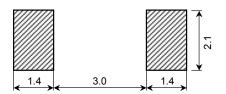
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
	V <sub>FM (1)</sub>	I <sub>FM</sub> = 0.1 A (pulse test)	_	0.88	_		
Peak forward voltage	V <sub>FM (2)</sub>	I <sub>FM</sub> = 1.0 A (pulse test)	_	1.3	_	V	
	V <sub>FM (3)</sub>	I <sub>FM</sub> = 2.0 A (pulse test)	_	1.6	1.8		
Peak repetitive reverse current	I <sub>RRM</sub>	V <sub>RRM</sub> = 400 V (pulse test)	_	_	10	μA	
Reverse recovery time	t <sub>rr</sub>	$I_F = 1 \text{ A, di/dt} = -30 \text{ A/}\mu\text{s}$	_	_	35	ns	
Forward recovery time	t <sub>fr</sub>	I <sub>F</sub> = 1.0 A	_	_	100	ns	
Thermal resistance (junction to ambient)	R <sub>th (j-a)</sub>	Device mounted on a ceramic board (board size: 50 mm × 50 mm) (soldering land: 2 mm × 2 mm) (board thickness: 0.64 t)	_	_	60		
		Device mounted on a glass-epoxy board (board size: 50 mm × 50 mm) (soldering land: 6 mm × 6 mm) (board thickness: 1.6 t)	_	_	135	°C/W	
		Device mounted on a glass-epoxy board (board size: 50 mm × 50 mm) (soldering land: 2.1 mm × 1.4 mm) (board thickness: 1.6 t)	_	_	210		
Thermal resistance (junction to lead)	R <sub>th (j-ℓ)</sub>	_	_	_	16	°C/W	

#### Marking

Type code	Product No.	
<u>H8</u>	CMH08A	

### Standard Soldering Pad





### **Handling Precaution**

The maximum ratings mean the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The followings are the general derating method that we recommend when you design a circuit with a device.

VRRM: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the maximum rating of VRRM for a DC circuit and be no greater than 50% of that of VRRM for an AC circuit.

VRRM has a temperature coefficient (0.1%/°C). Please consider this temperature coefficient when you design a device at low temperature.

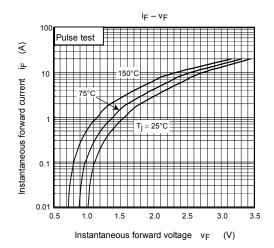
IF(AV): We recommend that the worst case current be no greater than 80% of the maximum rating of IF(AV). Please do the sufficient heat design. If you can't design a circuit with excellent heat radiation, please set a margin by using an allowable Tamax-IF (AV) curve.

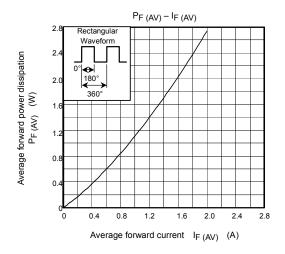
This rating specifies the non-repetitive peak current in one cycle of a 50-Hz sine wave, condition angle  $180^{\circ}$ . Therefore, this is only applied for an abnormal operation, which seldom occurs during device's lifespan.

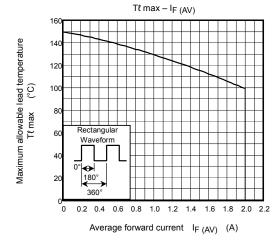
We recommend that a device be used Tj of below 120°C under the worst load and heat radiation conditions.

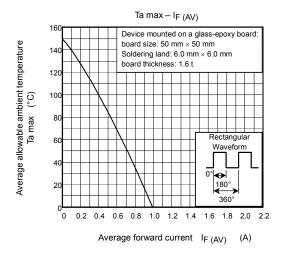
Thermal resistance between junction and ambient fluctuates depending on device's mounting condition. When using a device, please design a circuit board and a soldering land size to match the appropriate thermal resistance value.

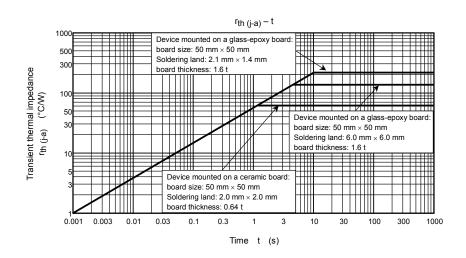
Please refer to Rectifier databook for further information.



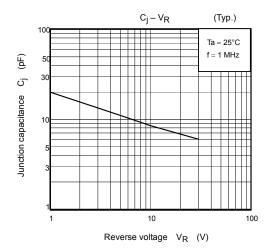


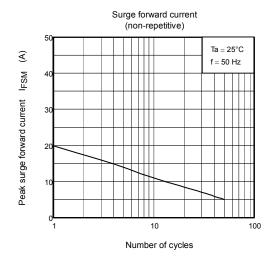






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