

## STPS5L60

## Power Schottky rectifier

### **Features**

- Negligible switching losses
- Low forward voltage drop for higher efficiency
- Low thermal resistance
- Avalanche capability specified

### **Description**

Power Schottky rectifier suited for switch mode power supplies and high frequency inverters.

This device is intended for use in low voltage output for small battery chargers and consumer SMPS such as DVD and set-top-box.

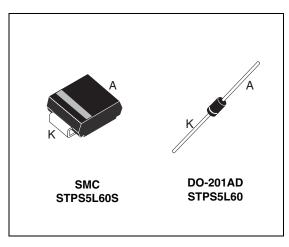


Table 1. Device summary

I <sub>F(AV)</sub>	5 A
V <sub>RRM</sub>	60 V
T <sub>j (max)</sub>	150 °C
V <sub>F (max)</sub>	0.53 V

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#### **Characteristics** 1

Table 2. **Absolute ratings (limiting values)** 

Symbol	Parameter			Value	Unit
$V_{RRM}$	Repetitive peak reverse voltage	Repetitive peak reverse voltage			V
I <sub>F(RMS)</sub>	RMS forward current			15	Α
	Average forward current	DO-201AD	$T_{I} = 100  ^{\circ}\text{C}  \delta = 0.5$	- 5	^
'F(AV)	I <sub>F(AV)</sub> Average forward current		$T_{I} = 100  ^{\circ}\text{C}  \delta = 0.5$	3	Α
I <sub>FSM</sub>	Surge non repetitive forward current	Half wave, s t <sub>p</sub> = 10 ms	ingle phase	150	Α
P <sub>ARM</sub>	Repetitive peak avalanche power $t_p = 1 \mu s T_j = 25  ^{\circ}C$			4000	W
T <sub>stg</sub>	Storage temperature range			-65 to + 175	°C
Tj	Maximum operating junction temperature <sup>(1)</sup>			150	°C
dV/dt	Critical rate of rise of reverse voltage (rated $V_R$ , $T_j = 25$ °C)			10000	V/µs

<sup>1.</sup>  $\frac{dPtot}{dTj} < \frac{1}{Rth(j-a)}$  condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal parameters

Symbol	Parameter			Value	Unit
R <sub>th (j-a)</sub>	Junction to ambient	DO-201AD		75	
R <sub>th (j-l)</sub>	Junction to leads	DO-201AD	Lead length = 10mm	15	°C/W
R <sub>th (j-l)</sub>	Junction to leads	SMC		15	

Table 4. Static electrical characteristics

Symbol	Parameter	Tests conditions		Min.	Тур.	Max.	Unit
I <sub>R</sub> <sup>(1)</sup> Reverse leakage current		T <sub>j</sub> = 25 °C				0.22	
	T <sub>j</sub> = 100 °C	$V_R = V_{RRM}$		10	25	mA	
		T <sub>j</sub> = 125 °C			40	100	
	V <sub>F</sub> <sup>(1)</sup> Forward voltage drop	T <sub>j</sub> = 25 °C			0.47	0.52	
V <sub>F</sub> <sup>(1)</sup>		T <sub>j</sub> = 100 °C	I <sub>F</sub> = 5 A		0.43	0.49	V
		T <sub>j</sub> = 125 °C			0.42	0.48	

<sup>1.</sup> Pulse test : tp = 380  $\mu$ s,  $\delta$  < 2%

To evaluate the conduction losses use the following equation : P = 0.39 x  $I_{F(AV)}$  + 0.028x  $I_{F}^2_{(RMS)}$ 

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Figure 1. Conduction losses versus average Figure 2. Average forward current versus current ambient temperature ( $\delta$  = 0.5)

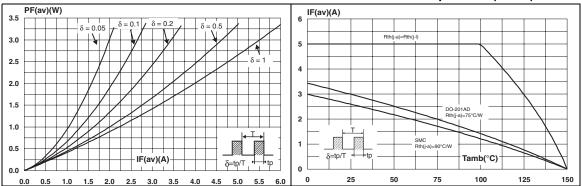


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

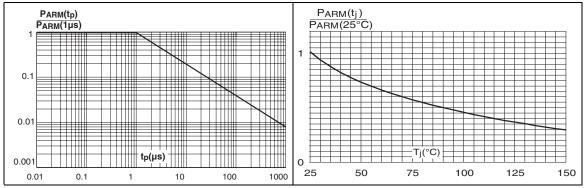
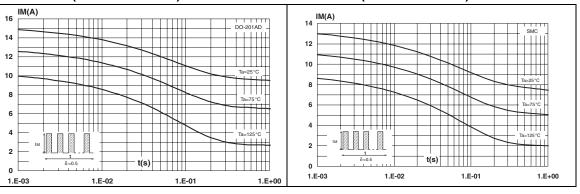


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values) DO-201AD

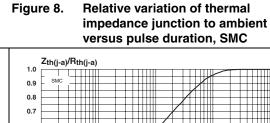
Figure 6. Non repetitive surge peak forward current versus overload duration (maximum values) SMC



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Figure 8.

Figure 7. Relative variation of thermal impedance junction to ambient versus pulse duration, DO-201AD



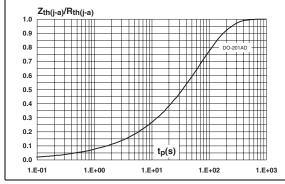


Figure 9. Reverse leakage current versus reverse voltage applied (typical values)

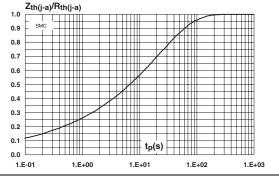
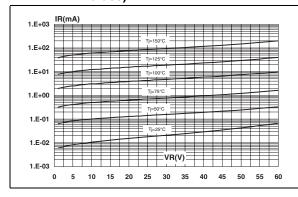
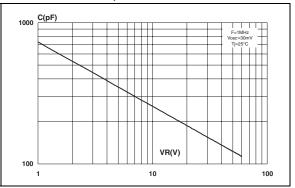


Figure 10. Junction capacitance versus reverse voltage applied (typical values)





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Figure 11. Forward voltage drop versus forward current (low level)

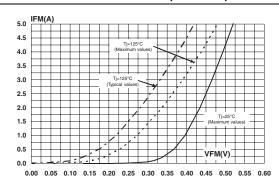


Figure 12. Forward voltage drop versus forward current (high level)

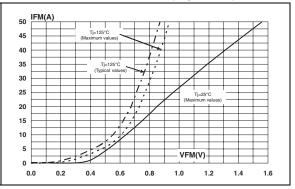
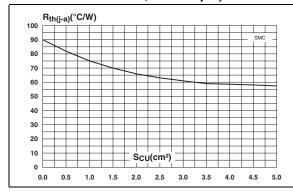


Figure 13. Thermal resistance junction to ambient versus copper surface under each lead (epoxy printed board FR4, Cu = 35 µm) SMC

Figure 14. Thermal resistance junction to ambient versus copper surface under each lead (epoxy printed board FR4, Cu = 35  $\mu$ m), DO-201AD



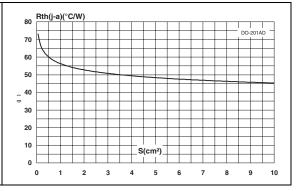
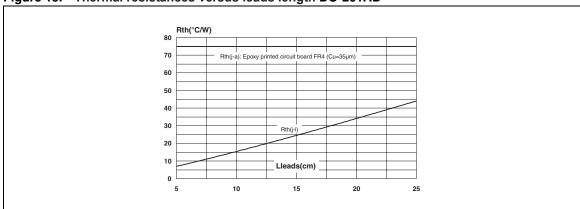


Figure 15. Thermal resistances versus leads length DO-201AD



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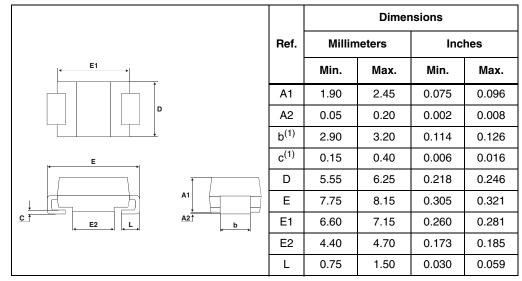
Package information STPS5L60

## 2 Package information

#### Epoxy meets UL94, V0

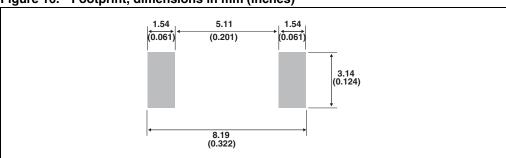
In order to meet environmental requirements, ST offers these devices in ECOPACK<sup>®</sup> packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at <a href="https://www.st.com">www.st.com</a>.

Table 5. SMC dimensions



<sup>1.</sup> Dimensions b and c apply to plated leads

Figure 16. Footprint, dimensions in mm (inches)



STPS5L60 Ordering information

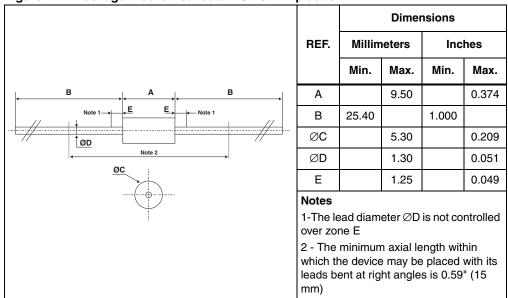


Figure 17. Package mechanical data DO-201AD plastic

# 3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS5L60	STPS5L60	D0-201AD	1.12 g	600	Ammopack
STPS5L60RL	STPS5L60	D0-201AD	1.12 g	1900	Tape and reel
STPS5L60S	S56	SMC	0.245 g	2500	Tape and reel

# 4 Revision history

Table 7. Document revision history

Date	Revision	Description of changes
July-2003	2	Previous issue.
16-May-2008	3	Added ECOPACK statement. Added SMC package. Updated characteristic curves.

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