STTH102



High efficiency ultrafast diode

Main product characteristics

I _{F(AV)}	1 A
V _{RRM}	200 V
T _j (max)	175° C
V _F (max)	0.78 V
t _{rr} (max)	20 ns

Features and benefits

- Very low conduction losses
- Negligible switching losses
- Low forward and reverse recovery times
- High junction temperature

Description

The STTH102, which is using ST's new 200 V planar technology, is specially suited for switching mode base drive and transistor circuits. The device is also intended for use as a free wheeling diode in power supplies and other power switching applications.

K K SMA DO-41 (JEDEC DO-214AC) STTH102 STTH102A STTH102 Order codes Order codes O V Part Number Marking

Part Number	Marking
STTH102A	U12
STTH102	STTH102
STTH102RL	STTH102

	Value	Unit			
Repetitive peak reverse voltage			200	V	
Average ferward ourrept	SMA	$T_{L} = 148^{\circ} C \delta = 0.5$	1	А	
AVerage forward current	DO-41	$T_{L} = 130^{\circ} C \delta = 0.5$		A	
Surge non repetitive forward	SMA	to - 10 ma Sinusaidal	40	А	
IFSM current	DO-41		50	~	
Storage temperature range	-65 to + 175	°C			
Maximum operating junction temperature			175	°C	
Critical rate of rise of reverse voltage			10000	V/µs	
	Repetitive peak reverse voltage Average forward current Surge non repetitive forward current Storage temperature range Maximum operating junction ter	Parameter Repetitive peak reverse voltage Average forward current SMA DO-41 Surge non repetitive forward current SMA DO-41 Storage temperature range Maximum operating junction temperature	Parameter Parameter Repetitive peak reverse voltage Average forward current SMA $T_L = 148^\circ$ C $\delta = 0.5$ DO-41 $T_L = 130^\circ$ C $\delta = 0.5$ Surge non repetitive forward current SMA $T_P = 10 \text{ ms Sinusoidal}$ Storage temperature range Maximum operating junction temperature	ParameterValueRepetitive peak reverse voltage200Average forward currentSMA $T_L = 148^\circ$ C $\delta = 0.5$ 1 $DO-41$ $T_L = 130^\circ$ C $\delta = 0.5$ 1Surge non repetitive forward currentSMA DO-41 $tp = 10 \text{ ms Sinusoidal}$ 40Storage temperature range-65 to + 175Maximum operating junction temperature175	

Table 1. Absolute ratings (limiting values)

1 Characteristics

Table 2.Thermal resistance

Symbol	Parameter			Value	Unit
P	Junction to lead		SMA	30	°C/W
R _{th(j-l)}	Sufficient to lead	Lead length = 10 mm	DO-41	50	0/11

Table 3. Static Electrical Characteristics

Symbol	Parameter	Tests conditions		Min.	Тур	Max.	Unit
ı (1)	I _R ⁽¹⁾ Reverse leakage current	$T_j = 25^\circ C$	V _V			1	uЛ
'R `´		T _j = 125° C	$V_{R} = V_{RRM}$		1	25	μA
	V _F ⁽²⁾ Forward voltage drop	T _i = 25° C	I _F = 700 mA (SMA)			0.90	
V _F ⁽²⁾			I _F = 1 A			0.97	V
		T _j = 125° C	I _F = 1 A		0.68	0.78	

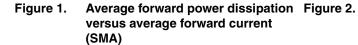
1. Pulse test: $t_p = 5 \text{ ms}, \delta < 2\%$

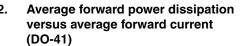
2. Pulse test: $t_p = 380 \ \mu s, \ \delta < 2\%$

To evaluate the conduction losses use the following equation: P = 0.65 x $I_{F(AV)}$ + 0.130 $I_{F}{}^{2}{}_{(RMS)}$

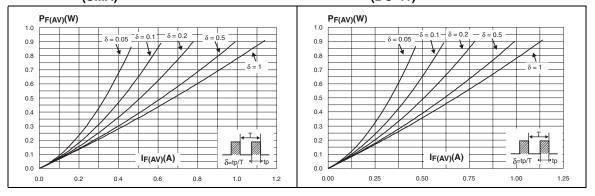
Table 4. Dynamic electrical characteristics

Symbol	Parameter	Tests conditions			Тур	Max	Unit
t _{rr}	Reverse recovery time	$T_j = 25^\circ C$	I _F = 0.5 A I _{rr} = 0.25 A I _R = 1 A		12	20	ns
t _{fr}	Forward recovery time	$T_j = 25^\circ C$	$I_F = 1 A dI_F/dt = 50 A/ms$ $V_{FR} = 1.1 x V_Fmax$		50		ns
V _{FP}	Forward recovery voltage	$T_j = 25^\circ C$	$I_F = 1 \text{ A} \text{ dI}_F/\text{dt} = 50 \text{ A/ms}$		1.8		V





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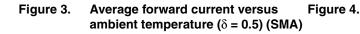


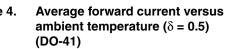
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175

 $\delta = tp/T$

1.E+03





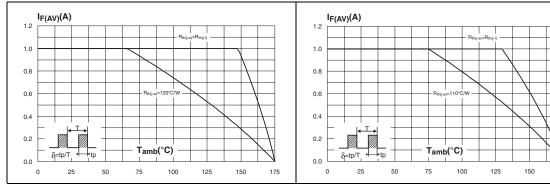


Figure 5. Relative variation of thermal impedance junction to ambient versus pulse duration (epoxy printed circuit board, e_(Cu) = 35 μm, recommended pad layout) (SMA)

Zth(j-c)/Rth(j-c)

1.0

0.9

0.8

0.7

0.6

0.5

0.4

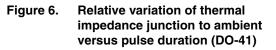
0.3

0.2

0.1

0.0

1.E-01



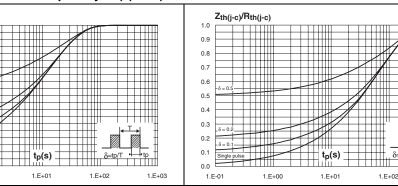
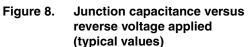
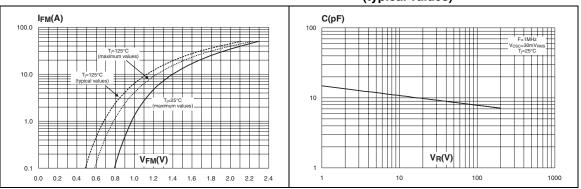


Figure 7. Forward voltage drop versus forward current

1.E+00





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Figure 9.Reverse recovery time versus dl_F/dtFigure 10.Peak recovery current versus dl_F/dt(90% confidence)(90% confidence)

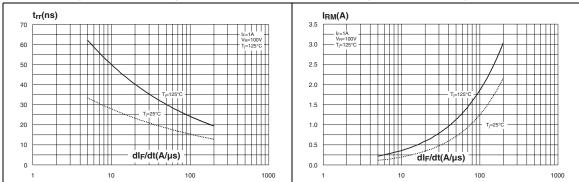
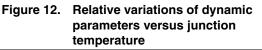
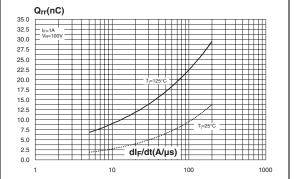
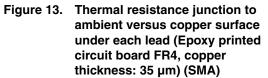
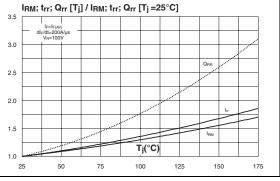


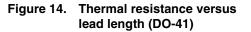
Figure 11. Reverse recovery charges versus dI_F/dt (90% confidence)

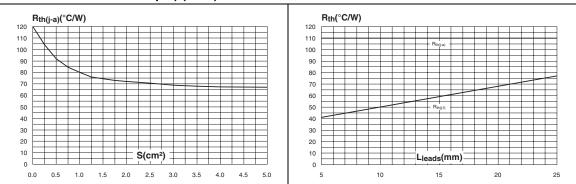












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2 Package information

• Epoxy meets UL94 V0

Table 5. SMA Dimensions

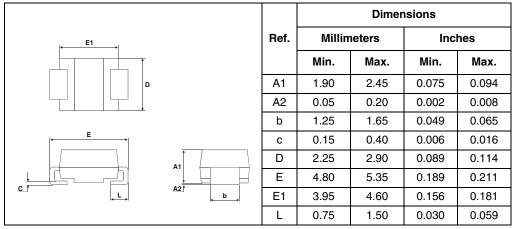


Figure 15. Footprint (dimensions in mm)

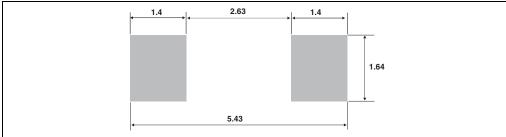
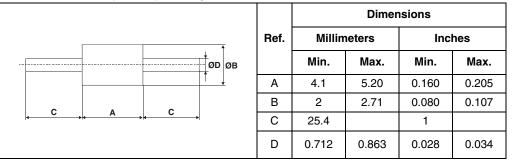


Table 6. DO-41 (Plastic) Package dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.



3 Ordering information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STTH102A	U12	SMA	0.068 g	5000	Tape and reel
STTH102	STTH102	DO-41	0.34 g	2000	Ammopack
STTH102RL	STTH102	DO-41	0.34 g	5000	Tape and reel

4 Revision history

Date	Revision	Description of Changes	
Jul-2003	2A	Last update.	
Aug-2004	3	SMA package dimensions update. Reference A1 max. changed from 2.70mm (0.106inc.) to 2.03mm (0.080). SMA and DO-41 datasheets merged	
27-Jun-2005	4	Corrected error in title.	
21-Nov-2006	5	Reformatted to current standards. Added Table 4. Dynamic electrical characteristics. Updated dimensions table for DO-41 Plastic package. Added cathode bands to package illustrations.	



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