



### SBR1U40LP

# 1.0A SBR<sup>®</sup> SUPER BARRIER RECTIFIER

#### **Features**

- Ultra Low Forward Voltage Drop
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- Lead Free By Design, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)

#### **Mechanical Data**

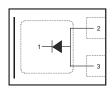
- Case: DFN1411-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper Lead Frame. Solderable per MIL-STD-202, Method 208
- Weight: 2.35mg (approximate)







**Bottom View** 



Top View Internal Schematic

### Ordering Information (Note 2)

Part Number	Case	Packaging
SBR1U40LP-7	DFN1411-3	3000/Tape & Reel

Notes:

- 1. No purposefully added lead.
- 2. For packaging details, go to our website at http://www.diodes.com.

### **Marking Information**



D4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: U = 2007) M = Month (ex: 9 = September)

Date Code Key

Year	2007	20	08	2009	2010	20	11	2012	2013	20	14	2015
Code	<u>U</u>	\	/	W	X	,	Y	Z	Α	I	3	С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## Maximum Ratings @TA = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage (Note 3) Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	40	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Rectified Output Current (See Figure 1)	l <sub>0</sub>	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave	I <sub>FSM</sub>	5	А
Non-Repetitive Peak Forward Surge Current 15s DC	I <sub>FSM</sub>	2.6	A

#### **Thermal Characteristics**

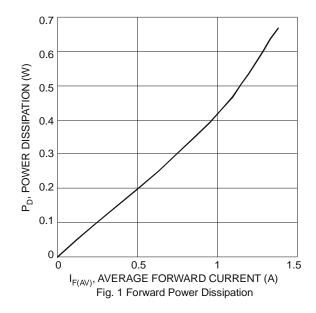
Characteristic	Symbol	Value	Unit
Power Dissipation	$P_{D}$	400	mW
Maximum Thermal Resistance Thermal Resistance Junction to Ambient (Note 4)	$R_{ hetaJA}$	190	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

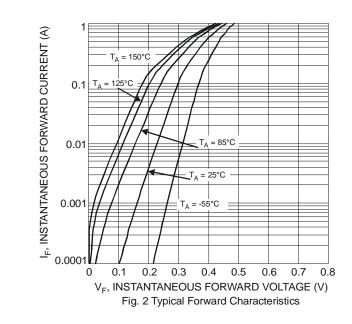
## Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	V <sub>F</sub>		0.39 0.46 0.34 0.43	0.42 0.49 0.37 0.47	V	I <sub>F</sub> = 0.5A, T <sub>J</sub> = 25°C I <sub>F</sub> = 1.0A, T <sub>J</sub> = 25°C I <sub>F</sub> = 0.5A, T <sub>J</sub> = 125°C I <sub>F</sub> = 1.0A, T <sub>J</sub> = 125°C
Leakage Current (Note 5)	I <sub>R</sub>	-	- -	50 100		$V_R = 40V, T_J = 25^{\circ}C$ $V_R = 40V, T_J = 125^{\circ}C$

Notes:

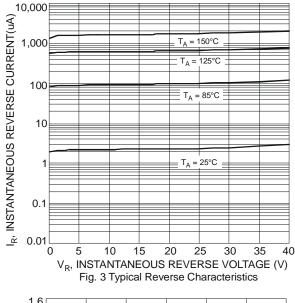
- 3. V<sub>RRM</sub> characteristic is base on 1mA leakage current test condition
- 4. Device mounted on Polymide substrate 1" x 1", 2oz. Copper double sided PCB board.
- 5. Short duration pulse test used to minimize self-heating effect.

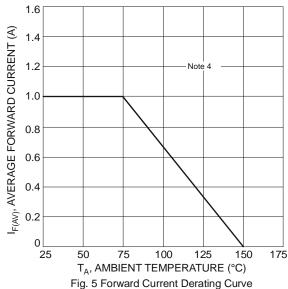




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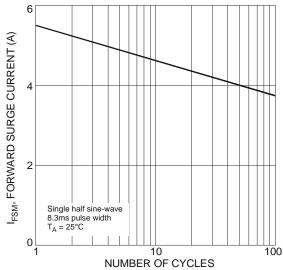


Fig. 7 Forward Surge Current vs. Number of Cycles

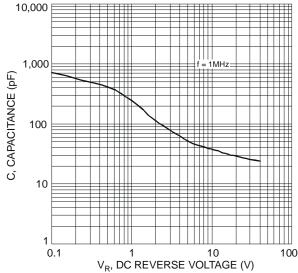
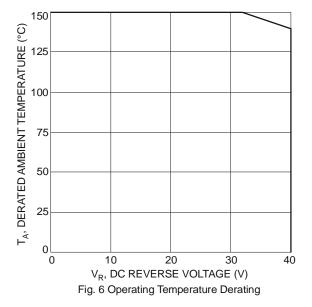


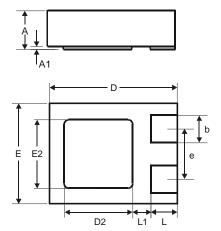
Fig. 4 Total Capacitance vs. Reverse Voltage



3 of 5

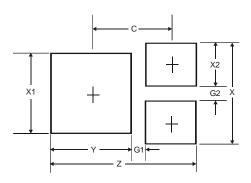


# Package Outline Dimensions



Dimensions	Value (in mm)
Z	1.38
G1	0.15
G2	0.15
Х	0.95
X1	0.75
X2	0.40
Y	0.75
С	0.76

## **Suggested Pad Layout**



DFN1411-3								
Dim	Min	Max	Тур					
Α	0.47	0.53	0.50					
A1	0	0.05	0.02					
b	0.25	0.35	0.30					
D	1.35	1.475	1.40					
D2	0.65	0.85	0.75					
Е	1.05	1.175	1.10					
E2	0.65	0.85	0.75					
е	_	_	0.55					
L	0.225	0.325	0.275					
L1	_		0.20					
All Dimensions in mm								



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