

# **1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER**

PowerDI<sup>®</sup>123

**DFLS130** 

### Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- Lead Free Finish, RoHS Compliant (Note 5)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

- Case: PowerDl<sup>®</sup>123
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.01 grams (approximate)



Top View

## **Maximum Ratings** $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	V
Average Forward Current	I <sub>F(AV)</sub>	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load	I <sub>FSM</sub>	35	А

## **Thermal Characteristics**

Characteristic	Symbol	Тур	Max	Unit
Power Dissipation (Note 1)	PD	—	1.67	W
Power Dissipation (Note 2)	PD	—	556	mW
Thermal Resistance Junction to Ambient (Note 1)	R <sub>θ</sub> JA	—	60	°C/W
Thermal Resistance Junction to Ambient (Note 2)	$R_{\theta JA}$	—	180	°C/W
Thermal Resistance Junction to Soldering (Note 3)	R <sub>0JS</sub>	—	10	°C/W

### Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	V <sub>(BR)R</sub>	30			V	I <sub>R</sub> = 1.5mA
			0.25	_		$I_{F} = 0.1A$
Forward Voltage (Note 4)	VF	_	0.33	0.37	V	$I_{F} = 0.7A$
		—	0.36	0.42		$I_{F} = 1.0A$
Leakage Current (Note 4)	I <sub>R</sub>	_	0.15	1.0	mA	V <sub>R</sub> = 30V, T <sub>A</sub> = 25°C
Total Capacitance	CT		40		pF	$V_R = 10V, f = 1.0MHz$

Notes: 1. Part mounted on 50.8mm X 50.8mm GETEK board with 25.4mm X 25.4mm copper pad, 25% anode, 75% cathode.

2. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads.

3. Theoretical R<sub>BJS</sub> calculated from the top center of the die straight down to the PCB cathode tab solder junction.

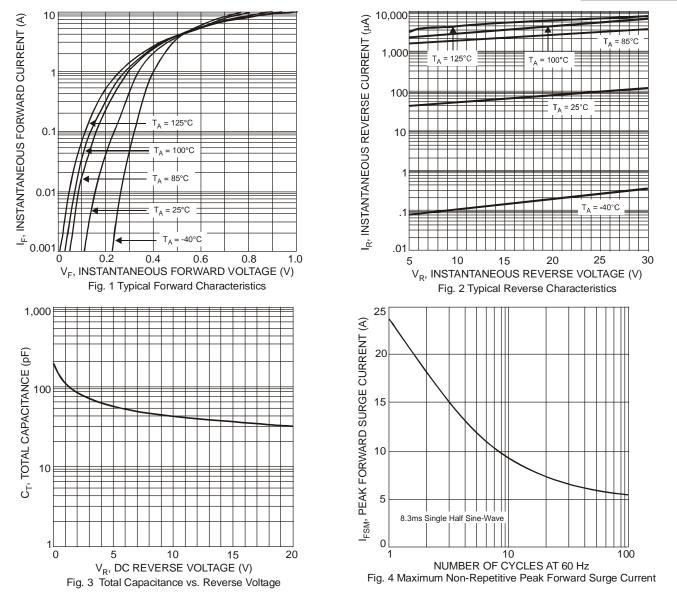
4. Short duration pulse test used to minimize self-heating effect.

5. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.

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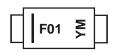


# Ordering Information (Note 6)

Part Number	Case	Packaging
DFLS130-7	PowerDI <sup>®</sup> 123	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**



F01 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

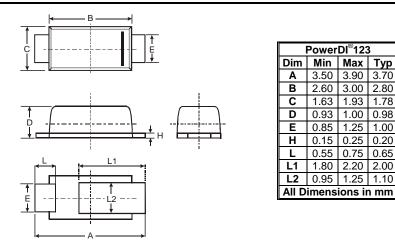
Date Code Key 2008 2010 2006 2007 2009 2011 2012 Year Code Т U V W Х Υ Ζ Feb Mar Apr May Jul Sep Oct Nov Dec Month Jan Jun Aug Code D 1 2 3 4 5 6 7 8 9 0 Ν

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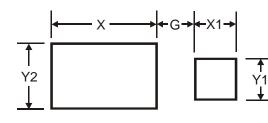


**DFLS130** 

#### **Package Outline Dimensions**



## **Suggested Pad Layout**



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

3.00 2.80

1.00 0.98

1.25 1.00

0.25 0.20

1.78

1.93

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