

1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

PowerDI<sup>®</sup>323

PD3S140

#### Features

- Guard Ring Die Construction for Transient Protection
- High Surge Capability
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability
- Ultra-Small Surface Mount Package

### **Mechanical Data**

- Case: PowerDl<sup>®</sup>323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 🚳
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.006 grams (approximate)





Bottom View

#### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	40	V
Average Forward Current (See also figure 4)	I <sub>F(AV)</sub>	1.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	22	A

## Thermal Characteristics

Notes:

	-			
Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point	$R_{\theta JS}$	—	15	°C/W
Thermal Resistance Junction to Ambient Air (Note 2)	R <sub>θJA</sub>	175	_	°C/W
Thermal Resistance Junction to Ambient Air (Note 3)	$R_{ heta JA}$	130	_	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-65 to	+150	°C

# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	V <sub>(BR)R</sub>	40	_		V	I <sub>R</sub> = 100μA
		_	0.37	0.42	v	I <sub>F</sub> = 0.1A
Forward Valtage	V	_	0.44	0.50		$I_{F} = 0.5A$
Forward Voltage	V <sub>F</sub>	—	0.46	0.52		$I_{F} = 0.7A$
		_	0.49	0.55		I <sub>F</sub> = 1.0A
Leakage Current (Note 4)			0.3	4	A	V <sub>R</sub> = 5V, T <sub>A</sub> = 25°C
Leakage Guilelli (NOLE 4)	IR	—	2	50	μA	V <sub>R</sub> = 40V, T <sub>A</sub> = 25°C
Total Capacitance (See also figure 3)	CT	_	32		pF	$V_{R} = 10V, f = 1.0MHz$

1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/quality/lead\_free.html.

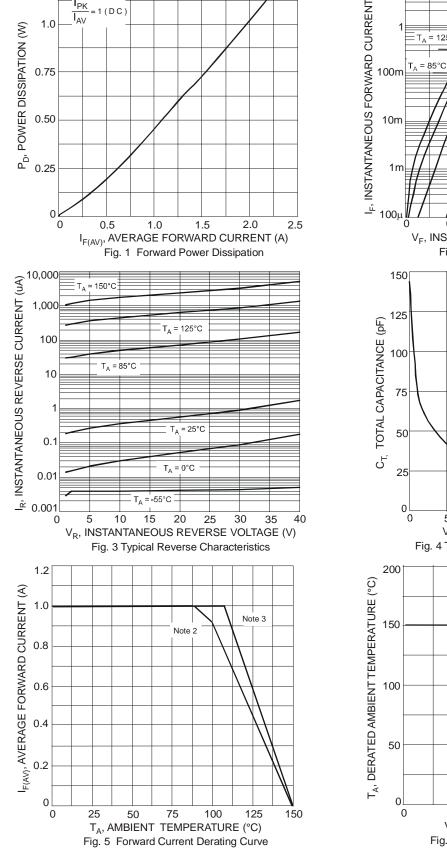
2. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf. T<sub>A</sub> = 25°C.

3. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.  $T_A = 25^{\circ}C$ .

4. Short duration pulse test used to minimize self-heating effect.

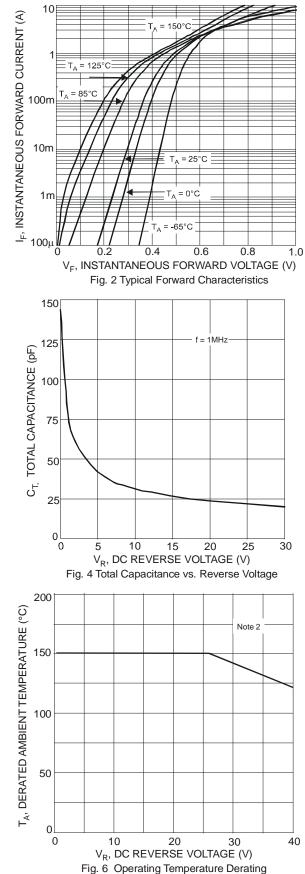


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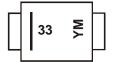


### Ordering Information (Note 5)

Part Number	Case	Packaging
PD3S140-7	PowerDl <sup>®</sup> 323	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# Marking Information

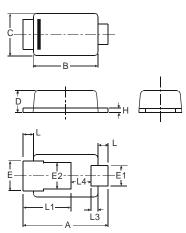


33 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: U = 2006) M = Month (ex: 9 = September)

Date Code Key

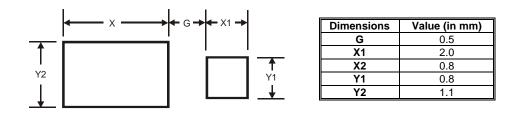
Year	200	6	2007		2008	20	09	2010		2011	2	2012
Code	Т		U		V	V	V	Х		Y		Z
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

# **Package Outline Dimensions**



PowerDl <sup>®</sup> 323							
Dim	Min	Max	Тур				
Α	2.40	2.60	2.50				
В	1.85	1.95	1.90				
С	1.20	1.30	1.25				
D	0.60	0.70	0.65				
Е	0.78	0.98	0.88				
E1	0.50	0.70	0.60				
E2	0.60	1.00	0.80				
Н	0.08	0.18	0.13				
L	0.20	0.40	0.30				
L1			1.40				
L3	_	_	0.20				
L4	0.40	0.80	0.60				
All Dimensions in mm							

# Suggested Pad Layout



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