

SBR2A40P1

2.0A SBR[®] SURFACE MOUNT SUPER BARRIER RECTIFIER *PowerDI*[®]123

Features

- Low Forward Voltage Drop
- Low Leakage Current
- Superior Reverse Avalanche Capability
- Excellent High Temperature Stability
- Patented Interlocking Clip Design for High Surge Current
 Capacity
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- ±16KV ESD Protection (HBM, 3B)
- ±25KV ESD Protection (IEC61000-4-2 Level 4, Air Discharge)
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q 101 Standards for High Reliability

Mechanical Data

- Case: PowerDI[®]123
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity Indicator: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.018 grams (approximate)



Top View

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{RM}	40	V
RMS Reverse Voltage	V _{R(RMS)}	28	V
Average Rectified Output Current (See Figure 1)	lo	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	50	A
Repetitive Peak Avalanche Power (1µs, 25°C)	PARM	6,000	W

Thermal Characteristics

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Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Thermal Resistance Junction to Soldering (Note 2) Thermal Resistance Junction to Ambient (Note 3) Thermal Resistance Junction to Ambient (Note 4)	R _{eJS} R _{eJA} R _{eJA}	5 180 115	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.

2. Theoretical $R_{0,S}$ calculated from the top center of the die straight down to the PCB cathode tab solder junction.

3. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.

4. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.

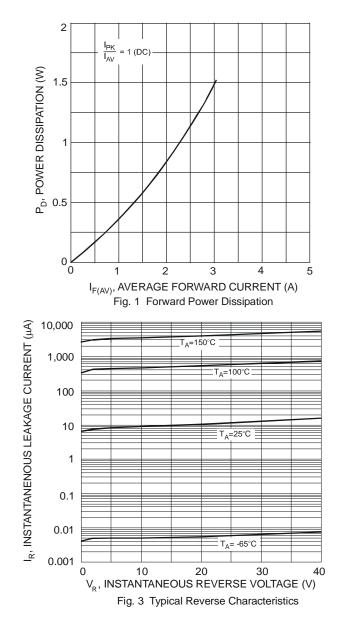


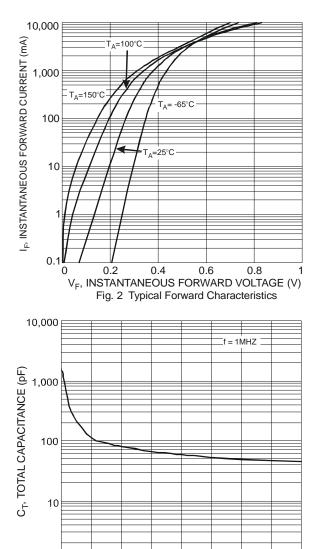
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
Reverse Breakdown Voltage (Note 5)	V _{(BR)R}	40	-	-	V	I _R = 100μA	
Forward Voltage Drop	VF	-	0.265	0.315		$I_F = 0.1A, T_J = 25^{\circ}C$	
		-	0.38	0.43	V	I _F = 1.0A, T _J = 25°C	
		-	0.45	0.50		I _F = 2.0A, T _J = 25°C	
		-	0.17	0.22		I _F = 0.1A, T _J = 125°C	
		-	0.325	0.375		I _F = 1.0A, T _J = 125°C	
		-	0.42	0.47		I _F = 2.0A, T _J = 125°C	
Leakage Current (Note 5)	I _R	-	8	40	μA	V _R = 5V, T _J = 25°C	
		-	16	100	μA	V _R = 40V, T _J = 25°C	
		-	1.3	8	mA	V _R = 5V, T _J = 125°C	
		-	2.1	10	mA	V _R = 40V, T _J = 125°C	

Notes:

5. Short duration pulse test used to minimize self-heating effect.





20 V_R, DC REVERSE VOLTAGE (V) Fig. 4 Total Capacitance vs. Reverse Voltage

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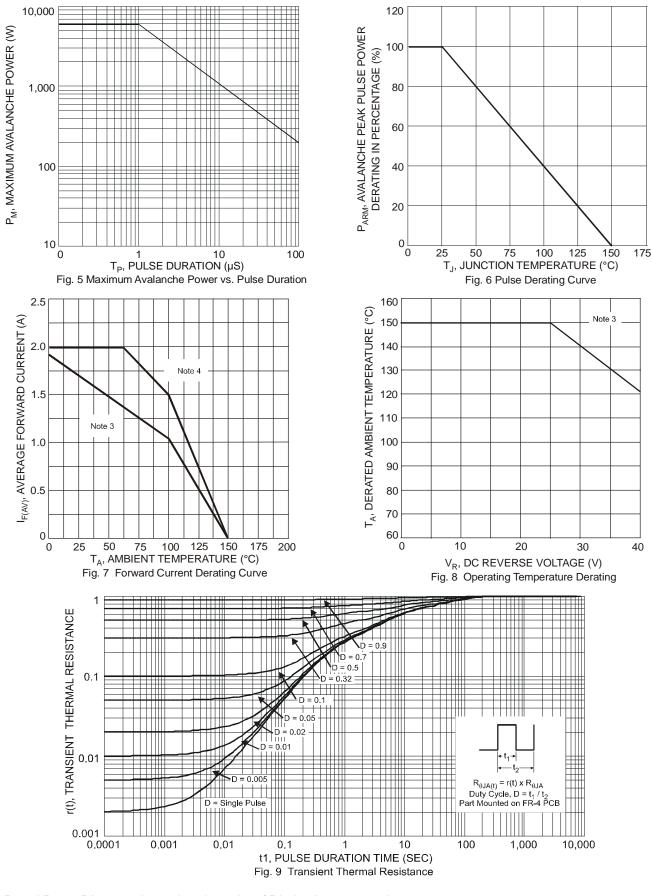
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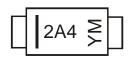


Ordering Information (Note 6)

Part Number	Case	Packaging		
SBR2A40P1-7	PowerDI [®] 123	3000/Tape & Reel		

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

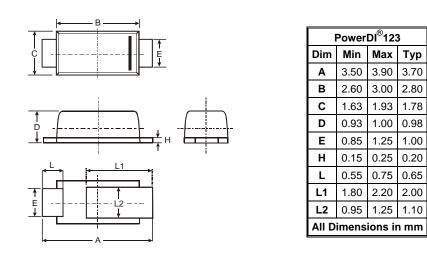


2A4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

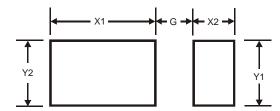
Date Code Key

Year	2006	2007	200	8	2009	2010	2011	2012	20	13	2014	2015
Code	Т	U	V		W	Х	Y	Z		۹.	В	С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

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