

3.3V HCMOS/ TTL COMPATIBLE SMD CRYSTAL CLOCK OSCILLATOR

ASFL1



- FEATURES:**
- Tri state function
 - Suitable for high density SMT
 - Available tight stability option +/-20ppm
 - Seam sealed package assures high reliability performance
 - Suitable for RoHS compliant reflow process

- APPLICATIONS:**
- CCD clock for VTR camera
 - Equipment connected to PC or PC cards
 - Thin equipment
 - Wireless communication
 - PDA, Laptop computer

STANDARD SPECIFICATIONS:

Parameters	Minimum	Typical	Maximum	Units	Notes
Frequency Range	0.321	-----	133.33	MHz	
Operating Temperature	-10	-----	+70	°C	See options
Storage Temperature	-55	-----	+125	°C	
Overall Frequency Stability	-100	-----	+100	ppm	See options
Supply Voltage (V _{dd})	2.97	3.3	3.63	V	
Input Current	0.321MHz - 29.9MHz	-----	8	15	mA
	30MHz - 79.9MHz	-----	20	45	
	80MHz - 133.33MHz	-----	28	85	
Symmetry (t ₁ /2V _{dd})	40	50	60	%	See options
Rise and Fall Time (T _r /T _f)	0.321MHz - 29.9MHz	-----	5	10	ns
	30MHz - 79.9MHz	-----	5	7	
	80MHz - 133.33MHz	-----	3	7	
Output Load	-----	-----	15	pf	
Output Voltage	V _{OH}	0.9V _{dd}	-----	V	
	V _{OL}	-----	0.1V _{dd}	V	
Tri-state Function	V _{IH}	0.7V _{dd}	-----	V	*1" on Output Oscillation *0" Output disable (DLZ)
	V _{IL}	-----	0.3V _{dd}	V	
Disable Current	-----	-----	10	µA	
Start-up Time	0.321MHz - 29.9MHz	-----	1	10	ms
	30MHz - 79.9MHz	-----	3	10	
	80MHz - 133.33MHz	-----	3	10	
Phase Jitter RMS (12-20MHz)	-----	-----	1	ps	Reference only. Please contact Abrakon for specific frequencies
Aging	-5.0	-----	+5.0	ppm	@+25°C, First year

OPTIONS AND PART IDENTIFICATION:



