MMBD6100LT1G

Monolithic Dual Switching Diode

Features

• These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	70	Vdc
Forward Current	١ _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-5 Board (Note 1)	PD		
$T_A = 25^{\circ}C$ Derate above 25°C		225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 2)	PD		
`T _A = 25°C Derate above 25°C		300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.

2. Alumina = 0.4 \times 0.3 \times 0.024 in. 99.5% alumina.

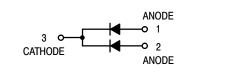
ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Reverse Breakdown Voltage (I _(BR) = 100 μAdc)	V _(BR)	70	-	Vdc	
Reverse Voltage Leakage Current $(V_R = 50 \text{ Vdc})$ (For each individual diode while the second diode is unbiased)	I _R	_	0.1	μAdc	
Forward Voltage (I _F = 1.0 mAdc) (I _F = 100 mAdc)	V _F	0.55 0.8	0.7 1.1	Vdc	
Reverse Recovery Time ($I_F = I_R = 10$ mAdc, $I_{R(REC)} = 1.0$ mAdc) (Figure 1)	t _{rr}	-	4.0	ns	
Capacitance (V _R = 0 V)	С	-	2.5	pF	



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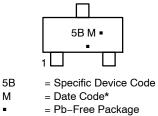
http://onsemi.com





CASE 318 STYLE 9

MARKING DIAGRAM



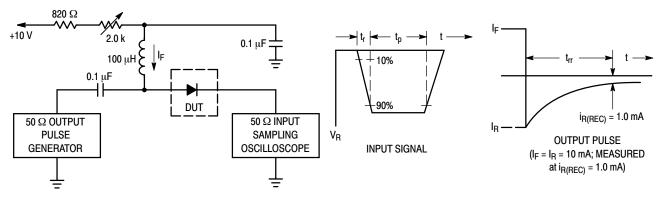
(Note: Microdot may be in either location) *Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBD6100LT1G	SOT-23 (Pb-Free)	3000/Tape & Reel
MMBD6100LT3G	SOT-23 (Pb-Free)	10,000/Tape & Reel

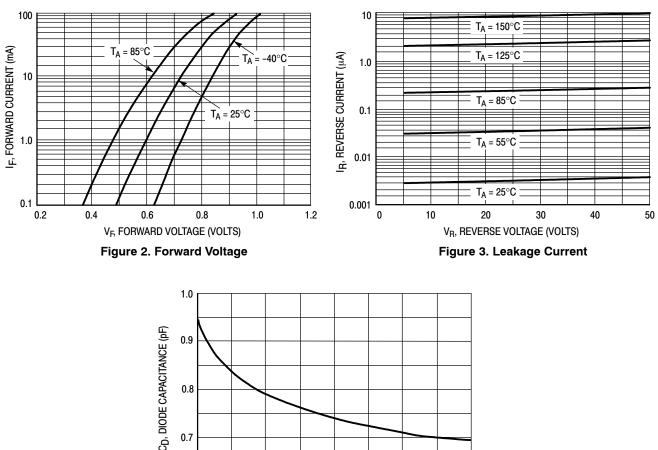
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMBD6100LT1G



Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA. 2. Input pulse is adjusted so $I_{R(\text{peak})}$ is equal to 10 mA. 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

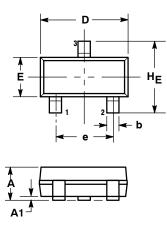


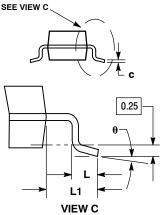
CURVES APPLICABLE TO EACH CATHODE

0.7 0.6 6 8 2 0 4 V_R, REVERSE VOLTAGE (VOLTS) Figure 4. Capacitance

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AN**





NOTES:

DIMENSIONING AND TOLERANCING PER 1. ANSI Y14.5M, 1982

CONTROLLING DIMENSION: INCH. MAXIMUM LEAD THICKNESS INCLUDES З.

LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF

BASE MATERIAL. 4. 318-01 THRU -07 AND -09 OBSOLETE, 4

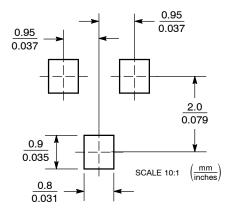
NEW STANDARD 318-08.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104

STYLE 9:

з CATHODE

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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