advanced

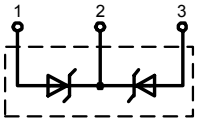
Schottky

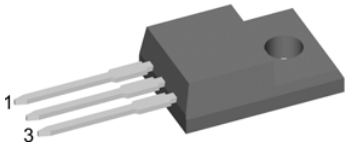
High Performance Schottky Diode
Low Loss and Soft Recovery
Common Cathode

V_{RRM} = 150 V
I_{FAV} = 2x 10 A
V_F = 0.74 V

Part number *(Marking on product)*

DSA 20 C 150PN





Features / Advantages:

- Very low Vf
- Extremely low switching losses
- Low Irm-values
- Improved thermal behaviour
- High reliability circuit operation
- Low voltage peaks for reduced protection circuits
- Low noise switching
- Low losses

Applications:

- Rectifiers in switch mode power supplies (SMPS)
- Free wheeling diode in low voltage converters

Package:

TO-220FPAB

- Industry standard outline
- Plastic overmolded tab for electrical isolation
- Epoxy meets UL 94V-0
- RoHS compliant

Symbol	Definition	Conditions		Ratings min.	typ.	max.	Unit
V_{RRM}	*max. repetitive reverse voltage*		T_{VJ} = 25 °C			150	V
I_R	*reverse current*	V_R = 150 V	T_{VJ} = 25 °C			0.3	mA
		V_R = 150 V	T_{VJ} = 125 °C			3	mA
V_F	*forward voltage*	I_F = 10 A	T_{VJ} = 25 °C			0.88	V
		I_F = 20 A				0.99	V
		I_F = 10 A	T_{VJ} = 125 °C			0.74	V
		I_F = 20 A				0.86	V
I_{FAV}	*average forward current*	rectangular, d = 0.5	T_C = 140 °C			10	A
V_{F0}	*threshold voltage* } *for power loss calculation only*		T_{VJ} = 175 °C			0.55	V
r_F	*slope resistance*					11.5	mΩ
R_{thJC}	*thermal resistance junction to case*					4.50	K/W
T_{VJ}	*virtual junction temperature*			-55		175	°C
P_{tot}	*total power dissipation*		T_C = 25 °C			35	W
I_{FSM}	*max. forward surge current*	t_p = 10 ms (50 Hz), sine	T_{VJ} = 45 °C			60	A
C_J	*junction capacitance*	V_R = V; f = 1 MHz	T_{VJ} = 25 °C				pF
E_{AS}	*non-repetitive avalanche energy*	I_{AS} = A; L = 100 µH	T_{VJ} = 25 °C			tbd	mJ
I_{AR}	*repetitive avalanche current*	$V_A = 1.5 \cdot V_R$ typ.; f = 10 kHz				tbd	A

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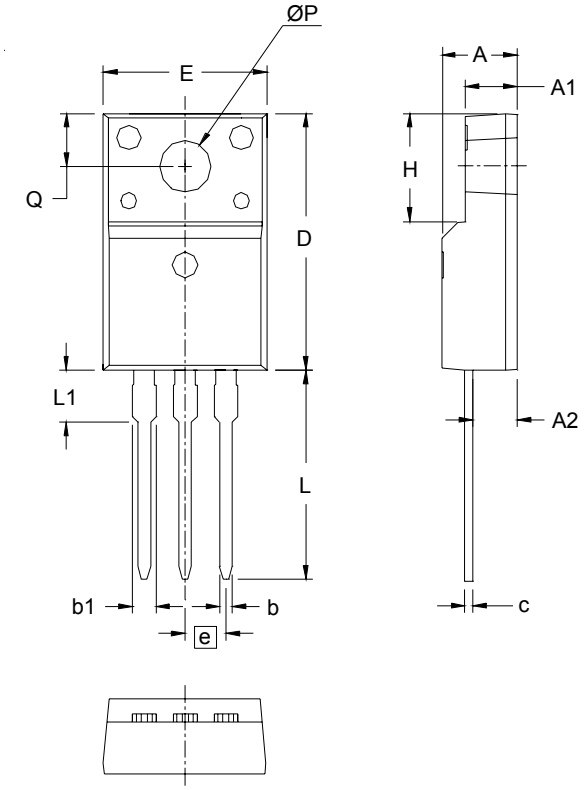
Symbol	Definition	Conditions	Ratings min.	typ.	max.	Unit
I_{RMS}	*RMS current*	per pin*			35	A
R_{thCH}	*thermal resistance case to heatsink*			0.50		K/W
M_D	*mounting torque*		0.4		0.6	Nm
F_C	*mounting force with clip*		20		60	N
T_{stg}	*storage temperature*		-55		150	°C
Weight				2		g

* Irms is typically limited by: 1. pin-to-chip resistance; or by 2. current capability of the chip.
In case of 1, a common cathode/anode configuration and a non-isolated backside, the whole current capability can be used by connecting the backside.

Outlines TO-220FPAB



SYM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	.177	.193	4.50	4.90
A1	.092	.108	2.34	2.74
A2	.101	.117	2.56	2.96
b	.028	.035	0.70	0.90
b1	.050	.058	1.27	1.47
c	.018	.024	0.45	0.60
D	.617	.633	15.67	16.07
E	.392	.408	9.96	10.36
e	.100 BSC		2.54 BSC	
H	.255	.271	6.48	6.88
L	.499	.523	12.68	13.28
L1	.119	.135	3.03	3.43
ØP	.121	.129	3.08	3.28
Q	.126	.134	3.20	3.40