## Features

- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- Lead Free Finish, RoHS Compliant (Note 2)
- "Green" Molding Compound (No Br, Sb)


Top View

## Mechanical Data

- Case: DPAK (TO-252)
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 ③:
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.34 grams (approximate)


Polarity

Maximum Ratings $@ T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified
Single phase, half wave, 60 Hz , resistive or inductive load.
For capacitance load, derate current by $20 \%$.

| Characteristic | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | VRRM <br> VRWM <br> VRM | 45 | V |
| RMS Reverse Voltage | $\mathrm{V}_{\mathrm{R} \text { (RMS) }}$ | 31 | V |
| Average Rectified Output Current @ $\mathrm{T}_{\mathrm{C}}=110^{\circ} \mathrm{C}$ | Io | 10 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | IfSM | 90 | A |

## Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Maximum Thermal Resistance (per leg) (Note 3) <br> Package $=$ TO-252 | $R_{\theta J A}$ | 47 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{J},} \mathrm{T}_{\text {STG }}$ | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

## Electrical Characteristics $@ T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Breakdown Voltage (Note 1) | $\mathrm{V}_{(B R) R}$ | 45 | - | - | V | $\mathrm{I}_{\mathrm{R}}=0.5 \mathrm{~mA}$ |
| Forward Voltage Drop (Per Leg) | $\mathrm{V}_{\mathrm{F}}$ | - | - | 0.55 | V | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~A}, \mathrm{~T}_{J}=25^{\circ} \mathrm{C}$ |
|  |  | - | 0.5 | 0.53 |  | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~A}, \mathrm{~T}_{J}=85^{\circ} \mathrm{C}$ |
| Leakage Current (Note 1) | $\mathrm{I}_{\mathrm{R}}$ | - | - | 0.5 | mA | $\mathrm{~V}_{\mathrm{R}}=45 \mathrm{~V}, \mathrm{~T}_{J}=25^{\circ} \mathrm{C}$ |
|  |  |  |  |  |  |  |

Notes: 1. Short duration pulse test used to minimize self-heating effect.
2. RoHS revision 13.2.2003. High temperature solder exemption applied, see EU Directive Annex Note 7.
3. Device mounted on polymide substrate 2 " x 2", 2oz. Copper, $1 \times$ MRP double-sided, PC boards.


Fig. 1 Forward Power Dissipation


Fig. 3 Typical Reverse Characteristics


Fig. 5 Forward Current Derating Curve
SBR1045CTL


Fig. 2 Typical Forward Characteristics


Fig. 4 Total Capacitance vs. Reverse Voltage


Fig. 6 Operating Temperature Derating

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Ordering Information (Note 3)

| Part Number | Case | Packaging |
| :---: | :---: | :---: |
| SBR1045CTL-13 | DPAK (TO-252) | 80 pieces/tube |

Notes: 3. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## Marking Information



SBR1045CT = Product Type Marking Code
$A B=$ Foundry and Assembly Code
YYWW = Date Code Marking
$\mathrm{YY}=$ Last two digits of year, ex: $07=2007$
WW = Week (01-52)

## Package Outline Dimensions



## Suggested Pad Layout



| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{Z}$ | 11.6 |
| $\mathbf{X 1}$ | 1.5 |
| $\mathbf{X 2}$ | 7.0 |
| $\mathbf{Y 1}$ | 2.5 |
| $\mathbf{Y 2}$ | 7.0 |
| $\mathbf{C}$ | 6.9 |
| $\mathbf{E 1}$ | 2.3 |

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