

## STPS40120C

## Power Schottky rectifier

## **Main product characteristics**

I <sub>F(AV)</sub>	2 x 20 A
V <sub>RRM</sub>	120 V
T <sub>j(max)</sub>	175° C
$V_{F(typ)}$	0.57 V

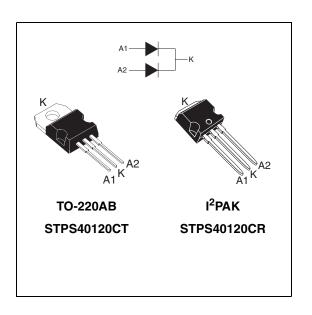
### Feature and benefits

- High junction temperature capability
- Avalanche rated
- Low leakage current
- Good trade-off between leakage current and forward voltage drop

## **Description**

Dual center tap Schottky rectifier suited for high frequency Switch Mode Power Supply.

Packaged in TO-220AB and I<sup>2</sup>PAK, this device is intended to be used in notebook and LCD adaptors, desktop SMPS, providing in these applications a margin between the remaining voltages applied on the diode and the voltage capability of the diode.



### Order code

Part Number	Marking
STPS40120CT	STPS40120CT
STPS40120CR	STPS40120CR

Table 1. Absolute ratings (limiting values, per diode)

Symbol	Parameter			Value	Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage			120	V
I <sub>F(RMS)</sub>	RMS forward voltage			30	Α
I <sub>F(AV)</sub>			Per diode Per device	20 40	Α
I <sub>FSM</sub>	Surge non repetitive forward current $t_p = 10 \text{ ms Sinusoidal}$			200	Α
P <sub>ARM</sub>	Repetitive peak avalanche power $t_p = 1 \mu s T_j = 25^{\circ} C$			10500	W
T <sub>stg</sub>	Storage temperature range			-65 to + 175	° C
T <sub>j</sub>	Maximum operating junction temperature <sup>(1)</sup>			175	° C

<sup>1.</sup>  $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$  condition to avoid runaway for a diode on its own heatsink

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Table 2. Thermal parameters

Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	Junction to case	Per diode Total	1.6 0.85	° C/W
R <sub>th(c)</sub>	Coupling	Total	0.1	° C/W

When the diodes 1 and 2 are used simultaneously:

 $\Delta T_i$ (diode 1) = P(diode 1) x R<sub>th(i-c)</sub>(per diode) + P(diode 2) x R<sub>th(c)</sub>

Table 3. Static electrical characteristics (per diode)

Symbol	Test conditions			Min.	Тур.	Max.	Unit
ı (1)	I <sub>R</sub> <sup>(1)</sup> Reverse leakage current	T <sub>j</sub> = 25° C	V - V			25	μΑ
'R` ′		T <sub>j</sub> = 125° C	$V_R = V_{RRM}$		4	12	mA
	V (2)	T <sub>j</sub> = 25° C	I <sub>F</sub> = 7.5 A			0.73	
		T <sub>j</sub> = 125° C			0.57	0.61	
V <sub>F</sub> <sup>(2)</sup>		T <sub>j</sub> = 25° C				0.9	V
V <sub>F</sub> <sup>(2)</sup> Forward voltage drop	T <sub>j</sub> = 125° C	I <sub>F</sub> = 20A		0.69	0.73	V	
		$T_j = 25^{\circ} \text{ C}$ $T_i = 125^{\circ} \text{ C}$	I <sub>F</sub> = 40 A			1	
		T <sub>j</sub> = 125° C			0.83	0.88	

<sup>1.</sup> Pulse test : tp = 5 ms,  $\delta$  < 2%

To evaluate the maximum conduction losses use the following equation : P = 0.58 x  $I_{F(AV)}$  + 0.0075  $I_{F}^{\,2}(\text{RMS})$ 

$$P = 0.58 \times I_{F(AV)} + 0.0075 I_{F}^{2}_{(BMS)}$$

<sup>2.</sup> Pulse test : tp = 380  $\mu$ s,  $\delta$  < 2%

STPS40120C Characteristics

Figure 1. Average forward power dissipation versus average forward current (per diode)

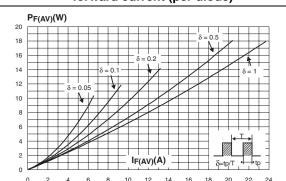


Figure 2. Average forward current versus ambient temperature  $(\delta = 0.5, \text{ per diode})$ 

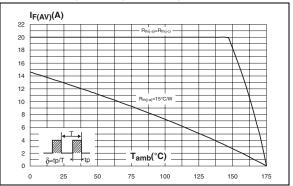


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

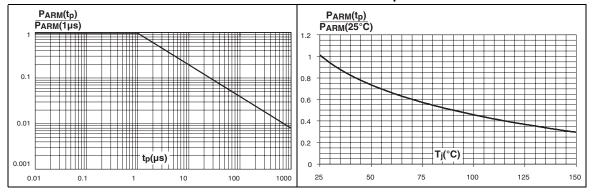
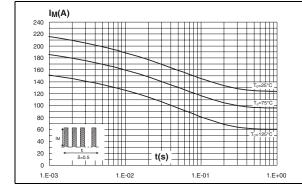
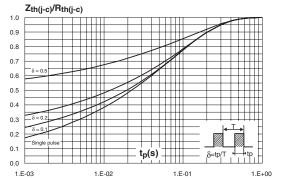


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

Figure 6. Relative variation of thermal impedance junction to ambient versus pulse duration

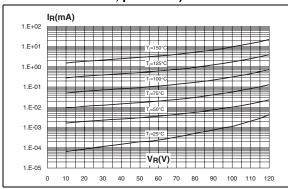




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Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)

Figure 8. Junction capacitance versus reverse voltage applied (typical values, per diode)



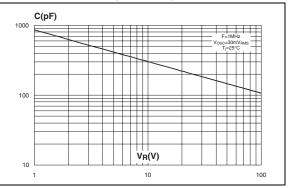
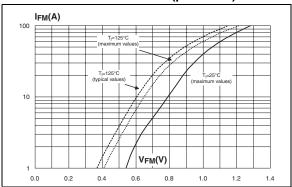


Figure 9. Forward voltage drop versus forward current (per diode)

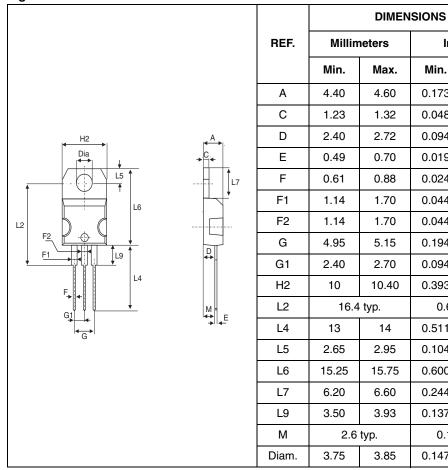


#### 2 **Package information**

Epoxy meets UL94, V0

Cooling method: by conduction (C) Recommended torque value: 0.8 Nm Maximum torque value: 1.0 Nm

Figure 10. TO-220AB dimensions



Inches

Max.

0.181

0.051

0.107

0.027

0.034

0.066

0.066

0.202

0.106

0.409

0.551

0.116

0.620

0.259

0.154

0.151

Min.

0.173

0.048

0.094

0.019

0.024

0.044

0.044

0.194

0.094

0.393

0.511

0.104

0.600

0.244

0.137

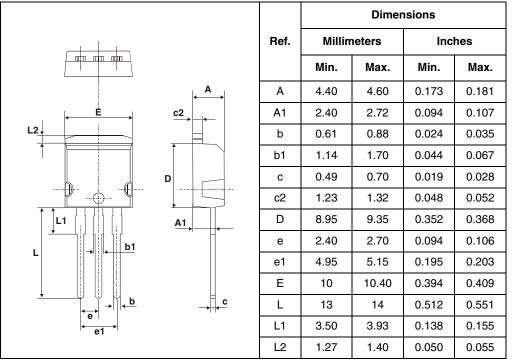
0.147

0.645 typ.

0.102 typ.

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Table 4. I<sup>2</sup>PAK dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

# 3 Ordering information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STPS40120CT	STPS40120CT	TO-220AB	2.23 g	50	Tube
STPS40120CR	STPS40120CR	I <sup>2</sup> PAK	1.49 g	50	Tube

# 4 Revision history

Date	Revision	Description of Changes
18-Feb-2005	1	First issue
1-Dec-2006	2	Reformatted to current standards. Added I <sup>2</sup> PAK.

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