

## CD4528BM/CD4528BC Dual Monostable Multivibrator

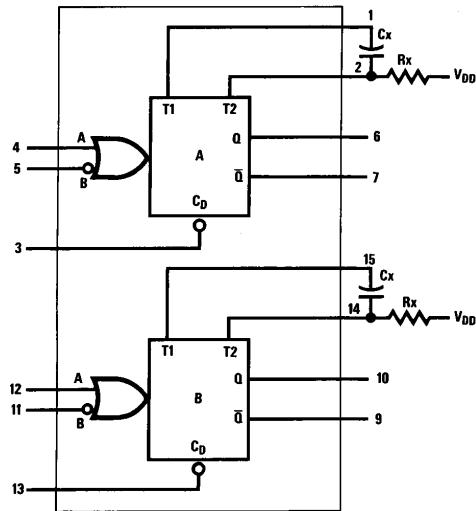
### General Description

The CD4528B is a dual monostable multivibrator. Each device is retriggerable and resettable. Triggering can occur from either the rising or falling edge of an input pulse, resulting in an output pulse over a wide range of widths. Pulse duration and accuracy are determined by external timing components Rx and Cx.

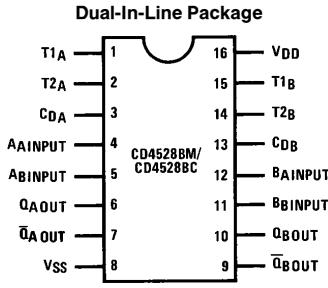
### Features

- Wide supply voltage range 3.0V to 18V
- Separate reset available
- Quiescent current = 5.0 nA/package (typ.) at 5.0 V<sub>DC</sub>
- Diode protection on all inputs
- Triggerable from leading or trailing edge pulse
- Capable of driving two low-power TTL loads or one low-power Schottky TTL load over the rated temperature range

### Connection Diagrams



TL/F/5998-1



TL/F/5998-2

Order Number CD4528B

### Truth Table

Inputs			Outputs	
Clear	A	B	Q	Q̄
L	X	X	L	H
X	H	X	L	H
X	X	L	L	H
H	L	↓	↑	↑
H	↑	H	↑	↑

H = High Level  
 L = Low Level  
 ↑ = Transition from Low to High  
 ↓ = Transition from High to Low  
 ↗ = One High Level Pulse  
 ↘ = One Low Level Pulse  
 X = Irrelevant

## Absolute Maximum Ratings (Notes 1 & 2)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

DC Supply Voltage ( $V_{DD}$ )	-0.5 V <sub>DC</sub> to +18 V <sub>DC</sub>
Input Voltage, All Inputs ( $V_{IN}$ )	-0.5 V <sub>DC</sub> to $V_{DD}$ + 0.5 V <sub>DC</sub>
Storage Temperature Range ( $T_S$ )	-65°C to +150°C
Power Dissipation ( $P_D$ )	
Dual-In-Line	700 mW
Small Outline	500 mW
Lead Temperature ( $T_L$ ) (Soldering, 10 seconds)	260°C

## Recommended Operating Conditions (Note 2)

DC Supply Voltage ( $V_{DD}$ )	3V to 15V
Input Voltage ( $V_{IN}$ )	0V to $V_{DD}$ V <sub>DC</sub>
Operating Temperature Range ( $T_A$ )	
CD4528BM	-55°C to +125°C
CD4528BC	-40°C to +85°C

## DC Electrical Characteristics CD4528BM (Note 2)

Symbol	Parameter	Conditions	-55°C		+ 25°C			+ 125°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
$I_{DD}$	Quiescent Device Current	$V_{DD} = 5V$		5	0.005		5		150	$\mu A$
		$V_{DD} = 10V$		10	0.010		10		300	$\mu A$
		$V_{DD} = 15V$		20	0.015		20		600	$\mu A$
$V_{OL}$	Low Level Output Voltage	$V_{DD} = 5V$		0.05			0.05		0.05	V
		$V_{DD} = 10V$		0.05			0.05		0.05	V
		$V_{DD} = 15V$		0.05			0.05		0.05	V
$V_{OH}$	High Level Output Voltage	$V_{DD} = 5V$	4.95		4.95	5.0		4.95		V
		$V_{DD} = 10V$	9.95		9.95	10.0		9.95		V
		$V_{DD} = 15V$	14.95		14.95	15.0		14.95		V
$V_{IL}$	Low Level Input Voltage	$V_{DD} = 5V, V_O = 0.5V$ or $4.5V$		1.5		2.25	1.5		1.5	V
		$V_{DD} = 10V, V_O = 1V$ or $9V$		3.0		4.50	3.0		3.0	V
		$V_{DD} = 15V, V_O = 1.5V$ or $13.5V$		4.0		6.75	4.0		4.0	V
$V_{IH}$	High Level Input Voltage	$V_{DD} = 5V, V_O = 0.5V$ or $4.5V$	3.5		3.5	2.75		3.5		V
		$V_{DD} = 10V, V_O = 1V$ or $9V$	7.0		7.0	5.50		7.0		V
		$V_{DD} = 15V, V_O = 1.5V$ or $13.5V$	11.0		11.0	8.25		11.0		V
$I_{OL}$	Low Level Output Current (Note 3)	$V_{DD} = 5V, V_O = 0.4V$	0.64		0.51	0.88		0.36		mA
		$V_{DD} = 10V, V_O = 0.5V$	1.6		1.3	2.25		0.9		mA
		$V_{DD} = 15V, V_O = 1.5V$	4.2		3.4	8.8		2.4		mA
$I_{OH}$	High Level Output Current (Note 3)	$V_{DD} = 5V, V_O = 4.6V$	-0.25		-0.2	-0.36		-0.14		mA
		$V_{DD} = 10V, V_O = 9.5V$	-0.62		-0.5	-0.9		-0.35		mA
		$V_{DD} = 15V, V_O = 13.5V$	-1.8		-1.5	-3.5		-1.1		mA
$I_{IN}$	Input Current	$V_{DD} = 15V, V_{IN} = 0V$		-0.1		-10 <sup>-5</sup>	-0.1		-1.0	$\mu A$
		$V_{DD} = 15V, V_{IN} = 15V$		0.1		10 <sup>-5</sup>	0.1		1.0	$\mu A$

**Note 1:** "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range", they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

**Note 2:**  $V_{SS} = 0V$  unless otherwise specified.

**Note 3:**  $I_{OH}$  and  $I_{OL}$  are tested one output at a time.

## DC Electrical Characteristics CD4528BC (Note 2)

Symbol	Parameter	Conditions	−40°C		+25°C			+85°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
I <sub>DD</sub>	Quiescent Device Current	V <sub>DD</sub> = 5V V <sub>DD</sub> = 10V V <sub>DD</sub> = 15V		20 40 80		0.005 0.010 0.015	20 40 80		150 300 600	μA μA μA
V <sub>OL</sub>	Low Level Output Voltage	V <sub>DD</sub> = 5V V <sub>DD</sub> = 10V V <sub>DD</sub> = 15V		0.05 0.05 0.05			0.05 0.05 0.05		0.05 0.05 0.05	V V V
V <sub>OH</sub>	High Level Output Voltage	V <sub>DD</sub> = 5V V <sub>DD</sub> = 10V V <sub>DD</sub> = 15V	4.95 9.95 14.95		4.95 9.95 14.95	5.0 10.0 15.0		4.95 9.95 14.95		V V V
V <sub>IL</sub>	Low Level Input Voltage	V <sub>DD</sub> = 5V, V <sub>O</sub> = 0.5V or 4.5V V <sub>DD</sub> = 10V, V <sub>O</sub> = 1V or 9V V <sub>DD</sub> = 15V, V <sub>O</sub> = 1.5V or 13.5V		1.5 3.0 4.0		2.25 4.50 6.75	1.5 3.0 4.0		1.5 3.0 4.0	V V V
V <sub>IH</sub>	High Level Input Voltage	V <sub>DD</sub> = 5V, V <sub>O</sub> = 0.5V or 4.5V V <sub>DD</sub> = 10V, V <sub>O</sub> = 1V or 9V V <sub>DD</sub> = 15V, V <sub>O</sub> = 1.5V or 13.5V	3.5 7.0 11.0		3.5 7.0 11.0	2.75 5.50 8.25		3.5 7.0 11.0		V V V
I <sub>OL</sub>	Low Level Output Current (Note 3)	V <sub>DD</sub> = 5V, V <sub>O</sub> = 0.4V V <sub>DD</sub> = 10V, V <sub>O</sub> = 0.5V V <sub>DD</sub> = 15V, V <sub>O</sub> = 1.5V	0.52 1.3 3.6		0.44 1.1 3.0	0.88 2.25 8.8		0.36 0.9 2.4		mA mA mA
I <sub>OH</sub>	High Level Output Current (Note 3)	V <sub>DD</sub> = 5V, V <sub>O</sub> = 4.6V V <sub>DD</sub> = 10V, V <sub>O</sub> = 9.5V V <sub>DD</sub> = 15V, V <sub>O</sub> = 13.5V	−0.2 −0.5 −1.4		−0.16 −0.4 −1.2	−0.36 −0.9 −3.5		−0.12 −0.3 −1.0		mA mA mA
I <sub>IN</sub>	Input Current	V <sub>DD</sub> = 15V, V <sub>IN</sub> = 0V V <sub>DD</sub> = 15V, V <sub>IN</sub> = 15V		−0.3 0.3		−10 <sup>−5</sup> 10 <sup>−5</sup>	−0.3 0.3		−1.0 1.0	μA μA

**Note 1:** "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range", they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

**Note 2:** V<sub>SS</sub> = 0V unless otherwise specified.

**Note 3:** I<sub>OH</sub> and I<sub>OL</sub> are tested one output at a time.

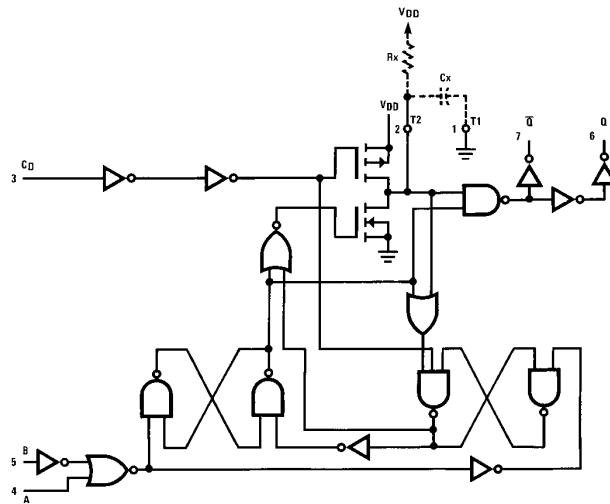
## AC Electrical Characteristics\* CD4528BM

$T_A = 25^\circ\text{C}$ ,  $C_L = 50 \text{ pF}$ ,  $R_L = 200 \text{ k}\Omega$ , Input  $t_r = t_f = 20 \text{ ns}$ , unless otherwise specified

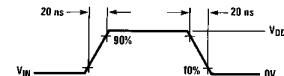
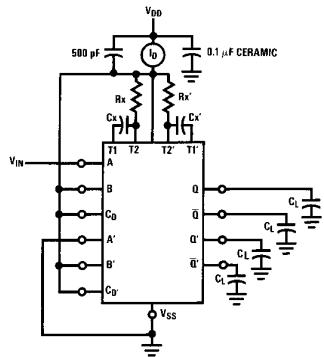
Parameter	Conditions	Min	Typ	Max	Units
Output Rise Time	$t_r = (3.0 \text{ ns/pF}) C_L + 30 \text{ ns}, V_{DD} = 5.0V$ $t_r = (1.5 \text{ ns/pF}) C_L + 15 \text{ ns}, V_{DD} = 10.0V$ $t_r = (1.1 \text{ ns/pF}) C_L + 10 \text{ ns}, V_{DD} = 15.0V$		180 90 65	400 200 160	ns ns ns
Output Fall Time	$t_f = (1.5 \text{ ns/pF}) C_L + 25 \text{ ns}, V_{DD} = 5.0V$ $t_f = (0.75 \text{ ns/pF}) C_L + 12.5 \text{ ns}, V_{DD} = 10V$ $t_f = (0.55 \text{ ns/pF}) C_L + 9.5 \text{ ns}, V_{DD} = 15.0V$		100 50 35	200 100 80	ns ns ns
Turn-Off, Turn-On Delay A or B to Q or $\bar{Q}$ $Cx = 15 \text{ pF}, Rx = 5.0 \text{ k}\Omega$	$t_{PLH}, t_{PHL} = (1.7 \text{ ns/pF}) C_L + 240 \text{ ns}, V_{DD} = 5.0V$ $t_{PLH}, t_{PHL} = (0.66 \text{ ns/pF}) C_L + 8 \text{ ns}, V_{DD} = 10.0V$ $t_{PLH}, t_{PHL} = (0.5 \text{ ns/pF}) C_L + 65 \text{ ns}, V_{DD} = 15.0V$		230 100 65	500 250 150	ns ns ns
Turn-Off, Turn-On Delay A or B to Q or $\bar{Q}$ $Cx = 100 \text{ pF}, Rx = 10 \text{ k}\Omega$	$t_{PLH}, t_{PHL} = (1.7 \text{ ns/pF}) C_L + 620 \text{ ns}, V_{DD} = 5.0V$ $t_{PLH}, t_{PHL} = (0.66 \text{ ns/pF}) C_L + 257 \text{ ns}, V_{DD} = 10.0V$ $t_{PLH}, t_{PHL} = (0.5 \text{ ns/pF}) C_L + 185 \text{ ns}, V_{DD} = 15.0V$		230 100 65	500 250 150	ns ns ns
Minimum Input Pulse Width A or B $Cx = 15 \text{ pF}, Rx = 5.0 \text{ k}\Omega$	$V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15V$		60 20 20	150 50 50	ns ns ns
$Cx = 1000 \text{ pF}, Rx = 10 \text{ k}\Omega$	$V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15.0V$		60 20 20	150 50 50	ns ns ns
Output Pulse Width Q or $\bar{Q}$ For $Cx < 0.01 \mu\text{F}$ (See Graph for Appropriate $V_{DD}$ Level) $Cx = 15 \text{ pF}, Rx = 5.0 \text{ k}\Omega$	$V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15.0V$		550 350 300		ns ns ns
For $Cx > 0.01 \mu\text{F}$ Use $PW_{out} = 0.2 Rx Cx \ln [V_{DD} - V_{SS}]$ $Cx = 10,000 \text{ pF}, Rx = 10 \text{ k}\Omega$	$V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15.0V$	15 10 15	29 37 42	45 90 95	$\mu\text{s}$ $\mu\text{s}$ $\mu\text{s}$
Pulse Width Match between Circuits in the Same Package $Cx = 10,000 \text{ pF}, Rx = 10 \text{ k}\Omega$	$V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15.0V$		6 8 8	25 35 35	% % %
Reset Propagation Delay, $t_{PLH}, t_{PHL}$ $Cx = 15 \text{ pF}, Rx = 5.0 \text{ k}\Omega$	$V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15.0V$		325 90 60	600 225 170	ns ns ns
$Cx = 1000 \text{ pF}, Rx = 10 \text{ k}\Omega$	$V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15.0V$		7.0 6.7 6.7		$\mu\text{s}$ $\mu\text{s}$ $\mu\text{s}$
Minimum Retrigger Time $Cx = 15 \text{ pF}, Rx = 5.0 \text{ k}\Omega$ $Cx = 1000 \text{ pF}, Rx = 10 \text{ k}\Omega$	$V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15.0V$ $V_{DD} = 5.0V$ $V_{DD} = 10.0V$ $V_{DD} = 15.0V$		0 0 0 0 0 0		ns ns ns ns ns ns

\*AC parameters are guaranteed by DC correlated testing.

## Logic Diagrams (1/2 of Device Shown)



TL/F/5998-3

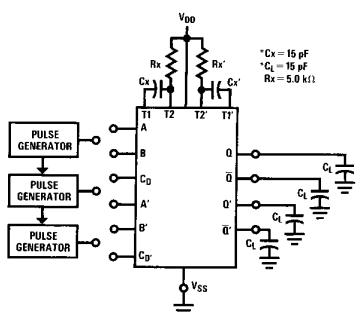


TL/F/5998-10

FIGURE 1. Power Dissipation Test Circuit and Waveforms

### Input Connections

Characteristics	C <sub>D</sub>	A	B
t <sub>PLH</sub> , t <sub>PHL</sub> , t <sub>r</sub> , t <sub>f</sub> , PW <sub>out</sub> , PW <sub>in</sub>	V <sub>DD</sub>	PG1	V <sub>DD</sub>
t <sub>PLH</sub> , t <sub>PHL</sub> , t <sub>r</sub> , t <sub>f</sub> , PW <sub>out</sub> , PW <sub>in</sub>	V <sub>DD</sub>	V <sub>SS</sub>	PG2
t <sub>PLH(R)</sub> , t <sub>PHL(R)</sub> , PW <sub>in</sub>	PG3	PG1	PG2



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\*Includes capacitance of probes, wiring, and fixture parasitic.

Note: AC test waveforms for PG1, PG2, and PG3 on next page.

PG1 =

PG2 =

PG3 =

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FIGURE 2. AC Test Circuit

## Logic Diagrams (1/2 of Device Shown) (Continued)

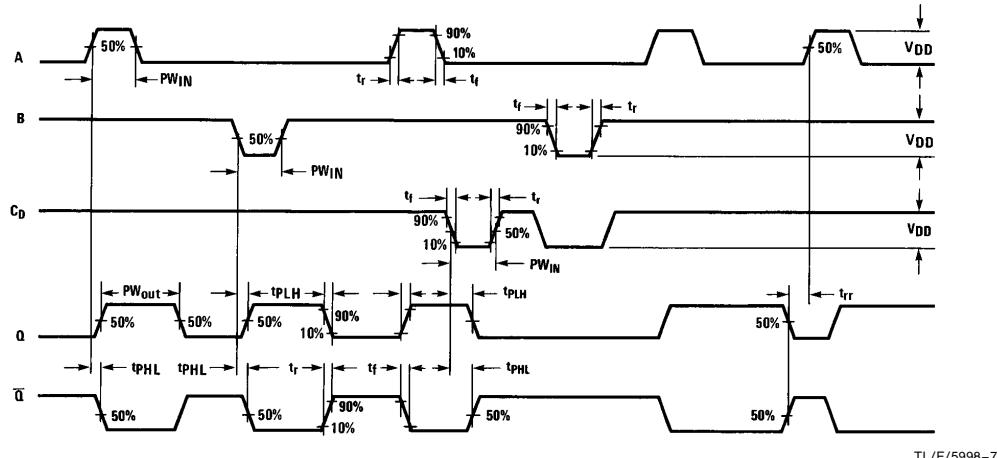


FIGURE 3. AC Test Waveforms

TL/F/5998-7

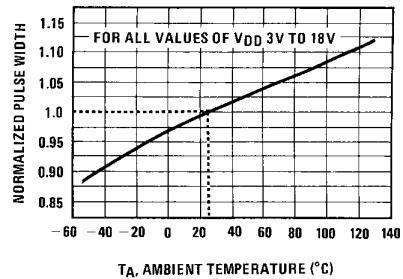


FIGURE 4. Normalized Pulse Width vs Temperature

TL/F/5998-8

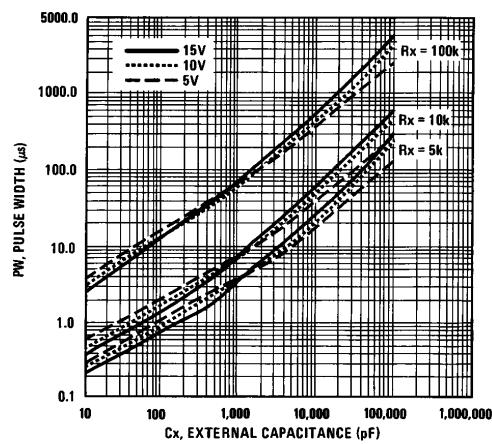
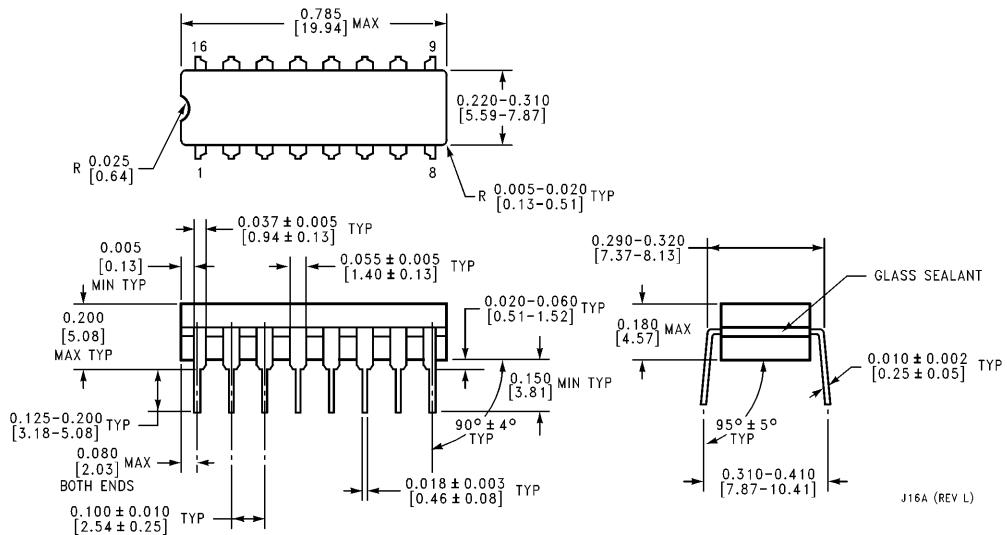


FIGURE 5. Pulse Width vs Cx

TL/F/5998-9

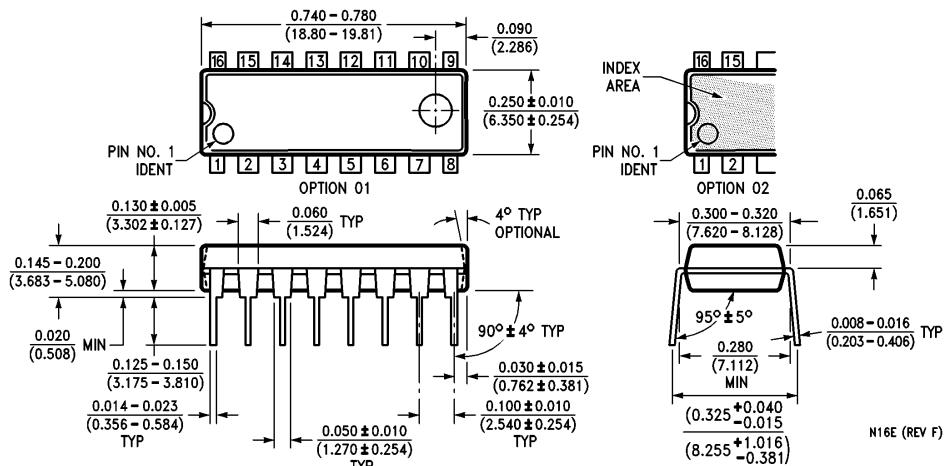
**Physical Dimensions** inches (millimeters)



Ceramic Dual-In-Line Package (J)  
Order Number CD4528BMJ or CD4528BCJ  
NS Package Number J16A

J16A (REV L)

**Physical Dimensions** inches (millimeters) (Continued)



**Molded Dual-In-Line Package (N)**  
Order Number CD4528BM or CD4528BCN  
NS Package Number N16E

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