TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC521P,TC74AC521F

8-Bit Equality Comparator

The TC74AC521 is an advanced high speed CMOS 8-BIT DIGITAL COMPARATOR fabricated with silicon gate and double-layer metal wiring C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

It compares two 8-bit binary or BCD words applied inputs $P_0\sim P_7$, and inputs $Q_0\sim Q_7$, and indicates whether or not they are equal.

A signal active low enable is provided to facilitate cascading of several packages to compare of words greater than 8 bits.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

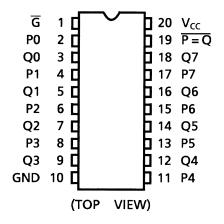
Features

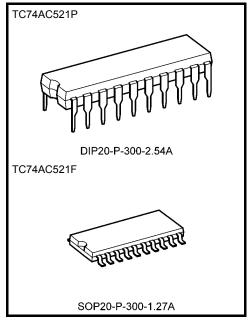
- High speed: $t_{pd} = 6.4 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 8 \mu A \text{ (max)}$ at $T_a = 25^{\circ}C$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24$ mA (min) Capability of driving 50 Ω

transmission lines.

- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 5.5 V
- Pin and function compatible with 74F521

Pin Assignment

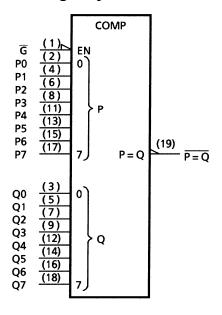




Weight

DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.)

IEC Logic Symbol

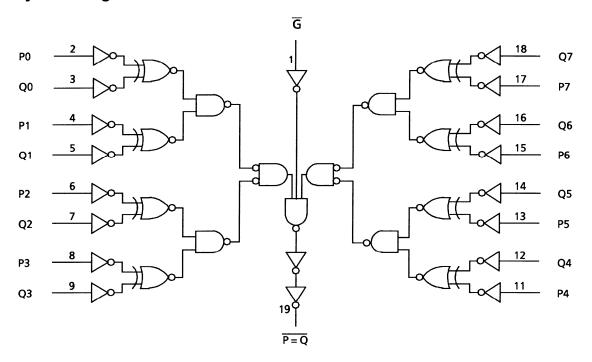


Truth Table

Inp	uts	Output				
P, Q	Ġ	$\overline{P} = \overline{Q}$				
P = Q	L	L				
P ≠ Q	L	Н				
Х	Н	Н				

X: Don't care

System Diagram



2

2007-10-01

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	−0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±100	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to $65^{\circ}C$. From Ta = 65 to $85^{\circ}C$ a derating factor of -10 mW/°C should be applied up to 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dV	0 to 100 ($V_{CC} = 3.3 \pm 0.3 \text{ V}$) 0 to 20 ($V_{CC} = 5 \pm 0.5 \text{ V}$)	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit		
Sharadichidios Symbol					V _{CC} (V)	Min	Тур.	Max	Min	Max	Onit	
		_		2.0	1.50	_	_	1.50	_			
High-level input voltage	V_{IH}			3.0	2.10	_	_	2.10	_	V		
					5.5	3.85	_	_	3.85	_		
					2.0	_	_	0.50	_	0.50		
Low-level input voltage	V_{IL}		_		3.0	_	_	0.90	_	0.90	V	
, and the second					5.5	—	_	1.65	—	1.65		
					2.0	1.9	2.0	_	1.9	_		
		V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -50 \mu A$		3.0	2.9	3.0	_	2.9	_		
High-level output	Voh			4.5	4.4	4.5	_	4.4	_	V		
voltage	VOН		$I_{OH} = -4 \text{ mA}$		3.0	2.58	_		2.48	_	v	
			$I_{OH} = -24 \text{ mA}$		4.5	3.94	_	_	3.80	_		
			$I_{OH} = -75 \text{ mA}$	(Note)	5.5	—	_	_	3.85	_		
Low-level output voltage		V _{IN} = V _{IH} or V _{IL}			2.0	_	0.0	0.1	_	0.1		
			$I_{OL} = 50 \mu A$		3.0	_	0.0	0.1	_	0.1		
	V_{OL}				4.5	_	0.0	0.1	_	0.1	V	
	VOL		$I_{OL} = 12 \text{ mA}$		3.0	_	_	0.36	_	0.44	V	
			$I_{OL} = 24 \text{ mA}$		4.5	_	_	0.36	_	0.44		
			$I_{OL} = 75 \text{ mA}$	(Note)	5.5	_	_	_	_	1.65		
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		5.5	_	_	±0.1	_	±1.0	μА		
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND			5.5	_	_	8.0	_	80.0	μА	

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

AC Characteristics ($C_L = 50 \text{ pF}, R_L = 500 \Omega, \text{ input: } t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	-,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay time	t _{pLH}	_	3.3 ± 0.3	_	10.5	17.5	1.0	20.0	ns
(Pn, Qn- $\overline{P} = Q$)	t _{pHL}		5.0 ± 0.5	_	7.2	11.0	1.0	12.5	
Propagation delay time	t _{pLH}	_	3.3 ± 0.3	_	7.2	11.5	1.0	13.0	ns
$(\overline{G} - \overline{P = Q})$	t _{pHL}		5.0 ± 0.5	_	4.8	7.0	1.0	8.0	110
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD} (Note)				34			_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

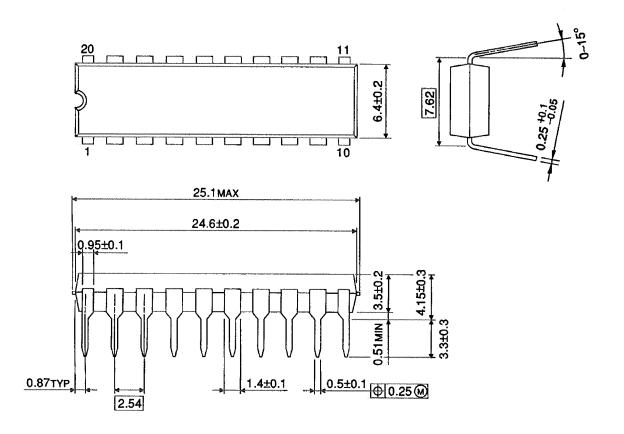
Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

4 2007-10-01

Package Dimensions

DIP20-P-300-2.54A Unit: mm

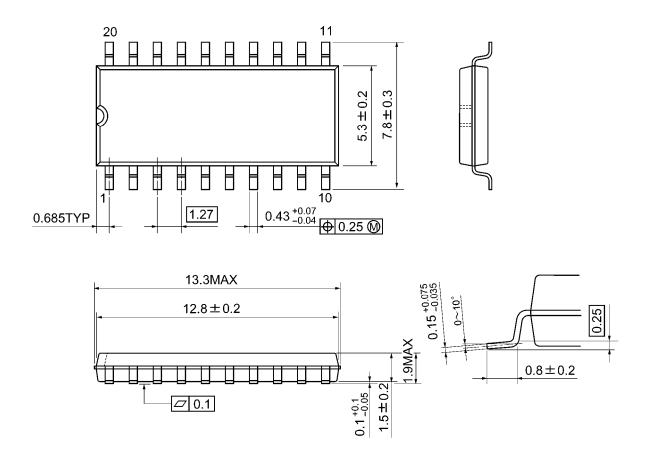


5

Weight: 1.30 g (typ.)

Package Dimensions

SOP20-P-300-1.27A Unit: mm



6

Weight: 0.22 g (typ.)

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