# 8-Bit Magnitude Comparators

The SN74LS682, 684, 688 are 8-bit magnitude comparators. These device types are designed to perform comparisons between two eight-bit binary or BCD words. All device types provide  $\overline{P} = \overline{Q}$  outputs and the LS682 and LS684 have  $\overline{P} > \overline{Q}$  outputs also.

The LS682, LS684 and LS688 are totem pole devices. The LS682 has a 20  $\,\mathrm{k}\Omega$  pullup resistor on the Q inputs for analog or switch data.

TYPE	P = Q	P > Q	OUTPUT ENABLE	OUTPUT CONFIGURATION	PULLUP
LS682	yes	yes	no	totem-pole	yes
LS684	yes	yes	no	totem-pole	no
LS688	yes	no	yes	totem-pole	no

#### **GUARANTEED OPERATING RANGES**

Symbol	Parameter	Min	Тур	Max	Unit
V <sub>CC</sub>	Supply Voltage	4.75	5.0	5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	0	25	70	Ŝ
I <sub>OH</sub>	Output Current - High			- 0.4	mA
I <sub>OL</sub>	Output Current - Low			24	mA



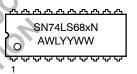
#### **ON Semiconductor**

http://onsemi.com

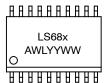
## LOW POWER SCHOTTKY



#### MARKING DIAGRAMS







x = 2, 4, or 8

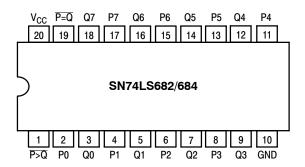
A = Assembly Location

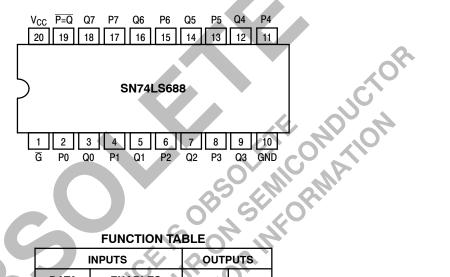
WL = Wafer Lot YY = Year WW = Work Week

#### **ORDERING INFORMATION**

Device	Package	Shipping
SN74LS682N	PDIP-20	1440 Units/Box
SN74LS682DW	SOIC-WIDE	38 Units/Rail
SN74LS682DWR2	SOIC-WIDE	2500/Tape & Reel
SN74LS684N	PDIP-20	1440 Units/Box
SN74LS684DW	SOIC-WIDE	38 Units/Rail
SN74LS684DWR2	SOIC-WIDE	2500/Tape & Reel
SN74LS688N	PDIP-20	1440 Units/Box
SN74LS688DW	SOIC-WIDE	38 Units/Rail
SN74LS688DWR2	SOIC-WIDE	2500/Tape & Reel

#### **CONNECTION DIAGRAMS** (TOP VIEW)





## FUNCTION TABLE

		NPUTS	OUTI	PUTS	
1	DATA	ENABL	ES.		)
	P, Q	G, GT	G2	<b>P</b> = <b>Q</b>	P > Q
	P = Q P > Q P < Q X	-3-2	JJUE	JEEE	<b>111</b>

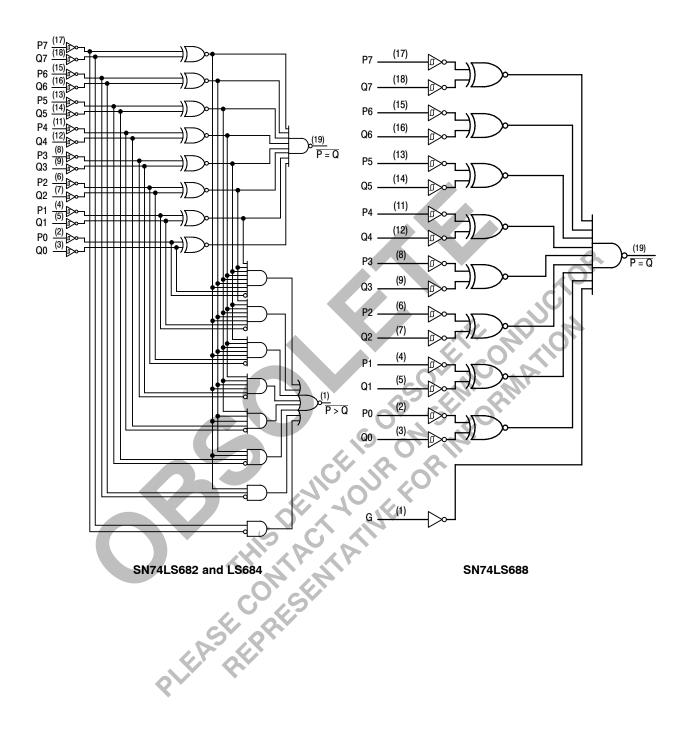
HIGH Level, L = LOW Level, X = Irrelevant

#### DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Symbol				Limits				
Symbol	Param	eter	Min	Тур	Max	Unit	Tes	t Conditions
/ <sub>IH</sub>	Input HIGH Voltage		2.0			V	Guaranteed Inpu All Inputs	ut HIGH Voltage for
/ <sub>IL</sub>	Input LOW Voltage	Input LOW Voltage			0.8	V	Guaranteed Inpu All Inputs	ut LOW Voltage for
√ <sub>IK</sub>	Input Clamp Diode Vo	oltage		-0.65	-1.5	V	V <sub>CC</sub> = MIN, I <sub>IN</sub> =	- 18 mA
V <sub>OH</sub>	Output HIGH Voltage		2.7	3.5		V	V <sub>CC</sub> = MIN, I <sub>OH</sub> or V <sub>IL</sub> per Truth	= MAX, V <sub>IN</sub> = V <sub>IH</sub> Table
				0.25	0.4	V	I <sub>OL</sub> = 12 mA	$V_{CC} = V_{CC} MIN,$
V <sub>OL</sub>	Output LOW Voltage			0.35	0.5	V	I <sub>OL</sub> = 24 mA	V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> per Truth Table
					20	μΑ	V <sub>CC</sub> ≜ MAX, V <sub>IN</sub>	
IH	Input HIGH Current	LS682-Q Inputs			0.1	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub>	
		Others		_	0.1	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub>	
		LS682-Q Inputs			-0.4	mA		<u> </u>
IL	Input LOW Current	Others			-0.2	mA	$V_{CC} = MAX, V_{IN}$	= 0,4 V
OS	Short Circuit Current	(Note 1)	-30	7	-130	mA	V <sub>CC</sub> = MAX	-12
		LS682			70	mA	1, 4	70,
CC	Power Supply	LS684			65	mA	V <sub>CC</sub> = MAX	
	Current	LS688 I be shorted at a time			65	mA	We will	
						Car		

<sup>1.</sup> Not more than one output should be shorted at a time, nor for more than 1 second

LOGIC DIAGRAMS



## AC CHARACTERISTICS $(T_A = 25^{\circ}C)$

#### SN74LS682

			Limits			
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, P to $\overline{P} = \overline{Q}$		13 15	25 25	ns	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, Q to $\overline{P} = \overline{Q}$		14 15	25 25	ns	V <sub>CC</sub> = 5.0 V C <sub>L</sub> = 45 pF
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, P to $\overline{P} > \overline{Q}$		20 15	30 30	ns	$R_L = 667 \Omega$
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, Q to P > Q		21 19	30 30	ns	

#### SN74LS684

			Limits			
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, P to $\overline{P} = \overline{Q}$		15 17	25 25	ns	×0,
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, Q to $\overline{P} = \overline{Q}$		16 15	25 25	ns	$V_{CC}$ = 5.0 V $C_L$ = 45 pF
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, P to $\overline{P} > \overline{Q}$		22 17	30 30	ns	C <sub>L</sub> = 45 pr R <sub>L</sub> = 667 Ω
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, Q to P > Q		24 20	30 30	ns	MICHA

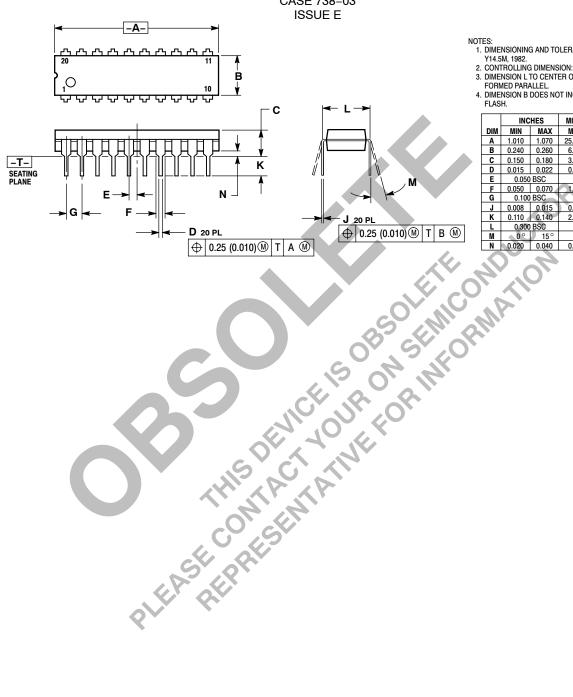
#### SN74LS688

			Limits	2	19	
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, P to $\overline{P} = Q$		12 17	18 23	ns	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, Q to $\overline{P} = \overline{Q}$	0	12 17	18 23	ns	$V_{CC}$ = 5.0 V $C_L$ = 45 pF $R_L$ = 667 $\Omega$
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, $\overline{G}$ , $\overline{G1}$ to $\overline{P} = \overline{Q}$		12 13	18 20	ns	-
	PLEASERER	ESE				

#### PACKAGE DIMENSIONS

## **N SUFFIX**

PLASTIC PACKAGE CASE 738-03



#### NOTES:

- OTES:

  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

  2. CONTROLLING DIMENSION: INCH.

  3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL

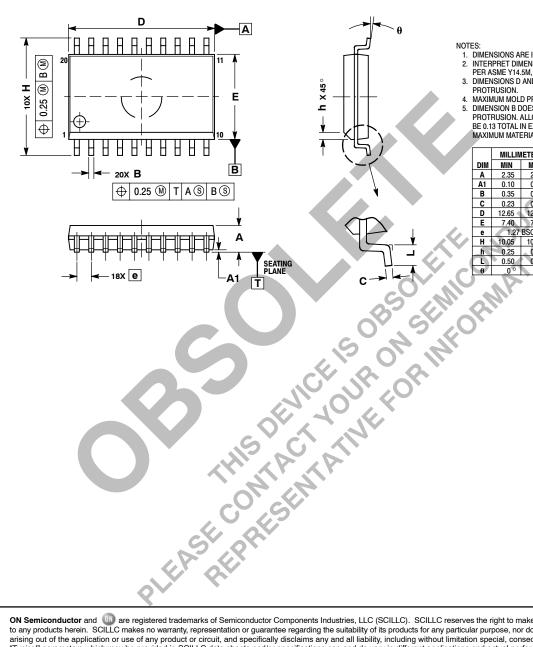
- 4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	1.010	1.070	25.66	27.17	
В	0.240	0.260	6.10	6.60	
С	0.150	0.180	3.81	4.57	
D	0.015	0.022	0.39	0.55	
Е	0.050	BSC	1.27 BSC		
F	0.050	0.070	1.27	1.77	
G	0.100	BSC	2.54	BSC	
J	0.008	0.015	0.21	0.38	
K	0.110	0.140	2.80	3.55	
L	0.300	BSC	7.62	BSC	
M	0°	15°	0°	15°	
N	0.020	0.040	0.51	1.01	

#### PACKAGE DIMENSIONS

#### **DW SUFFIX**

PLASTIC SOIC PACKAGE CASE 751D-05 ISSUE F



- DIMENSIONS ARE IN MILLIMETERS.
- INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION.
- MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
- DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF B DIMENSION AT MAXIMUM MATERIAL CONDITION

	MILLIN	IETERS
DIM	MIN	MAX
Α	2.35	2.65
A1	0.10	0.25
В	0.35	0.49
С	0.23	0.32
D	12.65	12.95
Е	7.40	7.60
е	1.27	BSC
H 4	10.05	10.55
h	0.25	0.75
L	0.50	0.90
θ	0 °	7°

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