TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HCT688AP,TC74HCT688AF

8-Bit Equality Comparator

The TC74HCT688A is a high speed CMOS 8-BIT EQUALITY COMPARATOR fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage leveles.

The TC74HCT688A compares two 8-bit binary or BCD words applied inputs P_0 ~P7, and inputs Q_0 ~Q7, and indicates whether or not they are equal.

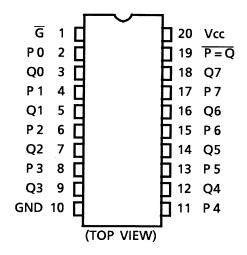
A signal active low enable is provided to facilitate cascading of several packege to compare of words greater than 8 bits.

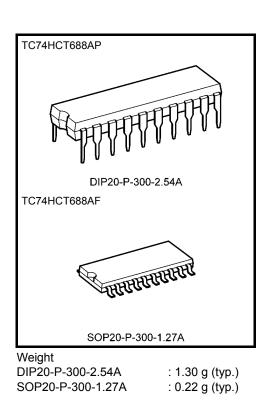
All inputs are equipped with protection circuits against atatic discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 17 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A \pmod{at Ta} = 25^{\circ}C$
- Compatible with TTL outputs: $V_{IH} = 2.0 V (min)$ $V_{IL} = 0.8 V (max)$
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74LS688

Pin Assignment

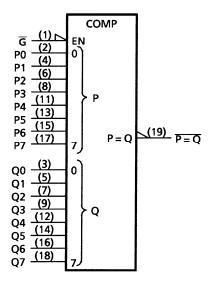




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IEC Logic Symbol

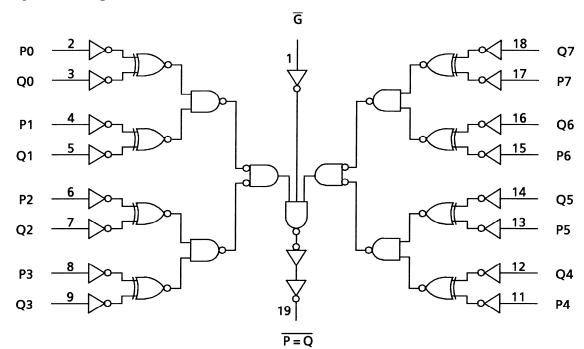


Truth Table

Inp	uts	Output			
P, Q	ĪG	$\overline{P} = Q$			
P = Q L		L			
P ≠ Q	L	Н			
ХН		Н			

X: Don't care

System Diagram



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Supply voltage range	V _{CC}	-0.5~7.0	V	
DC input voltage	VIN	-0.5~V _{CC} + 0.5	V	
DC output voltage	Vout	-0.5~V _{CC} + 0.5	V	
Input diode current	IIК	±20	mA	
Output diode current	I _{OK}	±20	mA	
DC output current	IOUT	±25	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW	
Storage temperature	T _{stg}	-65~150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~V _{CC}	V
Output voltage	Vout	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	t _r , t _f	0~500	ns

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = −40~85°C		Unit	
				$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Unit
High-level input voltage	VIH	—		4.5~5.5	2.0	_	_	2.0	_	V
Low-level input voltage	V _{IL}	—		4.5~5.5	_	_	0.8	_	0.8	V
High-level output VOH	Vou	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5		4.4		v
	VOH		$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31		4.13		
Low-level output V _{OL}	Ve	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \ \mu A$	4.5		0.0	0.1	—	0.1	V
	VOL		$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26	—	0.33	v
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		5.5	_	_	±0.1	_	±1.0	μA
		$V_{IN} = V_{CC}$ or GND		5.5	_	—	4.0		40.0	μA
Quiescent supply current	IC	Per input: $V_{IN} = 0.5 \text{ V or } 2.4 \text{ V}$ Other input: V_{CC} or GND		5.5			2.0	_	2.9	mA

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AC Characteristics ($C_L = 15 \text{ pF}$, $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}C$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH}		—	6	12	ns
	t _{THL}	—				
Propagation delay time	t _{pLH}			17	27	2
$(Pn, Qn - \overline{P = Q})$	t _{pHL}	—		17	21	ns
Propagation delay time	t _{pLH}		_	12	19	ns
$(\overline{G} - \overline{P} = Q)$	t _{pHL}					

AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -4	Unit	
Characteristics	Symbol		$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Offic
Output transition time	t _{TLH}		4.5	_	8	15	_	19	ne
	t _{THL}		5.5		7	13	—	16	ns
Propagation delay time	t _{pLH}		4.5		21	32	_	40	ns
$(Pn, Qn - \overline{P = Q})$	t _{pHL}	_	5.5	—	18	29	—	36	113
Propagation delay time	t _{pLH}	_	4.5		15	23	_	29	ns
$(\overline{G} - \overline{P = Q})$	t _{pHL}		5.5	—	13	21	—	26	110
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD} (Note)	_		_	32	_	—	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

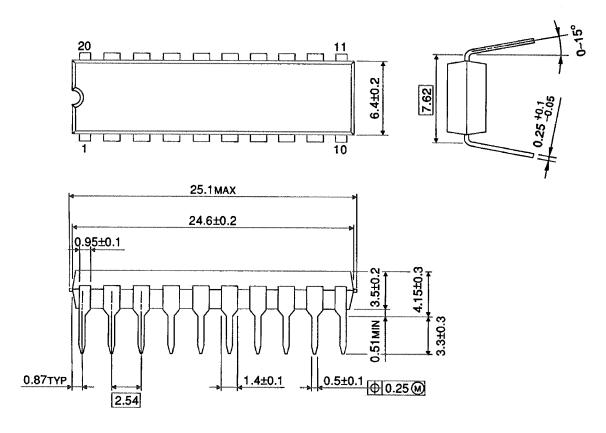
 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

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Package Dimensions

DIP20-P-300-2.54A

Unit : mm



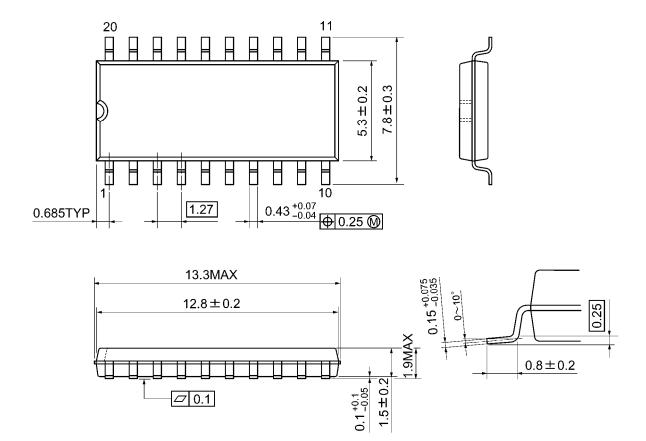
Weight: 1.30 g (typ.)



Package Dimensions

SOP20-P-300-1.27A

Unit: mm



Weight: 0.22 g (typ.)

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