

Memory Module Specification

KVR533D2S4/512

512MB 64M x 64-Bit DDR2-533 CL4 200-Pin SODIMM

DESCRIPTION:

This document describes ValueRAM's 64M x 64-bit (512MB) CL4 DDR2-533 SDRAM (Synchronous DRAM) memory module. The components on this module include eight 64M x 8-bit (16M x 8-bit x 4 Bank) DDR2-533 SDRAM in FBGA packages. This 200-pin SODIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

FEATURES:

UL Rating

☑ Clock Cycle Time (tCK) CL=4 3.75ns (min.) / 8ns (max.)

☑ Row Cycle Time (tRC) 55ns (min.)

Refresh to Active/Refresh Command Time (tRFC) 75ns

☑ Row Active Time (tRAS) 40ns (min.) / 70,000ns (max.)

☑ Single Power Supply of +1.8V(+/-.1V)

✓ Power 1.476 W (operating) 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 30mm.

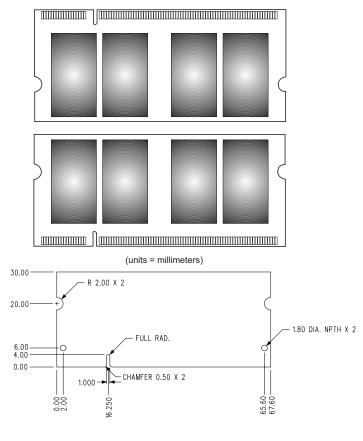


Figure 1: KVR533D2S4/512 200-Pin SODIMM