# TOSHIBA

TOSHIBA Photocoupler GaAs Ired + Photo-Triac

## TLP166J

Unit: mm

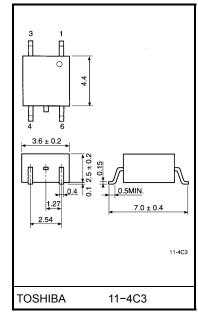
# TLP166J

Triac Drivers Programmable Controllers AC-Output Modules Solid State Relays

The TOSHIBA mini-flat coupler TLP166J is a small-outline coupler, suitable for surface-mount assembly.

The TLP166J consists of a gallium arsenide infrared emitting diode optically coupled to a triac-output photocoupler.

- Peak off-state voltage: 600 V (min)
- Trigger LED current: 10 mA (max)
- On-state current: 70 mA (max)
- Isolation voltage: 2500 Vrms (min)
- UL-approved: UL1577, file no. E67349
- Option(V4) type
   VDE approved: EN 60747-5-2 satisfied
   Maximum operating insulation voltage: 565 Vpk
   Maximum permissible overvoltage: 4000 Vpk



Weight: 0.09 g (typ.)

(Note): Please designate "Option (V4)",when an EN60747-5-2 approved type is needed. Please note that this product doesn't have 0.4mm insulator thickness when you apply for the safety

standard.

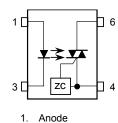
# **Trigger LED Current**

Type (Note 1)	Trigger LED Current (mA)		Marking Of
	V <sub>T</sub> = 3 V, Ta = 25°C		Marking Of Classification
	Min	Max	Olassification
(IFT7)	_	7	Τ7
None	_	10	T7, blank

\* e.g., IFT7: TLP166J(IFT7)

(Note 1): When applying for safety standard certification, use the standard part number. For example, TLP166J(IFT7): TLP166J

#### **Pin Configurations**



Anode
 Cathode

6. Terminal 2

llers

<sup>4.</sup> Terminal 1

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
	Forward current	١ <sub>F</sub>	50	mA		
	Forward current derating (Ta ≥ 5	ΔI <sub>F</sub> / °C	-0.7	mA / °C		
LED	Peak forward current (100µs pu	lse, 100pps)	I <sub>FP</sub>	1	А	
	Reverse voltage	VR	5	V		
	Junction temperature	Тј	125	°C		
	Off-state output terminal voltage	VDRM	600	V		
	On-state RMS Current	Ta=25°C		70	mA	
<u>ب</u>		Ta=70°C	I <sub>T(RMS)</sub>	40	ma	
Detector	On–state current derating(Ta ≥ 2	ΔI <sub>T</sub> / °C	-0.67	mA / °C		
	Peak on-state current (100µs p	ITP	2	А		
	Peak nonrepetitive surge curren (PW=10ms)	I <sub>TSM</sub>	1.2	А		
	Junction temperature	Тј	115	°C		
Storag	Storage temperature range			–55 to 125	°C	
Operating temperature range			T <sub>opr</sub>	-40 to 100	°C	
Lead soldering temperature (10s)			T <sub>sol</sub>	260	°C	
Isolatio	Isolation voltage (AC, 1min., R.H.≤ 60%) (Note 2)			2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 2): Device considered a two-terminal device: Pins 1 and 3 shorted together and Pin 4 and 6 shorted together.

#### **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V <sub>AC</sub>			240	Vac
Forward current	IF	15	20	25	mA
Peak on-state current	I <sub>TP</sub>	-	-	1	А
Operating temperature	T <sub>opr</sub>	-25		85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

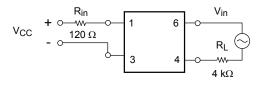
## **Electrical Characteristics (Ta = 25°C)**

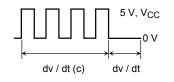
	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	10	μA
	Capacitance	CT	V = 0, f = 1 MHz	-	30	_	pF
Detector	Peak off-state current	I <sub>DRM</sub>	V <sub>DRM</sub> = 600 V	_	30	1000	nA
	Peak on-state voltage	V <sub>TM</sub>	I <sub>TM</sub> = 70 mA	_	1.7	2.8	V
	Holding current	Ι <sub>Η</sub>	—	Ι	0.6	—	mA
	Critical rate of rise of off–state voltage	dv / dt	V <sub>in</sub> = 240 Vrms, Ta = 85°C (Note 3)	200	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	I <sub>T</sub> = 15 mA, V <sub>in</sub> = 60 Vrms (Note 3)	_	0.2	_	V / µs

#### **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I <sub>FT</sub>	V <sub>T</sub> =3 V	_	_	10	mA
Inhibit voltage	VIH	I <sub>F</sub> = rated I <sub>FT</sub>	—	_	50	V
Leakage in inhibited state	IIН	I <sub>F</sub> = rated I <sub>FT</sub> V <sub>T</sub> = rated V <sub>DRM</sub>	—	_	600	μA
Capacitance input to output	CS	V <sub>S</sub> = 0, f = 1 MHz	—	0.8	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H.≤ 60%	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
	BVS	AC, 1 minute	2500	_	_	Vrms
Isolation voltage		AC, 1 second, in oil	_	5000	_	VIIIS
		DC, 1 minute, in oil	_	5000	_	Vdc

(Note 3): dv / dt Test circuit





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