Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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PHOTOCOUPLER PS9115

HIGH CMR, 10 Mbps TOTEM POLE OUTPUT TYPE 5-PIN SOP (SO-5) PHOTOCOUPLER -NEPOC Series-

DESCRIPTION

The PS9115 is an optically coupled high-speed, totem pole output isolator containing a GaAlAs LED on the input side and a photodiode and a signal processing circuit on the output side on one chip.

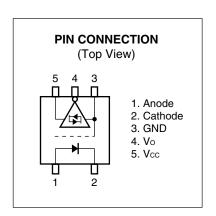
The PS9115 is specified high CMR, high CTR and pulse width distortion with operating temperature.

FEATURES

- High common mode transient immunity (CMH, CML = ±20 kV/μs TYP.)
- Small package (SO-5)
- Pulse width distortion (| tPHL tPLH | = 7 ns TYP.)
- High-speed (10 Mbps)
- High isolation voltage (BV = 3 750 Vr.m.s.)
- · Totem pole output
- Ordering number of taping product: PS9115-F3, F4: 2 500 pcs/reel
- · Pb-Free product
- Safety standards
 - UL approved: File No. E72422
 - DIN EN60747-5-2 (VDE0884 Part2) approved No. 40008902 (Option)

APPLICATIONS

- · Measurement equipment
- PDP
- Line Receiver for FA Network



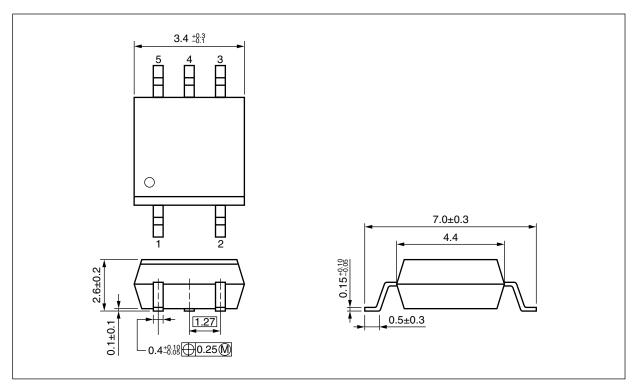
TRUTH TABLE

LED	Output
ON	L
OFF	Н

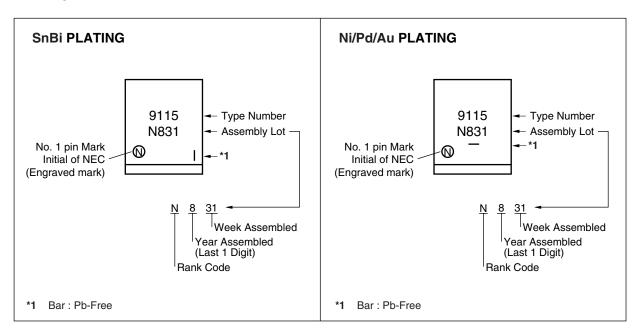
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<R> PACKAGE DIMENSIONS (UNIT: mm)



<R> MARKING EXAMPLE





<R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1
PS9115	PS9115-A	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9115
PS9115-F3	PS9115-F3-A	(SnBi)	Embossed Tape 2 500 pcs/reel	(UL approved)	
PS9115-F4	PS9115-F4-A				
PS9115-V	PS9115-V-A		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	
PS9115-V-F3	PS9115-V-F3-A		Embossed Tape 2 500 pcs/reel	(VDE0884 Part2)	
PS9115-V-F4	PS9115-V-F4-A			Approved (Option)	
PS9115	PS9115-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	
PS9115-F3	PS9115-F3-AX	(Ni/Pd/Au)	Embossed Tape 2 500 pcs/reel	(UL approved)	
PS9115-F4	PS9115-F4-AX				
PS9115-V	PS9115-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	
PS9115-V-F3	PS9115-V-F3-AX		Embossed Tape 2 500 pcs/reel	(VDE0884 Part2)	
PS9115-V-F4	PS9115-V-F4-AX			Approved (Option)	

^{*1} For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current ¹	lF	30	mA
	Reverse Voltage	VR	5	V
Detector	Supply Voltage	Vcc	7	V
	Output Voltage	Vo	7	V
	High Level Output Current	Іон	- 5	mA
	Low Level Output Current	loL	13	mA
	Power Dissipation ²	Pc	130	mW
Isolation Voltage ^{*3}		BV	3 750	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +85	°C
Storage Temperature		T _{stg}	-55 to +125	°C

- *1 Reduced to 0.3 mA/ $^{\circ}$ C at T_A = 25 $^{\circ}$ C or more.
- *2 TA = -40 to +85°C, Applies to output pin Vo and power supply pin Vcc. Reduced to 2.36 mW/°C at TA = 70°C or more
- *3 AC voltage for 1 minute at $T_A = 25^{\circ}C$, RH = 60% between input and output. Pins 1-2 shorted together, 3-5 shorted together.



RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
High Level Input Current	lғн	7.5		12.5	mA
Low Level Input Voltage	V _{FL}	0		0.8	V
Supply Voltage	Vcc	4.5	5.0	5.5	V
TTL (loads)	N			3	
Operating Ambient Temperature	TA	0		+85	°C

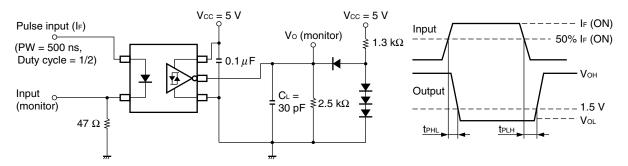
ELECTRICAL CHARACTERISTICS (TA = 0 to +85°C, unless otherwise specified)

Parameter		Symbol	Conditions	MIN.	TYP. ^{*1}	MAX.	Unit
Diode Forward Voltage		VF	IF = 10 mA, T _A = 25°C	1.4	1.65	1.9	٧
	Reverse Current	lR	VR = 3 V, TA = 25°C			10	μΑ
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T _A = 25°C		30		pF
Detector	High Level Output Current ²	Іон	Vcc = Vo = 5.5 V, V _F = 0.8 V		0.003	200	μΑ
	High Level Output Voltage	Vон	$Vcc = 4.5 \text{ V}, V_F = 0.8 \text{ V}, I_{OH} = -2 \text{ mA}$	2.4	3.0		٧
	Low Level Output Voltage	Vol	$Vcc = 4.5 \text{ V}, I_F = 7 \text{ mA}, I_{OL} = 8 \text{ mA}$		0.25	0.6	٧
	High Level Supply Current	Іссн	Vcc = 5.5 V, I _F = 0 mA, Vo = open		12	16	mA
	Low Level Supply Current	Iccl	Vcc = 5.5 V, I _F = 10 mA, Vo = open		13	16	mA
High Level Output Short Iosh Vcc = 5.5 V, Vo = GND, IF = 0 (Circuit Current 10 ms or less		Vcc = 5.5 V, Vo = GND, I _F = 0 mA, 10 ms or less		-26		mA	
	Low Level Output Short Circuit Current	losL	Vcc = Vo = 5.5 V, I _F = 8 mA, 10 ms or less		34		mA
Coupled	Threshold Input Current	IFHL	T _A = 25°C		2.3	5	mA
	$(H \to L)$	$V_{CC} = 5 \text{ V}, V_{O} = 0.6 \text{ V}$				6	
	Isolation Resistance	R _{I-O}	V _{I-O} = 1 kV _{DC} , RH = 40 to 60%, T _A = 25°C	1011			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1 MHz, T _A = 25°C		0.6		pF
	Propagation Delay Time	t PHL	T _A = 25°C	15	33	65	ns
	$(H \rightarrow L)^{*3}$		Vcc = 5 V, I _F = 7.5 mA	10		85	
	Propagation Delay Time	t PLH	T _A = 25°C	15	40	65	ns
	$(L \rightarrow H)^{*3}$		Vcc = 5 V, I _F = 7.5 mA	10		85	
	Pulse Width Distortion (PWD)*3	tphl-tplh	Vcc = 5 V, I _F = 7.5 mA		7	50	ns
	Common Mode Transient Immunity at High Level Output ^{*4}	СМн	Vcc = 5 V, T _A = 25°C, I _F = 0 mA, Vo (MIN.) = 2 V, VcM = 1 kV	10	20		kV/μs
	Common Mode Transient Immunity at Low Level Output ^{*4}	CM∟	Vcc = 5 V, Ta = 25°C, IF = 7.5 mA, Vo (MAX.) = 0.8 V, VcM = 1 kV	10	20		kV/μs



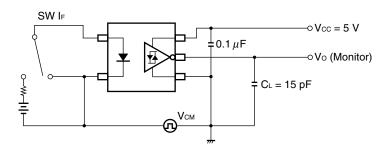


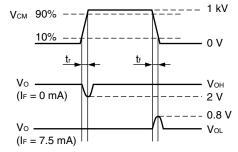
- *1 Typical values at T_A = 25°C
- *2 Because a high-level output current (Io_H) of 300 μA or more may be output when the temperature is 0°C or less and when Vcc is around 3 to 4 V, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.
- *3 Test circuit for propagation delay time



C∟ includes probe and stray wiring capacitance.

*4 Test circuit for common mode transient immunity





C∟ includes probe and stray wiring capacitance.

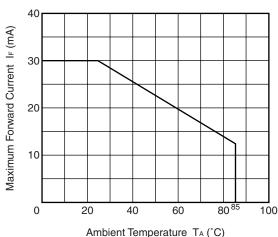
USAGE CAUTIONS

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of 0.1 μ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Avoid storage at a high temperature and high humidity.

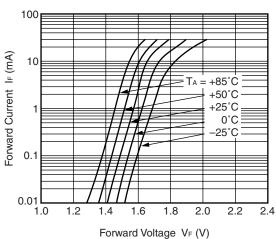


TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)

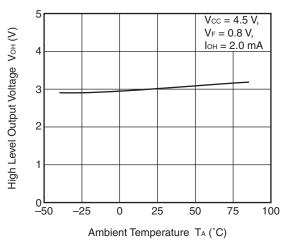




FORWARD CURRENT vs. FORWARD VOLTAGE

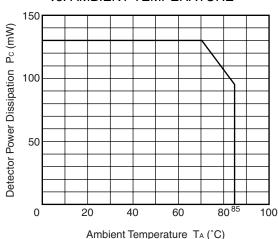


HIGH LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE

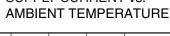


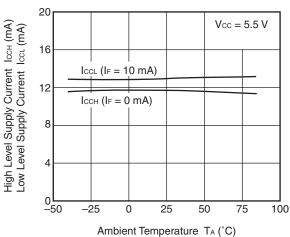
Remark The graphs indicate nominal characteristics.

DETECTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE

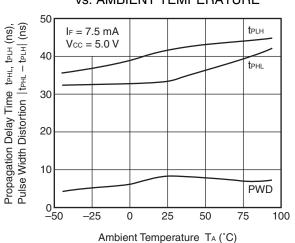


SUPPLY CURRENT vs.



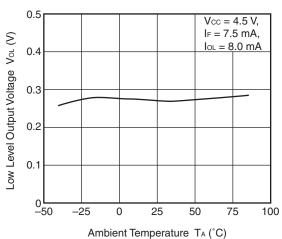


PROPAGATION DELAY TIME, PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE

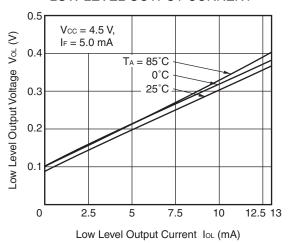




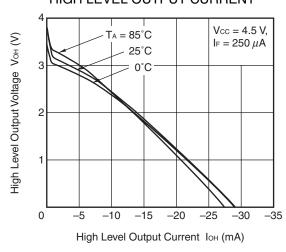
LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE



LOW LEVEL OUTPUT VOLTAGE vs. LOW LEVEL OUTPUT CURRENT

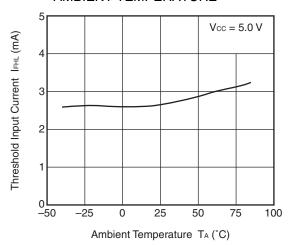


HIGH LEVEL OUTPUT VOLTAGE vs. HIGH LEVEL OUTPUT CURRENT

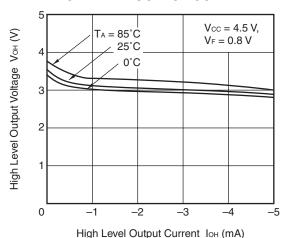


Remark The graphs indicate nominal characteristics.

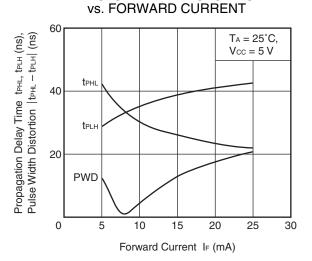
THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE



HIGH LEVEL OUTPUT VOLTAGE vs. HIGH LEVEL OUTPUT CURRENT

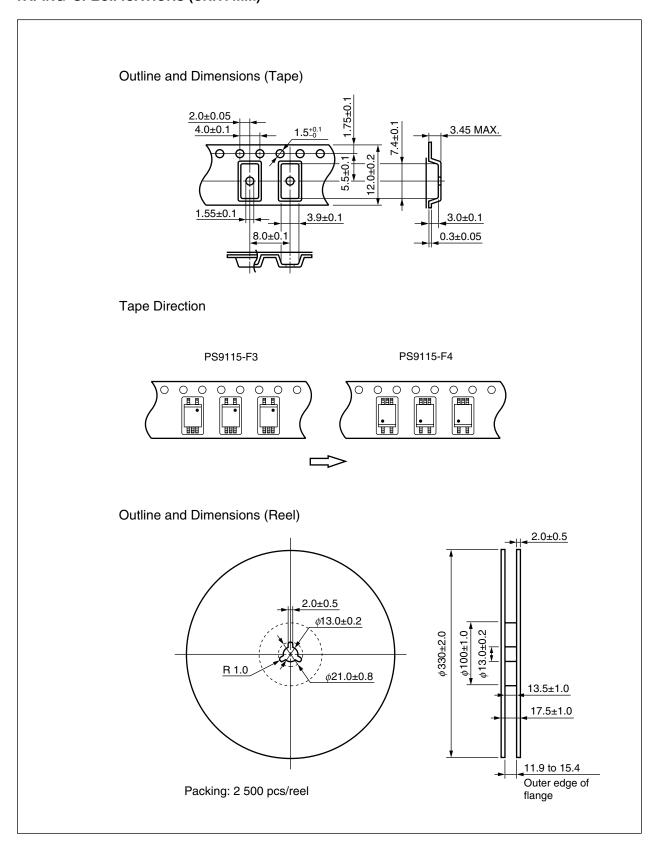


PROPAGATION DELAY TIME, PULSE WIDTH DISTORTION



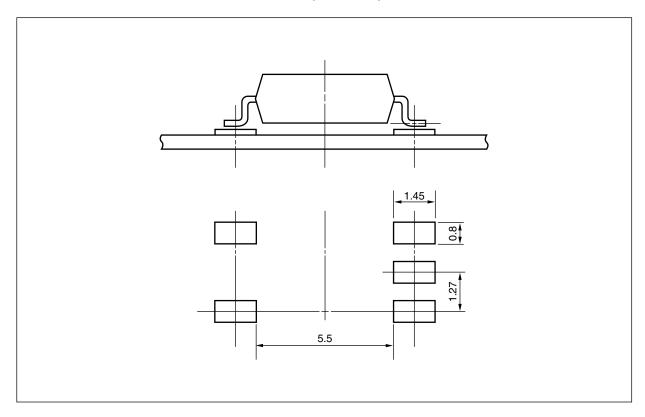


TAPING SPECIFICATIONS (UNIT: mm)





<R> RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)





NOTES ON HANDLING

1. Recommended soldering conditions

(1) Infrared reflow soldering

• Peak reflow temperature 260°C or below (package surface temperature)

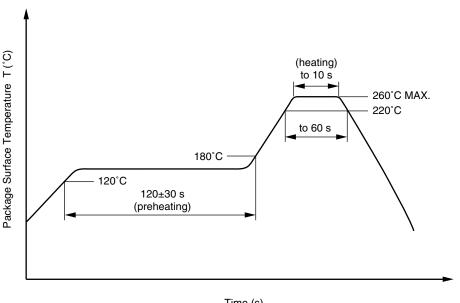
• Time of peak reflow temperature 10 seconds or less • Time of temperature higher than 220°C 60 seconds or less

• Time to preheat temperature from 120 to 180°C 120±30 s Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

 Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

· Preheating conditions 120°C or below (package surface temperature)

· Number of times One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

• Peak Temperature (lead part temperature) 350°C or below · Time (each pins) 3 seconds or less

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100°C





(4) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.



<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Application classification (DIN EN 60664-1 VDE0110 Part 1) for rated line voltages ≤ 300 Vr.m.s. for rated line voltages ≤ 600 Vr.m.s.		IV III	
Climatic test class (DIN EN 60664-1 VDE0110)		40/85/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{\text{IORM}}, P_{\text{d}} < 5 \text{pC}$	UIORM Upr	707 1 061	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices) $U_{pr}=1.875\times U_{IORM},P_d<5\;pC$	U_pr	1 326	V_{peak}
Highest permissible overvoltage	Utr	6 000	V _{peak}
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Clearance distance		>4.2	mm
Creepage distance		>4.2	mm
Comparative tracking index (DIN IEC 112/VDE 0303 Part 1)	СТІ	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	T _{stg}	-55 to +125	°C
Operating temperature range	Та	-40 to +85	°C
Isolation resistance, minimum value VIO = 500 V dc at TA = 25°C VIO = 500 V dc at TA MAX. at least 100°C	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I _F , Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	150 200 300	°C mA mW
Vio = 500 V dc at Ta = Tsi	Ris MIN.	10°	Ω





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M8E 02.11-1

NEC



Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.