

Features

- International Class 1 laser safety certified
- 1250 Mb/s data rate
- IEEE 802.3 Gigabit Ethernet compliant [1]
- Gigabit Interface Converter (GBIC) Revision 5.4 compliant [2]
- Both short wavelength (850 nm) (distance ≤550 m) and long wavelength (1310 nm) (distance ≤10 km) products available
- Gigabit electrical serial interface
- Serial electrical ⇔ light conversion
- UL and TUV approved
- Low bit error rate $(<10^{-12})$

Overview

The Gigabit Interface Converters (GBICs) are integrated fiber optic transceivers that provide highspeed serial links at a signaling rate of 1250 Mb/s. The JGB-12SYAA1 conforms to the IEEE 802.3 1000BASE-SX standard [1]. The JGB-12LYAA1 conforms to the IEEE 802.3 1000BASE-LX standard.

These Gigabit Interface Converters (GBICs) are ideally suited for Gigabit Ethernet applications, but can be used for other serial applications where high data rates are required. These modules are hot-pluggable and permit easy manufacturing and field configuration between shortwave, longwave, and copper implementations of the various standards.

The shortwave GBICs use short wavelength (850nm) VCSEL lasers. This enables low cost data transmission over optical fibers at distances up to 550m. A 50/125μm multimode optical fiber, terminated with an industry standard SC connector, is the preferred medium. A 62.5/125μm multimode fiber can be substituted with shorter maximum link distances.

The longwave GBICs use long wavelength (1310nm) lasers. This enables data transmission

Applications

- Gigabit Ethernet
- Client/Server environments
- Distributed multi-processing
- Fault tolerant applications
- Visualization, real-time video, collaboration
- Channel extenders, data storage, archiving
- Data acquisition

over optical fibers at distances up to 10km on a single mode (9/125μm) optical fiber, and distances up to 550m on multimode (50/125μm) optical fiber.

The GBICs with serial ID feature a 1 kbit EEPROM. The serial ID module can store up to 128 bytes of product information.

Encoded (8B/10B) [3], gigabit/sec, serial, differential, PECL signals traverse a 20-pin straddle mount connector interfacing the GBIC to the host card. The serial data modulates the laser and is sent out over the outgoing fiber of a duplex cable.

Incoming, modulated light is detected by a photoreceiver mounted in the SC receptacle. The optical signal is converted to an electrical one, amplified, and delivered to the host card. This module is designed to work with industry standard Serializer/Deserializer modules.

These GBICs are Class 1 laser safe products. The optical power levels, under normal operation, are at eye safe levels. Optical fiber cables can be connected and disconnected without shutting off the laser transmitter.

Pin Configuration

Pin Definitions

Ordering Information

Laser Safety Compliance Requirements

The GBICs are designed and certified as Class 1 laser products. If the power supply voltage exceeds 6.0 volts, the GBIC may no longer remain Class 1 products. The system using the GBIC must provide power supply over voltage protection that guarantees the supply does not exceed 6.0 volts under all fault conditions.

Operating the power supply above 6.0 V, or otherwise operating the GBIC in a manner inconsistent with their design and function may result in hazardous radiation exposure, and may be considered an act of modifying or new manufacturing of a laser product under US regulations contained in 21 CFR(J) or CENELEC regulations contained in EN 60825.

The person(s) performing such an act is required by law to recertify and reidentify the product in accordance with the provisions of 21 CFR(J) for distribution within the USA., and in accordance with provisions of CENELEC EN 60825 (or successive regulations) for distribution within the CENELEC countries or countries using the IEC 825 standard.

Block Diagram

Transmit Section

The input differential, serial data stream enters the AC Modulation section of the laser driver circuitry where it modulates a semiconductor laser. The DC Drive maintains the laser at the correct preset power level. In addition, there are safety circuits in the DC Drive that will shut off the laser if a fault is detected.

Receive Section

The incoming, modulated optical signal is converted to an electrical signal by the photoreceiver. This electrical signal is then amplified and converted to a differential, serial output data stream and delivered to the host. A transition detector detects a minimum AC level of modulated light entering the photoreceiver. This signal is provided to the host as a loss-of-signal status line.

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Output Signal Definitions

MOD_DEF(0:2)

Pins 4, 5, and 6 define the data rate at which the installed GBIC is operating, as shown in the following table.

Module Pin Definitions

MOD_DEF(0:2) Serial ID Implementations

In the GBICs with serial ID, a two-wire serial EEPROM is used to hold 128 bytes of information that describe some of the capabilities, standard interfaces, manufacturer, and other information relevant to the product. The information stored in the EEPROM is protected so that it cannot be changed by the user. Tables describing the specific addresses and values of the serial ID data are included in Serial ID Data and Descriptions on page 22. Operation of the serial ID function is described in Serial Module Definition Protocol (Serial ID) on page 10. Signal timings necessary for proper operation of the serial ID function are shown in Serial ID Timing Specifications on page 21.

The serial ID module requires both serial clock (SCL) and serial data I/O (SDA) connections. These signals are required to have pull up resistors (4.7 kΩ is the recommended value; however, a smaller value may be needed in order to meet the serial ID's rise and fall time requirements). The following list and figure show the necessary connections from an interface to a GBIC to ensure the capability of reading the serial ID data.

MOD_DEF(0): Logic Low MOD_DEF(1): SCL MOD_DEF(2): SDA

The serial clock (SCL) and the serial data (SDA) lines appear as NC to the host system upon initial power up.

Expected Connections to GBIC MOD_DEF Pins

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Rx_DAT

The incoming optical signal is converted and repowered as an AC coupled differential PECL serial data stream.

Rx_LOS

The Receive Loss of Signal line is high (a logical one) when there is no incoming light from the companion transceiver. (More accurately, this line indicates that the level of light is below that required to guarantee correct operation of the link. Normally, this only occurs when either the link is unplugged or the companion transceiver is turned off.) This signal is normally used by the system for diagnostic purposes. The timing is shown in the Receive Loss of Signal Detection diagram below.

This signal has an open collector TTL driver. A pull up resistor is required on the host side of the GBIC connector. The recommended value for this resistor is 10 kΩ.

Tx_Fault

Upon sensing an improper power level in the laser driver, the GBIC sets this signal high and turns off the laser. The Tx_Fault signal can be reset with the Tx_Disable line.

The laser is turned off within 100 μs as shown in the Transmitter Fault Detection timing diagram below.

This signal has an open collector TTL driver. A pull up resistor is required on the host side of the GBIC connector. The recommended value for this resistor is 10 kΩ.

Output Signal Timings

Input Signal Definitions

Tx_DAT

A differential PECL serial data stream is presented to the GBIC for transmission onto the optical fiber by intensity modulating a laser.

Tx_Disable

When high (logic one), the Tx_Disable signal turns off the power to both the AC and DC laser driver circuits. It will also reset the Tx_Fault output under some conditions (see Resetting a Fault (Tx_Fault) on page 8).

When low (logic zero), the laser will be turned on within 1ms if a hard fault is not detected. The timing diagram below shows this line under normal operating conditions.

Timing of Tx_Disable function

Power On Initialization Timings

Resetting a Fault (Tx_Fault)

Resetting the Tx_Fault output by toggling the Tx_Disable input permits the GBIC to attempt to power on the laser following a fault condition. Continuous resetting and repowering of the laser under a hard fault condition could cause a series of optical pulses with sufficient energy to violate laser safety standards. To alleviate this possibility, the GBIC will turn off the laser and lock the Tx_Fault line high if a second fault is detected within 25 ms of the laser powering on. This lock is cleared during each power on cycle.

Fault Condition Recovery Timings

Operation

Link Acquisition Sequence

The following sequence should be followed to get a GBIC in full synchronization with a companion card undergoing a similar sequence. It will also work with a single card when using an optical wrap connector. This sequence assumes the use of an industry standard 10b Ser/Des chip.

- 1. Power up the node. The clock to the 10b chip should be running.
- 2. Drive the Transmit Data lines to 0101010101. (This speeds up the synchronization process and assures that the Comma Detect line on the 10b chip will not pulse randomly on the companion card during the remainder of the sequence.)
- 3. Drive the input control lines as follows:
	- a. Enable Wrap (10b chip): low (will not be changed)
	- b. Enable Comma Detect (10b chip): high (will not be changed)
	- c. Lock to Reference (10b chip): high
- 4. After the laser has come on, bring Lock to Reference low for at least 500μs.
- 5. Bring the -Lock to Reference high.
- 6. After 2500 bit times (2.4μs), the link should be in bit synchronization (the internal clocks are aligned to the incoming bit stream), but not yet byte synchronization (the byte is aligned along the same boundary it had when sent from the companion system to the GBIC prior to serialization). The Receive Byte Clock (10b chip) frequency should now be running at 0.1 times the bit rate and the Comma Detect line is ready to indicate reception of the Comma Character.
- 7. Drive the Transmit Data lines with a K28.5 (Byte Sync) character.

As soon as the 10b chip receives the K28.5 character from the other side of the link, the clocks will align to the byte boundary and all the Receive Data lines will have valid data. This will be indicated by the activation of the Comma Detect line.

Troubleshooting: What If ...

The laser never comes on (the Tx_Fault signal is either high or low):

- Verify 5 volts on the connector to the GBIC and that the module is correctly plugged.
- If the Tx Fault line is high, try either unplugging and replugging or powering down the module to reset the Tx_Fault line (see "Resetting a Fault (Tx_Fault)" on page 8).
- Try another GBIC. If the replacement operates correctly then retry the original. If the original still fails, it is probably defective.
- If the replacement fails also, verify that Tx_Disable is low and that it toggles correctly on the connector.

The Rx_LOS signal remains high:

- Verify 5 volts on the connector to the GBIC and that the module is correctly plugged.
- Verify the level on pin 1 of the connector. If the level is correct, there might be a discontinuity on the host board.
- Try using a wrap connector or a simplex jumper to loop the transmitter to the receiver. If the Rx_LOS line goes low, the source of the optical signal or the link may be defective. Use an optical power meter to check the optical power level. If the average optical power is within specification $(> -17$ dBm for shortwave devices), then the GBIC might be faulty.

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Serial Module Definition Protocol (Serial ID)

The GBICs with serial ID have identifying information stored in a Serial ID EEPROM. To read the serial data from the serial ID module, the following must occur (refer to Serial ID Figures 1, 2, and 3 throughout these steps):

1. Send a start sequence to the module.

This is done by changing the data line from high to low while the clock is high.

2. Send the set data address sequence.

The set data address sequence is 10100000. This sequence will allow the user to set the memory address to start reading from.

Note: Be sure to toggle the data line only when the clock is low. Toggling the data line while the clock is high indicates a start or stop condition.

3. Receive an acknowledge signal.

One zero bit is the acknowledge signal.

4. Send the address of the first byte to read.

The most significant bit of the address byte is the first bit and is ignored.

- 5. Receive an acknowledge signal.
- 6. Send a start command.
- 7. Send the read data sequence.

The read data sequence is 10100001. This sequence will allow the user to begin reading the data.

- 8. Receive an acknowledge signal.
- 9. Read a data word.
- 10. Send an acknowledge signal to receive the next data word or send a stop command to stop receiving data.

A stop command is given by toggling the data from low to high while the clock is high.

The critical timings for communicating to the serial ID EEPROM are shown in Serial ID Figure 4 on page 11.

For more information on the Serial ID protocol, see Serial ID Data and Descriptions on page 22.

Serial ID Figure 2 Set Data Address Sequence Timing

Serial ID Figure 3 Read Data Sequence Timing

Serial ID Figure 4 Critical Timings for GBICs with serial ID Parameters are defined in Serial ID Timing Specifications on page 21.

Absolute Maximum Ratings

1. Stresses listed may be applied one at a time without causing permanent damage. Functionality at or above the values listed is not implied. Exposure to these values for extended periods may affect reliability.

2. Non-condensing environment.

Specified Operating Conditions

2. Non-condensing environment.

Electrical Characteristics - Power Supply

Transmit Signal Interface from host to GBIC

1. At 150 Ω, differential, pk-pk. The figure below shows the simplified circuit schematic for the GBIC high-speed differential input lines.

- 2. Deterministic jitter (DJ) and total jitter (TJ) values are measured according to those defined in the Fibre-Channel Jitter Methodology Technical Report.
- 3. Rise and fall times are measured from 20 to 80%, with a 150 Ω differential termination.

Receive Signal Interface from GBIC to host

1. At 150 Ω, differential, pk-pk. The figure below shows the simplified circuit schematic for the GBIC high-speed differential output lines.

2. Deterministic jitter (DJ) and total jitter (TJ) values are measured according to those defined in Fibre-Channel Jitter Methodology Technical Report. Jitter values at the output assume worst case jitter values at its input.

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Control Electrical Interface

1. A 4.7-10 k Ω pull-up resistor to $V_{DD}T$ is required.

2. A 10 kΩ pull-up resistor to $\mathsf{V}_{\mathsf{DD}}\mathsf{T}$ is present on the GBIC (-1mA max).

3. See "Tx_Disable" on page 7.

4. See "Resetting a Fault (Tx_Fault)" on page 8.

5. See "Tx_Fault" on page 6 and "Tx_Disable" on page 7 for additional timing information.

6. See "Rx_LOS" on page 6 for timing relations.

Optical Characteristics Short Wavelength

Notes for Short Wavelength Optical Characteristics

- 1. Launched optical power is measured at the end of a two meter section of a 50/125μm fiber for the shortwave GBICs, and a 9/125μm fiber for the longwave GBICs. The maximum and minimum of the allowed range of average transmitter power coupled into the fiber are worst case values to account for manufacturing variances, drift due to temperature variations, and aging effects.
- 2. Optical rise time is determined by measuring the 20% to 80% response of average maximum values using an oscilloscope and 4th order Bessel Thompson filter having a 3 dB bandwidth of 0.75•nominal baud rate. The measurement is corrected to the full bandwidth value. Optical fall times are measured using a 6 GHz photodetector followed by a 22 GHz sampling oscilloscope. No corrections due to filtering or system bandwidth limitations are made on the measured value.
- 3. Extinction Ratio is the ratio of the average optical power (in dB) in a logic level one to the average optical power in a logic level zero measured under fully modulated conditions with a pattern of five 1s followed by five 0s, in the presence of worst case reflections.
- 4. RIN_{12} is the laser noise, integrated over a specified bandwidth, measured relative to average optical power with 12 dB return loss. Deterministic Jitter is measured as the peak-to-peak timing variation of the 50% optical signal crossings when transmitting repetitive K28.5 characters.
- 5. Coupled Power Ratio is the ratio of average power coupled into a multimode fiber to the average power coupled into a single mode fiber. The single mode fiber should be single mode at the wavelength of interest. This measurement is defined in EIA/TIA-526-14A. Additionally, the values shall be time averaged while the multimode test jumper is shaken and bent to simulate temperature and time variations of the laser.
- 6. The minimum and maximum values of the average received power in dBm allow the input power range to maintain a BER < 10⁻¹² when the data is sampled in the center of the receiver eye. These values take into account power penalties caused by the use of a laser transmitter with a worst-case combination of spectral width, extinction ratio, and pulse shape characteristics.
- 7. The Rx_LOS has hysteresis to minimize "chatter" on the output line. In principle, hysteresis alone does not guarantee chatter-free operation. These GBICs, however, present an Rx_LOS line without chatter, where chatter is defined as a transient response having a voltage level of greater than 0.5 volts (in the case of going from the negate level to the assert level) and of any duration that can be sensed by the host logic.

Optical Characteristics Long Wavelength

Notes for Long Wavelength Optical Characteristics

- 1. Launched optical power is measured at the end of a two meter section of a 50/125μm fiber for the shortwave GBICs and a 9/125μm fiber for the longwave GBICs). The maximum and minimum of the allowed range of average transmitter power coupled into the fiber are worst case values to account for manufacturing variances, drift due to temperature variations, and aging effects.
- 2. Extinction Ratio is the ratio of the average optical power (in dB) in a logic level one to the average optical power in a logic level zero measured under fully modulated conditions with a pattern of five 1s followed by five 0s, in the presence of worst case reflections.
- 3. RIN_{12} is the laser noise, integrated over a specified bandwidth, measured relative to average optical power with 12 dB return loss.

Eye opening is the portion of the bit time where the bit error rate (BER) is $\leq 10^{-12}$. The general laser transmitter pulse shape characteristics are specified in the form of a mask of the transmitter eye diagram. These characteristics include pulse overshoot, pulse undershoot, and ringing, all of which should be controlled to prevent excessive degradation of the receiver sensitivity. When assessing the transmit signal, it is important to consider not only the eye opening, but also the overshoot and undershoot limitations.

- 4. Optical rise time is determined by measuring the 20% to 80% response of average maximum values using an oscilloscope and 4th order Bessel Thompson filter having a 3 dB bandwidth of 0.75 times the nominal baud rate. The measurement is corrected to the full bandwidth value. Optical fall times are measured using a 6 GHz photodetector followed by a 22 GHz sampling oscilloscope. No corrections due to filtering or system bandwidth limitations are made on the measured value.
- 5. The minimum and maximum values of the average received power in dBm allow the input power range to maintain a BER $< 10^{-12}$ when the data is sampled in the center of the receiver eye. These values take into account power penalties caused by the use of a laser transmitter with a worst-case combination of spectral width, extinction ratio, and pulse shape characteristics.
- 6. The Rx_LOS has hysteresis to minimize "chatter" on the output line. In principle, hysteresis alone does not guarantee chatter-free operation. These GBICs, however, present an Rx_LOS line without chatter, where chatter is defined as a transient response having a voltage level of greater than 0.5 volts (in the case of going from the negate level to the assert level) and of any duration that can be sensed by the host logic.

Optical Cable/Connector (Part 1 of 2)

1. Operation of 1310nm lasers on multimode fiber require the use of a Mode Conditioning Patch Cord to ensure compliance with IEEE P802.3 Gigabit Ethernet 1000BASE-LX. This patch cord will minimize the effects of Differential Mode Delay (DMD) and ensure the proper Coupled Power Ratio (CPR) for operation of 1310nm lasers over multimode fiber.

2. The optical interface connector dimensionally conforms to the industry standard SC type connector documented in JIS-5973. A dual keyed SC receptacle serves to align the optical transmission fiber mechanically to the shortwave GBICs. See Duplex SC Receptacle on page 27 for a drawing of the duplex SC receptacle that is part of the shortwave GBICs.

Optical Cable/Connector (Part 2 of 2)

1. Operation of 1310nm lasers on multimode fiber require the use of a Mode Conditioning Patch Cord to ensure compliance with IEEE P802.3 Gigabit Ethernet 1000BASE-LX. This patch cord will minimize the effects of Differential Mode Delay (DMD) and ensure the proper Coupled Power Ratio (CPR) for operation of 1310nm lasers over multimode fiber.

2. The optical interface connector dimensionally conforms to the industry standard SC type connector documented in JIS-5973. A dual keyed SC receptacle serves to align the optical transmission fiber mechanically to the shortwave GBICs. See Duplex SC Receptacle on page 27 for a drawing of the duplex SC receptacle that is part of the shortwave GBICs.

Thermal Characteristics

1. To meet the specified operating temperature, the ambient temperature of the air moving over the shortwave GBICs, and also the longwave GBICs should not exceed these values.

Reliability Projections

Serial ID Timing Specifications GBICs with serial ID only

1. See Serial ID Figure 4 on page 11 for timing relationships. See Serial Module Definition Protocol (Serial ID) on page 10 and Serial ID Data and Descriptions on page 22 for more information on Serial ID implementation.

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Serial ID Data and Descriptions

The Serial ID tables on the following pages contain specific information about the data contained within the Serial ID EEPROM. EEPROM. Tables 5 and 6 list actual Serial ID Data for the shortwave and longwave products, respectively.

All ID information is stored in eight-bit parameters addressed from 00h to 7Fh. All numeric information fields have the lowest address in the memory space storing the highest order byte. The highest order bit is always transmitted first. All numeric fields will be padded on the left with zeros. All character strings are ordered with the first character to be displayed located in the lowest address of the memory space. All character strings will be padded on the right with ASCII spaces (20h) to fill empty bytes.

Check Codes

The check codes contained within the identification data are one byte codes that can be used to verify that the data in previous addresses is valid. CCID check code is the lower eight bits of the sum of the contents of bytes 0-62. CCEX check code is the lower eight bits of the sum of the contents of bytes 64-94.

Serial ID Table 5 Serial ID Data Entries for JGB-12SYAA1

Serial ID Table 6 Serial ID Data Entries for JGB-12LYAA1

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Mechanical Description

Mechanical Outline

Two optical receptacles are at the end of the module. They are spaced 12.7mm apart to accept a standard duplex SC connector.

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System Board Considerations

The GBICs are intended to be used on a host card having a nominal thickness of 0.062" or 0.100" (see below for mating connector options). The host card footprint with essential keepouts and drill holes is shown on page 28.

Connector Availability

The connector used by these GBICs is a 20-pin model of the AMP SCA-2 connector. The following part numbers are available to provide mating connections:

Duplex SC Receptacle

Host Card Footprint

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References

Standards

1. IEEE 802.3. Drafts of this standard are available to members of the standards working committee. For further information see IEEE 802.3 public reflector at stds-802-3-hssg@mail.ieee.org. To be added to the reflector, send an E-mail to:

majordomo@mail.ieee.org

containing the line: **subscribe stds-802-3-hssg <***your email address***>**

The ftp site is **ftp://stdsbbs.ieee.org/pub/802_main/802.3/gigabit**

Industry Specifications

- 2. Gigabit Interface Converter specification, Revision 5.4 (GBIC V5.4). This document may be downloaded under anonymous ftp from: playground.sun.com. It is in the directory pub/OEmod.
- 3. A.X. Widmer and P.A. Franaszek, "A DC-Balanced, Partitioned-Block, 8B/10B Transmission Code," *IBM Journal of Research and Development*, vol. 27, no. 5, pp. 440-451, September 1983. This paper fully defines the 8B/10B code. It is primarily theoretical.

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