

# Memory Module Specification

## KVR667D2S5/512 512MB 64M x 64-Bit DDR2-667 CL5 200-Pin SODIMM

#### **DESCRIPTION:**

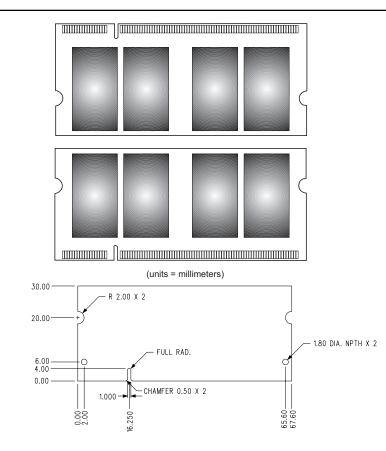
This document describes ValueRAM's 64M x 64-bit (512MB) CL5 DDR2-667 SDRAM (Synchronous **DRAM**) memory module. The components on this module include eight 64M x 8-bit (16M x 8-bit x 4 Bank) DDR2-667 SDRAM in FBGA packages. This 200-pin SODIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

### FEATURES:

- $\checkmark$  Clock Cycle Time (tCK) CL=5
- $\checkmark$  Row Cycle Time (tRC)
- ☑ Refresh to Active/Refresh Command Time (tRFC)
- $\checkmark$  Row Active Time (tRAS)
- ☑ Single Power Supply of
- Dever 2
- **UL** Rating

3ns (min.) / 8ns (max.) 54ns (min.) 105ns 39ns (min.) / 70,000ns (max.) +1.8V (+/- .1V) 1.595 W (operating) 94 V - 0

**Note:** The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 30mm.



#### Figure 1: KVR667D2S5/512 200-Pin SODIMM

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