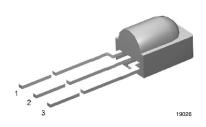


Vishay Semiconductors

COMPLIANT

IR Receiver Modules for Remote Control Systems



MECHANICAL DATA

Pinning:

 $1 = OUT, 2 = GND, 3 = V_S$

FEATURES

- · Low supply current
- Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Improved shielding against EMI
- Supply voltage: 2.7 V to 5.5 V
- Improved immunity against ambient light
- Insensitive to supply voltage ripple and noise
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

DESCRIPTION

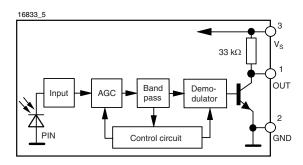
The TSOP582.., TSOP584.. series are miniaturized receivers for infrared remote control systems. A PIN diode and a preamplifier are assembled on a lead frame, the epoxy package acts as an IR filter.

The demodulated output signal can be directly decoded by a microprocessor. The TSOP582.. is compatible with all common IR remote control data formats. The TSOP584.. is optimized to suppress almost all spurious pulses from energy saving fluorescent lamps but will also suppress some data signals.

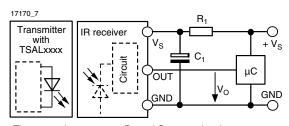
This component has not been qualified according to automotive specifications.

PARTS TABLE				
CARRIER FREQUENCY	STANDARD APPLICATIONS (AGC2/AGC8)	VERY NOISY ENVIROMENTS (AGC4)		
30 kHz	TSOP58230	TSOP58430		
33 kHz	TSOP58233	TSOP58433		
36 kHz	TSOP58236	TSOP58436		
36.7 kHz	TSOP58237	TSOP58437		
38 kHz	TSOP58238	TSOP58438		
40 kHz	TSOP58240	TSOP58440		
56 kHz	TSOP58256	TSOP58456		

BLOCK DIAGRAM



APPLICATION CIRCUIT



The external components R_1 and C_1 are optional to improve the robustness against electrical overstress (typical values are $R_1=100~\Omega,~C_1=0.1~\mu F).$ The output voltage V_0 should not be pulled down to a level below 1 V by the external circuit.

The capacitive load at the output should be less than 2 nF.

Document Number: 81221 Rev. 1.6, 20-Aug-10

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Supply voltage (pin 3)		V _S	- 0.3 to + 6	V		
Supply current (pin 3)		I _S	5	mA		
Output voltage (pin 1)		Vo	- 0.3 to 5.5	V		
Voltage at output to supply		V _S - V _O	- 0.3 to (V _S + 0.3)	V		
Output current (pin 1)		I _O	5	mA		
Junction temperature		T _j	100	°C		
Storage temperature range		T _{stg}	- 25 to + 85	°C		
Operating temperature range		T _{amb}	- 25 to + 85	°C		
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW		
Soldering temperature	t ≤ 10 s, 1 mm from case	T _{sd}	260	°C		

Note

• Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPTICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply current (pin 3)	$E_{V} = 0, V_{S} = 5 V$	I _{SD}	0.65	0.85	1.05	mA
	E _v = 40 klx, sunlight	I _{SH}		0.95		mA
Supply voltage		Vs	2.7		5.5	V
Transmission distance	$E_{V}=0$, test signal see fig. 1, IR diode TSAL6200, $I_{F}=400\ \text{mA}$	d		40		m
Output voltage low (pin 1)	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see fig. 1	V _{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see fig. 1	E _{e min.}		0.3	0.45	mW/m²
Maximum irradiance	t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see fig. 1	E _{e max.}	30			W/m ²
Directivity	Angle of half transmission distance	Ψ1/2		± 45		deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

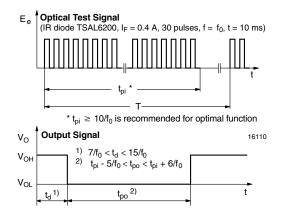


Fig. 1 - Output Active Low

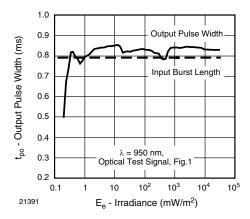
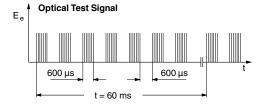


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient



IR Receiver Modules for Remote Control Systems

Vishay Semiconductors



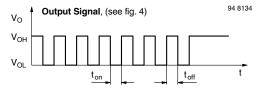


Fig. 3 - Output Function

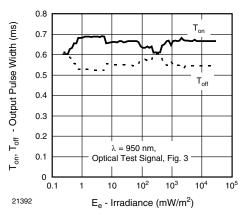


Fig. 4 - Output Pulse Diagram

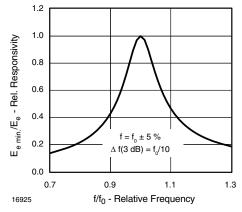


Fig. 5 - Frequency Dependence of Responsivity

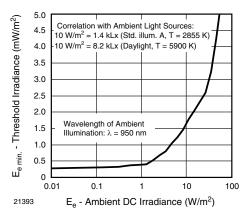
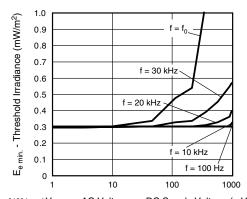


Fig. 6 - Sensitivity in Bright Ambient



 $_{
m 21394}$ $_{
m \Delta Vs_{RMS}}$ - AC Voltage on DC Supply Voltage (mV)

Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

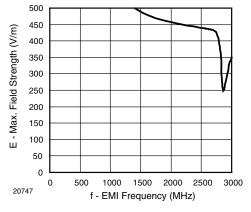


Fig. 8 - Sensitivity vs. Electric Field Disturbances

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



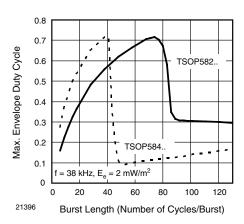


Fig. 9 - Max. Envelope Duty Cycle vs. Burst Length

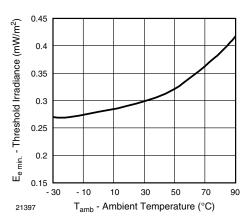


Fig. 10 - Sensitivity vs. Ambient Temperature

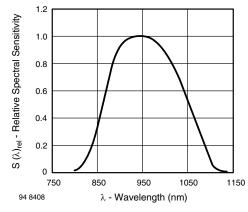


Fig. 11 - Relative Spectral Sensitivity vs. Wavelength

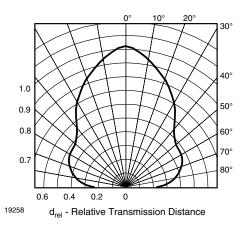


Fig. 12 - Horizontal Directivity

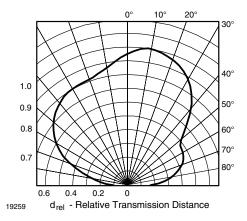


Fig. 13 - Vertical Directivity

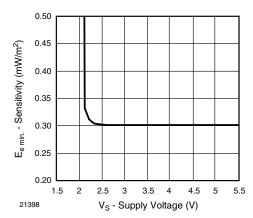


Fig. 14 - Sensitivity vs. Supply Voltage



IR Receiver Modules for Remote Control Systems

Vishay Semiconductors

SUITABLE DATA FORMAT

The TSOP582.., TSOP584.. series are designed to suppress spurious output pulses due to noise or disturbance signals. Data and disturbance signals can be distinguished by the devices according to carrier frequency, burst length and envelope duty cycle. The data signal should be close to the band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP582.., TSOP584.. in the presence of a disturbance signal, the sensitivity of the receiver is reduced to insure that no spurious pulses are present at the output. Some examples of disturbance signals which are suppressed are:

- DC light (e.g. from tungsten bulb or sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated noise from fluorescent lamps with electronic ballasts (see figure 15 or figure 16)

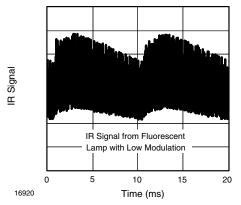


Fig. 15 - IR Signal from Fluorescent Lamp with Low Modulation

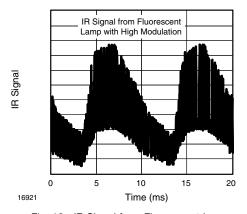


Fig. 16 - IR Signal from Fluorescent Lamp with High Modulation

	TSOP582	TSOP584
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 12 cycles	10 to 35 cycles ≥ 12 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	800	1300
Recommended for NEC code	yes	yes
Recommended for RC5/RC6 code	yes	yes
Recommended for Sony code	yes	no
Recommended for Thomson 56 kHz code	yes	yes
Recommended for Mitsubishi code (38 kHz, preburst 8 ms, 16 bit)	yes	yes
Recommended for Sharp code	yes	yes
Suppression of interference from fluorescent lamps	Most common disturbance signals are suppressed	Even extreme disturbance signals are suppressed

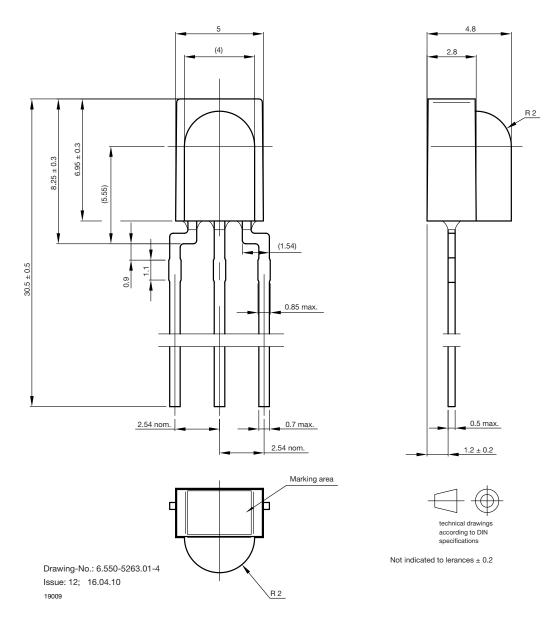
Note

For data formats with short bursts please see the datasheet of TSOP581.., TSOP583.. .

Vishay Semiconductors IR Receiver Modules for Remote **Control Systems**



PACKAGE DIMENSIONS in millimeters







Vishay

Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.

Revision: 18-Jul-08

Document Number: 91000 www.vishay.com