

5V Highly-Integrated, Digital Dimming CCFL Inverter Module

PRODUCTION DATASHEET

DESCRIPTION

RangeMAX Wide Range Dimming, Single Output Inverter. The LXM1612-05-xx series of Direct Drive[™] CCFL (Cold Cathode Fluorescent Lamp) Inverter Modules are specifically designed for driving LCD backlight lamps.

RangeMAX Digital Dimming Technique. Digital Dimming provides flicker-free brightness control in any wide-range dimming application.

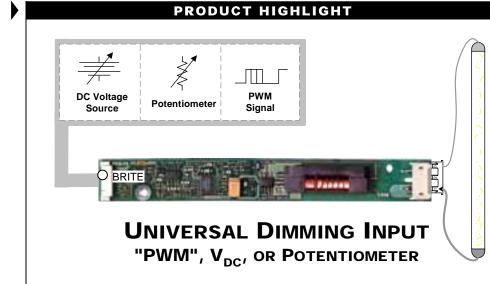
The modules are equipped with a dimming input that permits brightness control from an external potentiometer, DC voltage source, or PWM signal. The resultant "burst drive" that energizes the lamp was designed specifically to ensure that no premature lamp degradation occurs.

Input Voltage Range: The modules convert DC voltage from the system battery or AC adapter directly to highfrequency, high-voltage waves required to ignite and operate CCFL lamps. A 12V input inverter is also available (LXM1612-12-xx)

Direct Drive Technology: The module design is based on a new Direct Drive topology, which provides a number of cost and performance ad-vantages.

Additional Feature: Other benefits of this new topology are fixed-frequency operation and secondary-side strikevoltage regulation and all LXM1612-05xx modules feature both open and shorted lamp protection.

IMPORTANT: For the most current data, consult *MICROSEMI*'s website: <u>http://www.microsemi.com</u> Protected by U.S. Patents: 5,923,129; 5,930,121; Patents Pending



KEY FEATURES

- RangeMAX Wide Range Dimming
- Single 5V Input Voltage
 Fully Integrated Direct Drive And Dimming Control
- Single Sided PCB Layout
- Easy To Use Brightness Control
- MicroAmp SLEEP Mode
 Output Short-Circuit Protection And Automatic Strike-Voltage Regulation
- Fixed Frequency Operation
- UL 60950 E175910

APPLICATIONS

- Notebook And Sub-Notebook Computers
- Portable Instrumentation
- Desktop Displays
- Low Ambient Light Conditions (i.e. Aircraft Cabins, Automobile)

BENEFITS

- Smooth, Flicker Free 1-100% Full-Range Brightness Control Gives Your Product A High Quality Image
- Output Open Circuit Voltage Regulation Minimizes Corona Discharge For Long Life And High Reliability
- Power Efficient. "Low Brightness" Capability Allows For Advanced Power Management

PACKAGE ORDER INFO					
PART NUMBER	OUTPUT CONNECTOR	INVERTER MATES DIRECTLY TO PANEL CONNECTORS			
LXM1612-05-01	JST SM02(8.0)B-BHS-1-TB or Yeon Ho 20015WR-05A00	BHR-03VS-1			
LXM1612-05-02	JST SM02B-BHSS-1-TB or Yeon Ho 35001WR-02A00	BHSR-02VS-1			
LXM1612-05-03	Honda QZ-19-A3MYL #02	Honda QZ-19-3F01			

Copyright © 2000 Rev. 1.0a, 2005-01-10

Microsemi Linfinity Microelectronics Division

11861 Western Avenue, Garden Grove, CA. 92841, 714-898-8121, Fax: 714-893-2570



5V Highly-Integrated, Digital Dimming CCFL Inverter Module

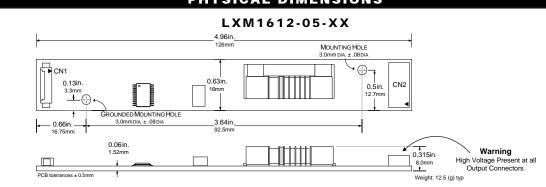
PRODUCTION DATASHEET

ABSOLUTE MAXIMUM RATINGS (NOTE 1)

Input Signal Voltage (V _{IN1})	-0.3V to 6.5V
Output Voltage, no load	
Output Current	
Output Power	6.0W
Input Signal Voltage (BRITE Input)	0.3V to V_{IN1} +0.3V
Input Signal Voltage (SLEEP, V _{SYNC} Inputs)	-0.3V to V_{IN1} +0.3V
Ambient Operating Temperature, zero airflow	0° C to 70° C
Storage Temperature Range	-40°C to 85°C

Note 1: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

		FUNCTIONAL PIN DESCRIPTION					
CONN	ΡιΝ	DESCRIPTION					
CN1 (Mole)	(53261-0890)	Mates with 51021-0800 housing, 50079-8100 pins					
CN1-1	V _{IN1}	Main Input Power Supply (4.5V \leq V _{IN1} \leq 5.5V					
CN1-2	VINT						
CN1-3	GND	Power Supply Return					
CN1-4	GND						
CN1-5	SLEEP	ON/OFF Control. (2.1V < $\overline{\text{SLEEP}}$ < V_{IN1} = ON, 0V < $\overline{\text{SLEEP}}$ < 0.8V = OFF, $\overline{\text{SLEEP}}$ Floating = OFF)					
CN1-6	BRITE	Brightness Control (0.0V to $2.5V_{DC}$). $2.5V_{DC}$ gives maximum lamp current.					
CN1-7	BRITE RTN	Analog Ground. Use for BRITE signal return ONLY.					
CN1-8	V _{SYNC}	Vertical Synchronization Input. 2.5 to 5.0V Logic Level. (20Hz < f _{SYNC} < 200Hz)					
CN2 for LX	(M1612-05-01	and -02 (JST SM02(8.0)B-BHS-1-TB Yeon Ho 20015WR-05A00 or SM02B-BHSS-1-TB Yeon Ho 35001WR-02A00)					
CN2-1	V _{HI}	High voltage connection to high Side of lamp. Connect to lamp terminal with shortest lead length. DO NOT connect to Ground.					
CN2-2	V _{LO}	Connection to low side of lamp. Connect to lamp terminal with longer lead length. DO NOT connect to Ground					
CN2 for LX	M1612-05-03	(Honda QZ-19-A3MYL#02)					
CN2-3	V _{HI}	High voltage connection to high side of lamp. Connect to lamp terminal with shortest lead length. DO NOT connect to Ground.					
CN2-1	V _{LO}	Connection to low side of lamp. Connect to lamp terminal with longer lead length. DO NOT connect to Ground					
		PHYSICAL DIMENSIONS					



Copyright © 2000 Rev. 1.0a, 2005-01-10 www.Microsemi.com



5V Highly-Integrated, Digital Dimming CCFL Inverter Module

PRODUCTION DATASHEET

RECOMMENDED OPERATING CONDITIONS (R.C.)

This module has been designed to operate over a wide range of input and output conditions. However, best efficiency and performance will be obtained if the module is operated under the condition listed in the 'R.C.' column. Min. and Max. columns indicate values beyond which the inverter, although operational, will not function optimally.

Parameter	Symbol	Recommended Operating Conditions			Units	
Falameter	Symbol	Min	R.C.	Max	Units	
Input Supply Voltage Range (Fully Regulated Lamp Current)	V _{IN1}	4.5	5	5.5	V	
Input Supply Voltage Range (Functional)		4.35	5	5.65		
Output Power	Po		3.5	4.0	W	
Brightness Control Input Voltage Range	V_{BRT_ADJ}	0.0	2.5	V _{IN}	V	
Lamp Operating Voltage	VLAMP	450	535	620	V _{RMS}	
Lamp Current (Full Brightness)	IOLAMP		5.8		mA _{RMS}	
Operating Ambient Temperature Range	T _A	0		70	°C	

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, the following specifications apply over the recommended operating condition and ambient temperature of 25° C except where otherwise noted.

Parameter	Symbol	Test Conditions	LXM1612-05-xx			Units
i arameter	Gymbol		Min	Тур	Max	Onits
OUTPUT PIN CHARACTERISTICS						
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ}=2.5V_{DC}$, SLEEP=HIGH, Burst Duty=100%, V _{IN1} =5V _{DC}	5.2	5.8	6.4	mA _{RM}
Min. Average Lamp Current (Note 2)	I _{L(MIN)}	$V_{BRT_{ADJ}}=0V_{DC}$, SLEEP=HIGH, Burst Duty=2.0%, $V_{IN1}=5V_{DC}$		0.12		mA _{R№}
Lamp Start Voltage	V_{LS}	$0^{\circ}C < T_{A} < 70^{\circ}C, V_{IN1} > 5.0V_{DC}$	1500	1650	1800	V _{RMS}
Operating Frequency	fo	$V_{BRT_{ADJ}}=2.5V_{DC}, \overline{SLEEP}=HIGH, V_{IN1}=5.0V$	63	68	73	kHz
BRITE INPUT						
Input Current		V _{BRT_ADJ} =0V _{DC}	-685	-728	-775	μA _{DC}
input Current	I _{BRT}	V _{BRT_ADJ} =2.5V _{DC}	-260	-282	-305	μA _{DC}
Input Voltage for Max. Lamp Current	V _{BRT_ADJ}	I _{O(LAMP)} =100% Duty Cycle	2.30	2.35	2.40	V _{DC}
Input Voltage for Min. Lamp Current	$V_{\text{BRT}_\text{ADJ}}$	I _{O(LAMP)} = Minimum Duty Cycle	0.0			V _{DC}
SLEEP INPUT						
RUN Mode	V _{SLEEP} (HI)	V _{IN1} =4.5V	1.9	2.1	V _{IN1}	V _{DC}
SLEEP Mode	V _{SLEEP} (LO)	V _{IN1} =5.5V	-5.0	1.9	2.0	V _{DC}
Input Current	·	SLEEP=5.0V	390	410	435	μA _D
input Current	SLEEP	SLEEP=0V	0.0	0.0	0.0	μA _{DC}
VSYNC CHARACTERISTICS						
	V _{SYNCH(HI)}	V _{IN1} =4.5V	1.50	2.5	5.0	V _{DC}
Logic High Level	V _{SYNCH(LO)}	V _{IN1} =5.5V	-3.0	0.0	1.2	V _{DC}
Input Impedance	Z _{IN}		25	27	29	KΩ
Input Frequency	f _{SYNC}		20		200	Hz
POWER CHARACTERISTICS						
Sleep Current		$V_{IN1}=5V_{DC}$, SLEEP=0.0V	0.0	0.5	10	μA _D

Copyright © 2000 Rev. 1.0a, 2005-01-10

Microsemi Linfinity Microelectronics Division

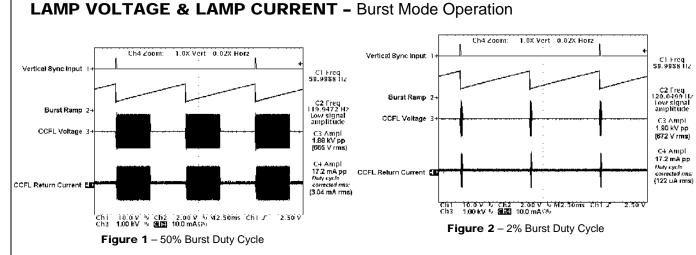
ELECTRICALS



5V Highly-Integrated, Digital Dimming CCFL Inverter Module

PRODUCTION DATASHEET

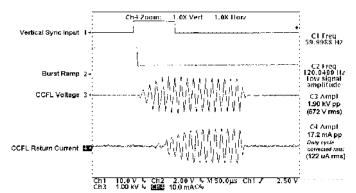
HOW RANGEMAX WORKS

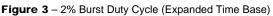


Rather than using the traditional dimming technique of varying lamp current magnitude to adjust light output, RangeMAX inverters use a fixed lamp current value with a duty cycle control method.

The lamp current burst width can be modulated from 100% (continuous lamp current) down to a 2% duty cycle, allowing the lamp to be dimmed to less than 1% of its full brightness.

As can be seen in Trace 4 of Figure 3 photo at right, careful design consideration was given to controlling lamp start voltage to softly start current flow. This eliminates current overshoot that can result in premature cathode wear and reduce lamp life.





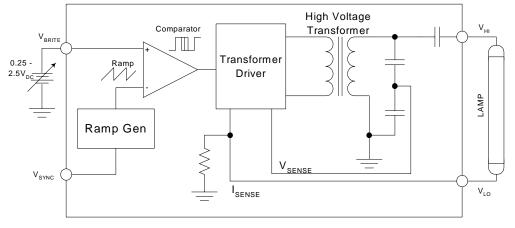


Figure 4 – RangeMAX Block Diagram

www.**Microsemi**.com



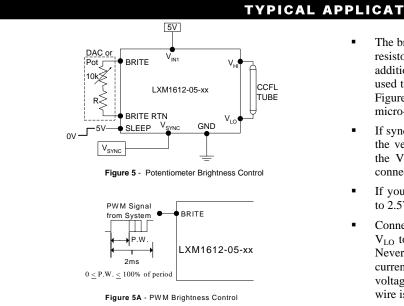
5V Highly-Integrated, Digital Dimming CCFL Inverter Module

PRODUCTION DATASHEET

HOW RANGEMAX WORKS (CONTINUED)

Highlights

- Integrated brightness control circuit includes a DC voltage to pulse width converter that minimizes system design work and system noise susceptibility. This provides a familiar and convenient interface while reducing the potential for externally induced noise, which can cause lamp flicker.
- RangeMAX inverter modules are designed to operate with the burst frequency synchronized to the video frame rate. This provides operation with no visible display disturbances caused by beat frequencies between the lamps and video frame rates. In this synchronous mode, the inverter burst rate operates at twice the video refresh rate, well beyond standard 50/60Hz video refresh rates where the eye can perceive pulsing light.
- In applications with no access to a vertical sync., an onboard oscillator operates the inverter burst rate at about 250Hz. In this non-synchronous mode, minor display disturbances can be found under certain video conditions. This performance mav be acceptable for many applications, but synchronization must be used when no disturbance can be tolerated.
- A single input will accommodate negative and positive vertical sync pulses at any pulse width.
- Separate feedback loops for lamp current and open circuit voltage regulation insure reliable strike under all operating conditions, automatic over-voltage prevention with broken or failed lamps, and accurate lamp current regulation.



TYPICAL APPLICATION

- The brightness control may be a voltage output DAC, digital resistor or a simple 10k potentiometer. If desired, an additional series resistor with the 10k potentiometer can be used to set a higher minimum brightness setting as shown in Figure 5. A 2.5V to 5V Logic Level PWM signal from a micro-controller may also be used as shown in Figure 5A.
- If synchronization to the video frame rate is desired, connect the vertical sync pulse from the system video controller to the V_{SYNC} input. If no video synchronization is desired, connect V_{SYNC} to ground.
- If you need to turn the inverter ON/OFF remotely, connect to 2.5V to 5V logic signal to the SLEEP input.
- Connect V_{HI} to high voltage wire from the lamp. Connect V_{LO} to the low voltage wire (wire with thinner insulation). Never connect V_{LO} to circuit ground as this will defeat lamp current regulation. If both lamp wires have heavy high voltage insulation, connect the longest wire to VLO. This wire is typically white.

LXM1612-05-XX OUTPUT CURRENT ADJUST

The LXM1612-05 output current can be adjusted by changing the value of two resistors on the PCB. The following table shows the approximate new output current values:

Output Current	R12	R16
7.0mA	2.00K 1%	Not Used
6.0mA	2.61K 1%	Not Used
5.8mA	2.74K 1%	Not Used
5.0mA	2.74K 1%	499K 1%
4.0mA	2.74K 1%	178K 1%
3.0mA	2.74K 1%	78.7K 1%

Copyright © 2000 Rev. 1.0a, 2005-01-10

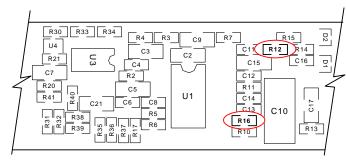


Figure 6- Component Replacement Locations

Page 5

DESCRIPTION



5V Highly-Integrated, Digital Dimming CCFL Inverter Module

PRODUCTION DATASHEET

NOTES

www.Microsemi.com

PRODUCTION DATA – Information contained in this document is proprietary to Microsemi and is current as of publication date. This document may not be modified in any way without the express written consent of Microsemi. Product processing does not necessarily include testing of all parameters. Microsemi reserves the right to change the configuration and performance of the product and to discontinue product at any time.

Copyright © 2000 Rev. 1.0a, 2005-01-10