

Series/Type: X6857D

The following products presented in this data sheet are being withdrawn.

Ordering Code	Substitute Product		Deadline Last Orders	Last Shipments
B39360X6857N201		2011-01-14	2011-09-30	2012-09-30

For further information please contact your nearest EPCOS sales office, which will also support you in selecting a suitable substitute. The addresses of our worldwide sales network are presented at www.epcos.com/sales.

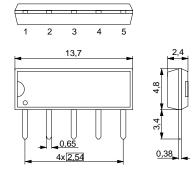


**Data Sheet** 

Duroplast package SIP5D

#### **Features**

- IF filter for digital TV
- Optimized for cascade of two devices
- Standard IC package



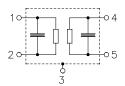
#### **Terminals**

■ Tinned CuFe alloy

Dimensions in mm, approx. weight 0,5 g

### Pin configuration

- 1 Input
- 2 Input ground
- 3 Chip carrier ground
- 4 Output
- 5 Output



Туре	Ordering code	Marking and package according to	Packing according to		
X 6857 D	B39360-X6857-N201	C61157-A1-A21	F61074-V8049-Z000		

### **Maximum ratings**

Operable temperature range	$T_{A}$	-25/+65	°C	
Storage temperature range	$T_{\rm stg}$	-40/+85	°C	
DC voltage	$V_{\rm DC}$	5	V	between any terminals
AC voltage	$V_{\sf pp}$	10	V	between any terminals



**Data Sheet** 

### Characteristics

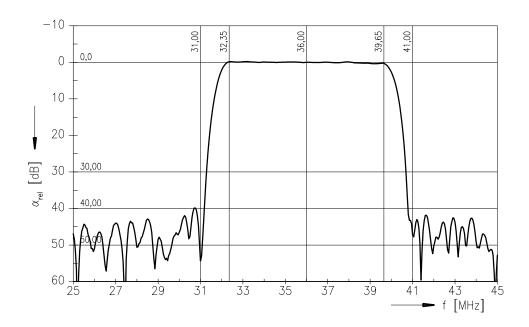
Reference temperature:  $T_{\rm A}=25\,^{\circ}{\rm C}$ Terminating source impedance:  $Z_{\rm S}=50\,\Omega$ Terminating load impedance:  $Z_{\rm L}=2\,{\rm k}\Omega\,||\,3\,{\rm pF}$ 

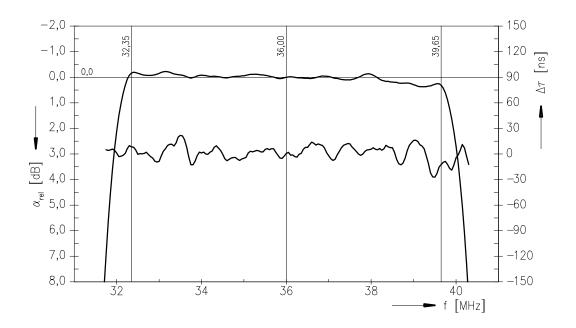
					min.	typ.	max.	
Insertion attenuation Reference level for the following data		36,00	MHz	α	19,0	20,5	22,0	dB
Amplitude ripple (p-p)	32,35	39,65	MHz	Δα	_	0,7	_	dB
Pass bandwidth								
$\alpha_{rel} \leq$ 1,5 dB				B <sub>1,5dB</sub>	_	7,8	_	MHz
$\alpha_{\text{rel}} \leq 3 \text{ dB}$				B <sub>3dB</sub>	_	8,1	_	MHz
$\alpha_{rel} \leq 15 \text{ dB}$				B <sub>15dB</sub>	_	8,9	_	MHz
$\alpha_{\text{rel}} \leq 30 \text{ dB}$				B <sub>30dB</sub>	_	9,4	_	MHz
Relative attenuation				$\alpha_{\rm rel}$				
		31,65	MHz	- Tei	7,0	10,0		dB
		40,35			7,0	10,0	_	dB
		31,30	MHz		22,0	29,0	_	dB
		40,70	MHz		22,0	29,0	_	dB
Lower sidelobe	25,00	31,00	MHz		36,0	40,0		
Upper sidelobe	41,00				36,0	41,0		
Reflected wave signal suppression 1,0 μs 6,0 μs after main pulse (test pulse 250 ns, carrier frequency 36,00 MHz)					42,0	52,0	_	dB
Feedthrough signal suppression 1,3 μs 1,2 μs before main pulse (test pulse 250 ns, carrier frequency 36,00 MHz)				_	50,0	_	dB	
Group delay ripple (p-p	-			Δτ				
	32,35	39,65	MHz			50		ns
Impedance at 36,00 MHz								
Input: $Z_{IN} = R_{IN} \parallel C_{IN}$				_	2,8    15,5	_	kΩ    pF	
Output: $Z_{OUT} = R_{OUT} \parallel C_{OUT}$					2,4    4,4		kΩ    pF	
Temperature coefficient of frequency			TC <sub>f</sub>		-72	_	ppm/K	



**Data Sheet** 

### Frequency response

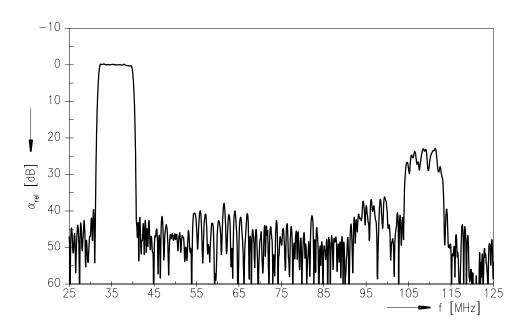




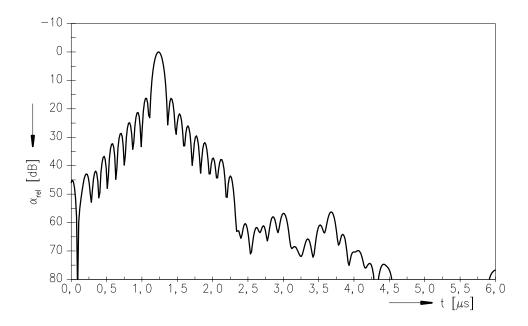


**Data Sheet** 

## Frequency response



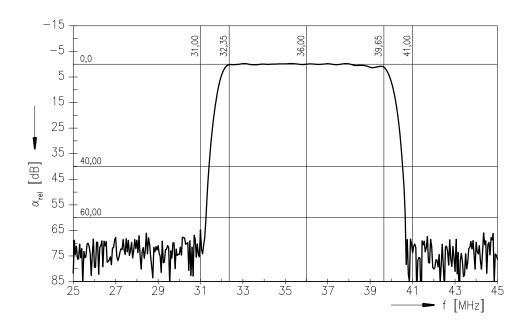
### Time domain response

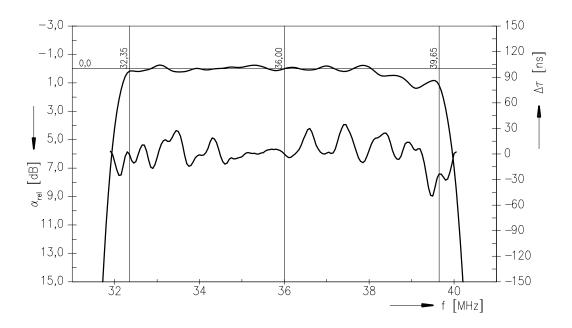




**Data Sheet** 

# Frequency response of two cascaded devices







**Data Sheet** 

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