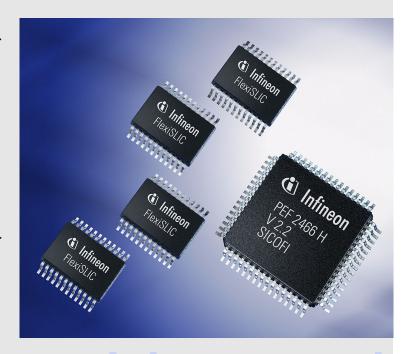
The SICOFI-4µC in combination with a member of the FlexiSLIC<sup>™</sup> family enables building cost effective linecards with centralized ringing.

The FlexiSLIC™ offers pin and control compatible devices specialized for customer requirements, while minimizing board space and reducing the number of external components.

With seamless integration between the SICOFI-4 $\mu$ C and the FlexiSLIC<sup>TM</sup>, and with fully programmable analog line functions, the SICOFI-4 $\mu$ C delivers country independent solutions with a single PCB design.

Generic linecards designed with the SICOFI-4µC and the FlexiSLIC™ give system designers significant economies of scale since one card is easily adapted to many different markets and applications.



### **Applications**

 Analog linecards with centralized ring generator for balanced and unbalanced ringing

#### **Features**

- Country independent linecard design with a single PCB layout
- 4-channel Codec
- Three SLIC versions with the same pin out
- Compliant to ITU-T Q.552, G.712, LSSGR
- Software programmable gains frequency response, trans-hybrid balance, and impedance matching

- Metering up to 4.0 V<sub>ms</sub> supported and TTX filter integrated
- Two PCM interfaces deliver up to 8 Mbit/s
- Control via a single serial controller interface (SCI/SPI)
- Programmable feed current up to 45 mA
- Two battery voltages for on-hook and off-hook with automatic battery switch
- Silent battery switching
- Constant current and resistive DC feeding
- Ground key and ground start supported

- Off-hook detection with programmable de-bounce time
- Line voltage measurement to determine line length or short circuits
- Level metering and loops support diagnostic tests
- Small number of operating states eases software development
- Very low power dissipation
- Small footprint SSOP packages
- Industrial temperature range available
- EASY 2466 and TB 208 evaluation boards are available

## SICOFI® + FlexiSLIC™

Cost Effective Line Cards
PEB 2466
PBL 38620, PBL 38621, PBL 38650

Infineon

Never stop thinking

Never stop thinking.

Chip Set	PEB 2466, PBL 38620	PEB 2466, PBL 38621	PEB 2466, PBL 38650
Application	Basic linecard	Line card with polarity reversal	Extended linecard
Maximum feed current	30 mA	30 mA	45 mA
Polarity reversal	No	Yes	Yes
Metering	Low level	Low level	Up to 4.0 V <sub>ms</sub>
Ground Key	Yes	Yes	Yes
Ground Start	No	No	Yes
Line voltage	No	No	Yes
measurement			
Ring relay driver	Integrated	Integrated	Integrated
Automatic battery switching	Yes	Yes	Yes

# Overview of SLIC Features

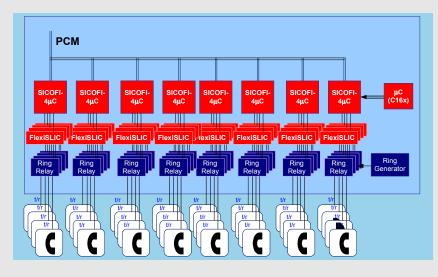
## **Ordering Information**

### **Design Tools**

Туре	Sales Code	Package	Description
SICOFI	EASY 2466	Evaluation package	four-channel SICOFI-4µC TE board
FlexiSLIC	TB 208	Evaluation package	single-channel FlexiSLIC board

## Chip Set

Туре	Sales Code	Package	Description
SICOFI-4µC	PEB 2466	P-MQFP-64	SICOFI-4µC single-chip CMOS
FlexiSLIC	PBL 38620	SSOP-24	FlexiSLIC for basic linecard
FlexiSLIC	PBL 38621	SSOP-24	FlexiSLIC for linecard with polarity reversal
FlexiSLIC	PBL 38650	SSOP-24	FlexiSLIC for extended linecard functionality



## 32-Channel Line Card

High density linecard with centralized ring generator

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