

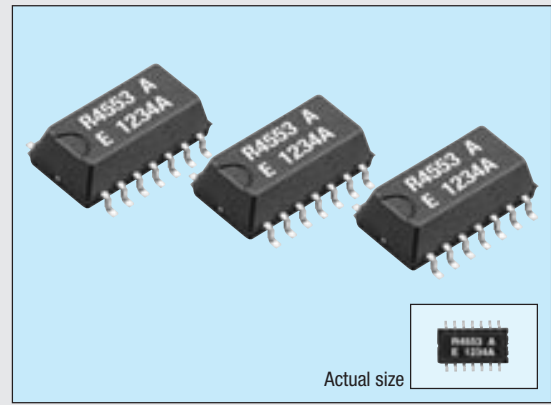
SERIAL-INTERFACE REAL TIME CLOCK MODULE WITH SRAM

RTC-4553

Product number (please refer to page 5)

Q4145535xxxxx00

- Built-in 32.768 kHz crystal oscillator with high accuracy.
- Dual Alarm and Timer IRQ function are Available.
- 32.768 kHz clock frequency output. (Nch open drain)
- Low backup current : 0.48 μ A / 3 V (Typ.)
- Wide operating voltage range : 1.7 V to 5.5 V
- Wide timekeeper voltage range : 1.15 V to 5.5 V
- CPU interrupt generation function (cycle time range : 1 month to 0.5 seconds, includes interrupt flags and interrupt syop function)
- Oscillation stop detection function (used to determine presence of internal data)
- Power supply voltage monitoring function (with selectable detection threshold)
- Available for lead (Pb) - free soldering.
- Available for lead (Pb) - free treminal.



The details are mentioned in the application manual.

<http://www.epsondevice.com>

Specifications (characteristics)

Absolute Max. rating

Item	Symbol	Condition	Min.	Max.	Unit
Supply voltage	VDD	VDD to GND		+6.0	V
Input voltage	VIN	SIN, SCK, WR, CS0, CS1	-0.3	VDD +0.3	
Output voltage	VOU	SOUT, TPOUT			
Storage temperature	TSTG	Stored as bare product after unpacking	-55	+125	°C

Operating range

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power voltage	VDD	—	2.7	5.0	5.5	V
Clock voltage	VCLK	—	2.0	—	5.5	V
Operating temperature	VOPR	No condensation	-30	—	+70	°C

Frequency characteristics

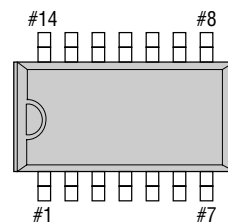
Item	Symbol	Condition	Range	Unit	
Frequency tolerance	$\Delta f/f$	Ta = +25 °C, VDD = 5 V	AA	5 \pm 5	x 10 ⁻⁶
			A	5 \pm 10	
			B	5 \pm 20	
Oscillation start up time	tSTA	Ta = +25 °C, VDD = 3.0 V	3.0 Max.	s	
Frequency temperature characteristics	Top	Ta = -10 °C to +70 °C, VDD = 5 V Reference at +25 °C	+10 -120	x 10 ⁻⁶	
Frequency voltage characteristics	f/V	Ta = Fix, VDD = 2 V to 5.5 V Reference at 5 V	\pm 5		
Aging	fa	Ta = +25 °C, VDD = 5 V, first year		x 10 ⁶ / year	

DC characteristics (GND = 0 V, VDD = 5 V \pm 10 %, Ta = -30 °C to +70 °C)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Current consumption	IDD1	SCK = 500 kHz	—	—	100	μ A
	IDD2	SCK = 0 Hz	—	1.0	3.0	
Output voltage	VOH	IOH = -400 μ A	VDD -0.4	—	—	V
	VOL	IOL = 1.6 mA	—	—	0.4	
Off leak current	IOZH	VOUT = 5.5 V	-2.0	—	2.0	μ A
	IOZL	VOUT = 0 V	—	—	—	
Input voltage	VIH	—	4/5 VDD	—	—	V
	VIL	—	—	—	1/5 VDD	
Input current	IiH	VIN = 5.5 V	-2.0	—	2.0	μ A
	IiL	VIN = 0 V	—	—	—	

Terminal connection

RTC-4553



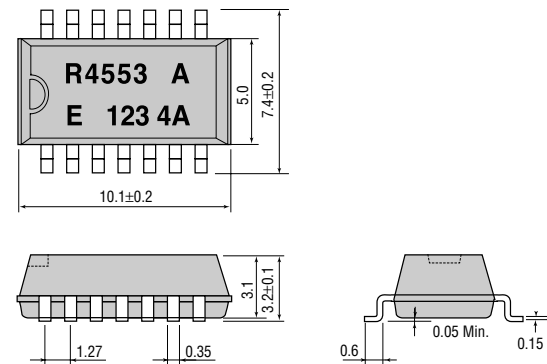
No.	Pin terminal	No.	Pin terminal
1	GND	14	TPOUT
2	WR	13	SOUT
3	SIN	12	CS1
4	SCK	11	CS0
5	L1	10	L5
6	L2	9	L4
7	L3	8	VDD

L1 to L5 are test pin. Do not connect them to any terminals.

External dimensions

(Unit: mm)

RTC-4553 (SOP 14-pin)



Metal may be exposed on the top or bottom of this product. This won't affect any quality, reliability or electrical spec.

Block diagram

