

MIC33050 Evaluation Board

4MHz Internal Inductor PWM Buck Regulator with HyperLight Load™

General Description

The MIC33050 is a 600mA 4MHz switching regulator featuring HyperLight Load $^{™}$ mode. The MIC33050 is highly efficient throughout the entire output current range, drawing just 20µA of quiescent current in operation. The tiny 3mm x 3mm MLF® package, in combination with the 4MHz switching frequency, enables a compact sub-1mm height solution with only three external components. The MIC33050 provides accurate output voltage regulation under the most demanding conditions and responds extremely quickly to a load transient with exceptionally small output voltage ripple.

Factoring in the output current, the internal circuitry of the MIC33050 automatically selects between two modes of operation for optimum efficiency. Under light load conditions, the MIC33050 goes into HyperLight Load™ mode. HyperLight Load™ uses a Pulse-Frequency Modulation (PFM) control scheme that controls the off time at light load. This proprietary architecture reduces the amount of switching needed at light load, thereby increasing operating efficiency. The MIC33050 attains up to 83% efficiency at 1mA output load. As the load current increases beyond approximately 100mA, the device operates using the Pulse-Width Modulation (PWM) method for up to 93% efficiency at higher load. The two modes of operation ensure the highest efficiency across the entire load range.

The MIC33050 operates from an input voltage range of 2.7V to 5.5V and features internal power MOSFETs that deliver up to 600mA of output current. This step-down regulator provides an output voltage accuracy of ±2.5% across the junction temperature range of -40°C to +125°C. The MIC33050 is available in fixed output versions supporting an output voltage as low as 0.72V.

Requirements

The MIC33050 evaluation board requires an input power source that is able to deliver greater than 650mA at 2.7V. The output load can either be an active (electronic) or passive (resistive) load.

Getting Started

- 1. Connect an external supply to the V_{IN} (J1) terminal. Apply the desired input voltage to V_{IN} (J1) and ground (J2) terminals of the evaluation board, paying careful attention to polarity and supply voltage (2.7V \leq $V_{IN} \leq$ 5.5V). An ammeter may be placed between the input supply and the V_{IN} (J1) terminal. Be sure to monitor the supply voltage at the V_{IN} (J1) terminal, since the ammeter and/or power lead resistance can reduce the voltage supplied to the device.
- 2. Connect a load to the V_{OUT} (J3) and ground terminal (J4). The load can be either passive (resistive) or active (electronic load). An ammeter may be placed between the load and the output terminal. Ensure the output voltage is monitored at the V_{OUT} (J3) terminal.
- 3. **Enable the MIC33050**. The MIC33050 evaluation board has a pull-up resistor to V_{IN} . To disable the device, apply a voltage below 0.5V to the EN terminal. In the absence of the pull-up resistor, the device is enabled by applying a voltage greater than 1.2V to the EN terminal. The enable pin must be either pulled high or low for proper operation. Removing the pull-up resistor and leaving the pin floating will cause the regulator to operate in an unknown state.

Output Voltage

The MIC33050 evaluation board is available with the following output voltage options listed below:

Ordering Information

Part Number	Description
MIC33050-4YHL EV	1.2V Fixed Output Evaluation Board
MIC33050-GYHL EV	1.8V Fixed Output Evaluation Board
MIC33050-SYHL EV	3.3V Fixed Output Evaluation Board

Note: Other voltage options available on request. Contact Micrel.

HyperLight Load is a trademark of Micrel, Inc.

Micrel, Inc. MIC33050 Evaluation Board

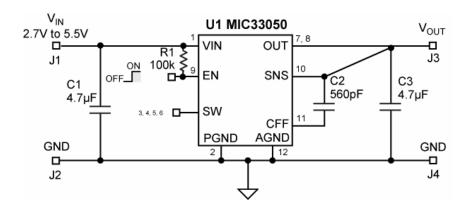
HyperLight Load™ Mode

MIC33050 uses a minimum on and off time proprietary control loop (patented by Micrel). When the output voltage falls below the regulation threshold, the error comparator begins a switching cycle that turns the PMOS on and keeps it on for the duration of the minimum-on-time. This increases the output voltage. If the output voltage is over the regulation threshold, then the error comparator turns the PMOS off for a minimumoff-time until the output drops below the threshold. The NMOS acts as an ideal rectifier that conducts when the PMOS is off. Using a NMOS switch instead of a diode allows for lower voltage drop across the switching device when it is on. The asynchronous switching combination between the PMOS and the NMOS allows the control loop to work in discontinuous mode for light load operations. In discontinuous mode, the MIC33050 works in pulse frequency modulation (PFM) to regulate the output. As the output current increases, the off-time decreases, thus provides more energy to the output. This switching scheme improves the efficiency of MIC33050 during light load currents by only switching when it is needed. As the load current increases, the MIC33050 goes into continuous conduction mode (CCM) and switches at a frequency centered at 4MHz. The equation to calculate the load when the MIC33050 goes into continuous conduction mode may be approximated by the following formula:

$$I_{LOAD} > \left(\frac{(V_{IN} - V_{OUT}) \times D}{2L \times f}\right)$$

As shown in the previous equation, the load at which MIC33050 transitions from HyperLight Load $^{\text{TM}}$ mode to PWM mode is a function of the input voltage (V_{IN}) , output voltage (V_{OUT}) , duty cycle (D), inductance (L) and frequency (f).

MIC33050 Typical Circuit



Bill of Materials

Item	Part Number	Manufacturer	Description	Qty
C1, C3	C1608X5R0J475K	TDK ⁽¹⁾	4.7μF Ceramic Capacitor, 6.3V, X5R, Size 0603	2
C2	VJ0603A561KXAA	Vishay ⁽²⁾	560pF Ceramic Capacitor, 50V, NPO, Size 0603	1
R1	CRCW06031003FT1	Vishay ⁽²⁾	100kΩ, 1%, Size 0603	1
U1	MIC33050-xYHL	Micrel, Inc. (3)	4MHz PWM Buck Regulator with HyperLight Load™ Mode	1

Notes:

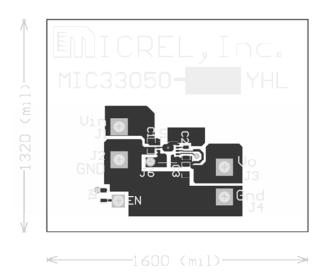
1. TDK: www.tdk.com.

2. Vishay: www.vishay.com.

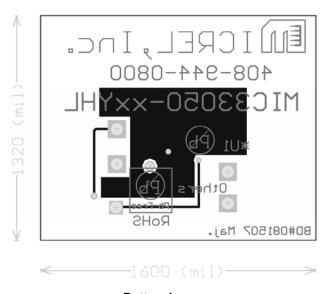
3. Micrel, Inc: www.micrel.com.

Micrel, Inc. MIC33050 Evaluation Board

PCB Layout Recommendations



Top Layer



Bottom Layer

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