OFFLINE GATE DRIVE TRANSFORMERS





- UL and C-UL recognized, TÜV approved components
- **3000Vrms gate to drive winding test**
- Useful operating frequency from 50kHz to 500kHz
- Most popular winding configurations

Electrical Specifications @ 25°C — Operating Temperature -40°C to 130°C											
Part ⁴ Number	Turns Ratio	Reference Data		Primary Inductance	Leakage Inductance	DCR Drive	DCR Gates	Drive			
		ET (V * µsec MAX)	Maximum Flux Density	(1-10) (µH MIN)	Gate to Drive (µH MAX)	(1-10) (mΩ ±20%)	$(m\Omega \pm 20\%)$	Pri-Sec (Vrms)			
P0584	1:1:1	95.0	2100	450	0.5	80	72	3000			
P0585	1:1:1:1:1	95.0	2100	450	3.0	330	180	3000			
NOTES				2 -	The temperature rise (of the component is	calculated based o	n the total core loss			

NOTES:

- 1. These gate drive transformers are meant to operate between 50 and 300kHz with a 12V, 45% bipolar waveform.
- The peak flux density should remain below 2100 Gauss to ensure that the core does not saturate. Use the following procedure to calculate the peak flux density:
 - A. Calculate the Volt-µsec product (ET):
 - ET = 10³ * (Drive Voltage) * (Don) / (Frequency in kHz)
 - B. Calculate the operating flux density (B): Bpk (Gauss) = 40.32 * ET/Ff where: Ff = 1 for unipolar drive applications and 2 for bipolar drive applications

Mechanical

3. The temperature rise of the component is calculated based on the total core loss and copper loss:

- A. To calculate total copper loss (W), use the following formula: Copper Loss (W) = Irms² * (DCR_Drive + (# of Gates) * DCR_Gates)
 B. To calculate total core loss (W), use the following formula: Core Loss (W) = 7.5E-5 * (Frequency in kHz)^{1.67} * (20.16 * ET/1000)^{2.532}
- Core Loss (W) = 7.5E-5 * (Frequency in kHz)^{1.67*} (20.16 * ET/1000)^{2.532} C. To calculate temperature rise, use the following formula: Temperature Rise (C) = 60.18 * (Core Loss(W) + Copper Loss (W))⁸³³

Temperature Rise (C) = 60.18 * (Core Loss(W) + Copper Loss (W))⁸³³ 4. To order RoHS compliant part, add the suffix "**NL**" to the part number (i.e. P0584 becomes P0584**NL**).

Schematics

<u>.480</u> MAX 12.19 810 MAX P0584 P0585 Pri Sec 2 GATE ~ 3 -03 .750 19.05 MAX -0 9 GATE Pulse P058X 10 হ 8 08 DRIVE 10 C -O 4 GATE 10 C 05 , GATE ⊖6 130 P0584 (6 pins) P0585 (10 pins) 3.30 700 (@ 100 400 10 10 10.1610 (Ő 0 (6X) .040 (10X) .040 Dimensions: Inches mm Unless otherwise specified. all tolerances are $\pm \frac{.010}{0.25}$ SUGGESTED PCB HOLE PATTERN

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