

LM120QML

Series 3-Terminal Negative Regulators

General Description

The LM120 series are three-terminal negative regulators with a fixed output voltage of –5V, –12V, and –15V, and up to 1.5A load current capability. Where other voltages are required, the LM137 and LM137HV series provide an output voltage range of –1.2V to –47V.

The LM120 needs only one external component—a compensation capacitor at the output, making them easy to apply. Worst case guarantees on output voltage deviation due to any combination of line, load or temperature variation assure satisfactory system operation.

Exceptional effort has been made to make the LM120 Series immune to overload conditions. The regulators have current limiting which is independent of temperature, combined with thermal overload protection. Internal current limiting protects against momentary faults while thermal shutdown prevents junction temperatures from exceeding safe limits during prolonged overloads.

Although primarily intended for fixed output voltage applications, the LM120 Series may be programmed for higher output voltages with a simple resistive divider. The low quiescent drain current of the devices allows this technique to be used with good regulation.

Features

- Preset output voltage error less than ±3%
- Preset current limit
- Internal thermal shutdown
- Operates with input-output voltage differential down to 1V
- Excellent ripple rejection
- Low temperature drift
- Easily adjustable to higher output voltage

LM120 Series Packages and Power Capability

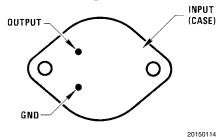
Package	Rated Power Dissipation	Design Load Current
TO-3 (K)	20W	1.5A
TO-39 (H)	2W	0.5A

Ordering Information

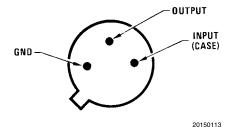
NS Part Number	SMD Part Number	NS Package Number	Package Description
LM120H-5.0/883		H03A	3LD T0-39 Metal Can
LM120H-12/883		H03A	3LD T0-39 Metal Can
LM120H-15/883		H03A	3LD T0-39 Metal Can
LM120K-12/883		K02C	2LD T0-3 Metal Can
LM120K-15/883		K02C	2LD T0-3 Metal Can
LM120KG-5 MD8		(Note 1)	Bare Die
LM120KG-12 MD8		(Note 1)	Bare Die
LM120KG-15 MD8		(Note 1)	Bare Die

Note 1: FOR ADDITIONAL DIE INFORMATION, PLEASE VISIT THE HI REL WEB SITE AT: www.national.com/analog/space/level_die

Connection Diagrams

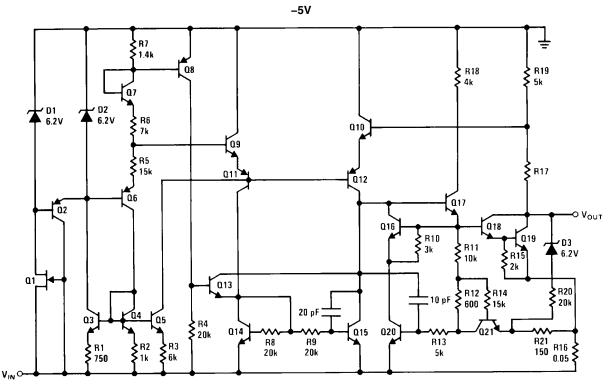


Bottom View Steel Metal Can Package TO-3 (K) See NS Package Number K02C

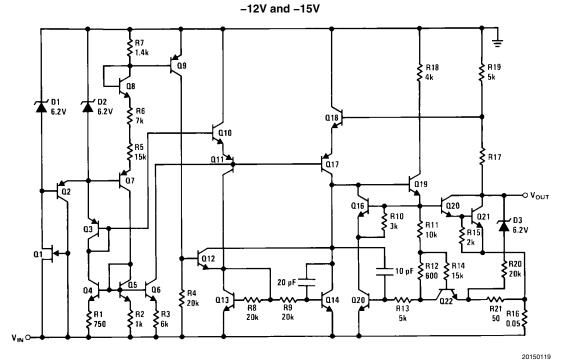


Bottom View
Metal Can Package TO-39 (H)
See NS Package Number H03A

Schematic Diagrams



20150118



Absolute Maximum Ratings (Note 2)

	LM120-5	LM120-12	LM120-15			
Power Dissipation		Internally Limited				
Input Voltage	–25V	-35V	-40V			
Input-Output Voltage Differential	25V	30V	30V			
Junction Temperatures		150°C				
Storage Temperature Range		$-65^{\circ}\text{C} \le \text{T}_{\text{A}} \le +150^{\circ}\text{C}$				
Operating Temperature Range		$-55^{\circ}\text{C} \le \text{T}_{\text{A}} \le +125^{\circ}\text{C}$				
Lead Temperature (Soldering, 10 sec.)		300°C				
Thermal Resistance						
θ_{JA}						
H-Pkg (Still Air @ 0.5W)		191°C/W				
H-Pkg (500LF/Min Air flow @ 0.5W)		70°C/W				
K-Pkg (Still Air @ 0.5W)		35°C/W				
K-Pkg (500LF/Min Air flow @ 0.5W)		TBD				
θ_{JC}						
H-Pkg		29°C/W				
K-Pkg	3°C/W					
ESD Tolerance (Note 4)	4000V					

Quality Conformance Inspection

Mil-Std-883, Method 5005 - Group A

Subgroup	Subgroup Description	
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55
12	Settling time at	+25
13	Settling time at	+125
14	Settling time at	-55

LM120H-5.0

DC Parameters

The following conditions apply, unless otherwise specified. $V_{IN} = -10V$, $I_{L} = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
1	Quiescent Current	V _{IN} = -7V			2.0	mA	1, 2, 3
l _Q	Quiescent Current	V _{IN} = -25V			2.0	mA	1, 2, 3
		Em \ < 1 < 0 5 \		-0.4	0.4	mA	1
٨١	Quiescent Current Change	5mA ≤ I _L ≤ 0.5A		-0.5	0.5	mA	2, 3
Δl _Q	Quiescent Current Change	251/51/57/		-0.4	0.4	mA	1
		-25V ≤ V _{IN} ≤ -7V		-0.5	0.5	mA	2, 3
				-5.1	-4.9	V	1
		V _{IN} = -7.5V		-5.2	-4.8	V	1, 2, 3
V _{OUT}	Output Voltage	$V_{IN} = -7.5V, I_{L} = 0.5A$		-5.2	-4.8	V	1, 2, 3
		V _{IN} = -25V		-5.2	-4.8	V	1, 2, 3
		$V_{IN} = -25V, I_{L} = 100mA$		-5.2	-4.8	V	1, 2, 3
D	Line Degulation	051/51/57/		-25	25	mV	1
R _{Line}	Line Regulation	-25V ≤ V _{IN} ≤ -7V		-50	50	mV	2, 3
D	Load Regulation	Em A < 1 < 0 E A		-50	50	mV	1
R _{Load}	Load Regulation	$5\text{mA} \le I_L \le 0.5\text{A}$	·	-100	100	mV	2, 3
I _{os}	Short Circuit Current	V _{IN} = -25V		0.1	1.5	Α	1
RR	Ripple Rejection	$f = 120$ Hz, $I_L = 125$ mA, $e_I = 1$ V _{RMS}		54		dB	4

LM120K-5.0

DC Parameters

The following conditions apply, unless otherwise specified. $V_{IN} = -10V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
	Outpoont Comment	V _{IN} = -7V			2.0	mA	1, 2, 3
l _Q	Quiescent Current	V _{IN} = -25V			2.0	mA	1, 2, 3
		5mA ≤ I ₁ ≤ 1.5A		-0.4	0.4	mA	1
٨١	Quiescent Current Change	SITIA SIL SITISA		-0.5	0.5	mA	2, 3
Δl _Q	Quiescent ourient onlange	-25V ≤ V _{IN} ≤ -7V		-0.4	0.4	mA	1
		-23V = V _{IN} = -7V		-0.5	0.5	mA	2, 3
				-5.1	-4.9	V	1
		V _{IN} = -7.5V		-5.2	-4.8	٧	1, 2, 3
V _{OUT}	Output Voltage	$V_{IN} = -7.5V, I_{L} = 1.5A$		-5.2	-4.8	V	1, 2, 3
		V _{IN} = -25V		-5.2	-4.8	V	1, 2, 3
		$V_{IN} = -25V, I_{L} = 1A$		-5.2	-4.8	V	1, 2, 3
D	Line Regulation	051/61/67/		-25	25	mV	1
R _{Line}	Line negulation	-25V ≤ V _{IN} ≤ -7V		-50	50	mV	2, 3
D	Load Regulation	5m1 < 1 < 1 5 1		-75	75	mV	1
R _{Load}	Load Regulation	5mA ≤ I _L ≤ 1.5A		-100	100	mV	2, 3
I _{os}	Short Circuit Current	V _{IN} = -25V		0.4	3.0	Α	1
RR	Ripple Rejection	$f = 120$ Hz, $I_L = 350$ mA, $e_I = 1$ V _{RMS}		54		dB	4

LM120H-12

DC Parameters

The following conditions apply, unless otherwise specified. $V_{IN} = -17V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
	Outropent Comment	V _{IN} = -14V			4.0	mA	1, 2, 3
l _Q	Quiescent Current	V _{IN} = -32V			4.0	mA	1, 2, 3
		V _{IN} = -17V,			0.4	mA	1
Δ1	Quiescent Current Change	$5mA \le I_L \le 200mA$			0.5	mA	2, 3
Δl _Q	Quiescent Guirent Change	-32V ≤ V _{IN} ≤ -14V			0.4	mA	1
		-32 V = V _{IN} = -14 V			0.5	mA	2, 3
R _{Load}	Load Regulation	$V_{IN} = -17V, 5mA \le I_{L} \le 200mA$		-25	25	mV	1
' 'Load	Load Hegulation			-50	50		2, 3
R _{Line}	Line Regulation	-32V ≤ V _{IN} ≤ -14V		-10	10	mV	1
Line	Line Regulation	-32 V = V _{IN} = -14 V		-20	20	mV	2, 3
I _{os}	Short Circuit Current	V _{IN} = -32V		0.1	1.5	Α	1
		V _{IN} = -17V		-12.3	-11.7	V	1
		V _{IN} = -32V		-12.5	-11.5	V	1, 2, 3
V _{OUT}	Output Voltage	$V_{IN} = -32V, I_{L} = 100mA$		-12.5	-11.5	V	1, 2, 3
		V _{IN} = -14.5V		-12.5	-11.5	V	1, 2, 3
		$V_{IN} = -14.5V, I_{L} = 200mA$		-12.5	-11.5	V	1, 2, 3
RR	Ripple Rejection	$f = 120$ Hz, $I_L = 125$ mA, $e_i = 1$ V _{RMS}		56		dB	4

LM120K-12

DC Parameters

The following conditions apply to all the following parameters, unless otherwise specified. $V_{IN} = -17V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
1	Quiescent Current	V _{IN} = -14V			4.0	mA	1, 2, 3
I _Q	Quiescent Current	V _{IN} = -32V			4.0	mA	1, 2, 3
		$V_{IN} = -17V$, $5mA \le I_I \le 1A$			0.4	mA	1
ΔI _Q	Quiescent Current Change	V _{IN} = -17 V, SIIIA = I _L = IA			0.5	mA	2, 3
ΔIQ	Quiescent Gurrent Griange	-32V ≤ V _{IN} ≤ -14V			0.4	mA	1
		-32 V = V _{IN} = -14 V			0.5	mA	2, 3
R _{Load}	Load Regulation	$V_{IN} = -17V$, $5mA \le I_L \le 1A$		-80	80	mV	1, 2, 3
R _{Line}	Line Regulation	-32V ≤ V _{IN} ≤ -14V		-10	10	mV	1
Line	Line Regulation	-32 V = V _{IN} = -14 V		-20	20	mV	2, 3
I _{os}	Short Circuit Current	V _{IN} = -32V		0.4	3.0	Α	1
		V _{IN} = -17V		-12.3	-11.7	V	1
		V _{IN} = -32V		-12.5	-11.5	V	1, 2, 3
V _{OUT}	Output Voltage	$V_{IN} = -32V, I_{L} = 1A$		-12.5	-11.5	V	1, 2, 3
		V _{IN} = -14.5V		-12.5	-11.5	V	1, 2, 3
		V _{IN} = -14.5V, I _L = 1A		-12.5	-11.5	V	1, 2, 3
RR	Ripple Rejection	$f = 120$ Hz, $I_L = 350$ mA, $e_i = 1V_{RMS}$	·	56		dB	4

LM120H-15

DC Parameters

The following conditions apply to all the following parameters, unless otherwise specified. $V_{IN} = 20V$, $I_{L} = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
	Outles cont Current	V _{IN} = -17V			4.0	mA	1, 2, 3
l _Q	Quiescent Current	V _{IN} = -35V			4.0	mA	1, 2, 3
		V _{IN} = -17V,			0.4	mA	1
٨١	Quiescent Current Change	5mA ≤ I _L ≤ 200mA			0.5	mA	2, 3
Δl _Q	Quiescent Current Change	251/ < 1/ < 171/			0.4	mA	1
		-35V ≤ V _{IN} ≤ -17V			0.5	mA	2, 3
D	Load Regulation	V _{IN} = -20V,		-25	25	mV	1
R _{Load}	Load Regulation	5mA ≤ I _L ≤ 200mA		-50	50	mV	2, 3
R _{Line}	Line Regulation	25// < // < 17//		-10	10	mV	1
Line	Line negulation	-35V ≤ V _{IN} ≤ -17V		-20	20	mV	2, 3
Ios	Short Circuit Current	V _{IN} = -35V		0.1	1.5	Α	1
		V _{IN} = -20V		-15.3	-14.7	V	1
		V _{IN} = -35V		-15.5	-14.5	V	1, 2, 3
V _{OUT}	Output Voltage	V _{IN} = -35V, I _L = 100mA		-15.5	-14.5	V	1, 2, 3
		V _{IN} = -17.5V		-15.5	-14.5	V	1, 2, 3
		V _{IN} = -17.5V, I _L = 200mA		-15.5	-14.5	V	1, 2, 3
RR	Ripple Rejection	$f = 120$ Hz, $I_L = 125$ mA, $e_i = 1$ V _{RMS}		56		dB	4

LM120K-15

DC Parameters

The following conditions apply, unless otherwise specified. V_{IN} = 20V, I_{L} = 5mA

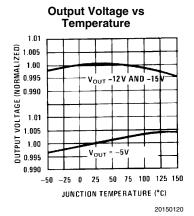
Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
1	Ouissant Current	V _{IN} = -17V			4.0	mA	1, 2, 3
IQ	Quiescent Current	V _{IN} = -35V			4.0	mA	1, 2, 3
		V _{IN} = -17V,			0.4	mA	1
٨١	Quiescent Current Change	$5mA \le I_L \le 1A$			0.5	mA	2, 3
ΔI_Q	Quiescent Current Change	-35V ≤ V _{IN} ≤ -17V			0.4	mA	1
		-55V = V _{IN} = -17V			0.5	mA	2, 3
D	Load Regulation	V _{IN} = -20V,		-80	80	mV	1, 2, 3
R _{Load}	Load negulation	$5mA \le I_L \le 1A$					
D	Line Regulation	-35V ≤ V _{IN} ≤ -17V		-10	10	mV	1
R _{Line}	Line Regulation	-35V = V _{IN} = -17V		-20	20	mV	2, 3
I _{OS}	Short Circuit Current	V _{IN} = -35V		0.4	3.0	Α	1
		V _{IN} = -20V		-15.3	-14.7	V	1
		V _{IN} = -35V		-15.5	-14.5	V	1, 2, 3
V_{OUT}	Output Voltage	$V_{IN} = -35V, I_{L} = 1A$		-15.5	-14.5	V	1, 2, 3
		V _{IN} = -17.5V		-15.5	-14.5	V	1, 2, 3
		V _{IN} = -17.5V, I _L = 1.5A		-15.5	-14.5	V	1, 2, 3
$\Delta V_{O} / \Delta t$	Long Term Stability		(Note 3)		150	mV	1
RR	Ripple Rejection	$f = 120$ Hz, $I_L = 350$ mA, $e_I = 1$ V _{RMS}		56		dB	4

Note 2: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

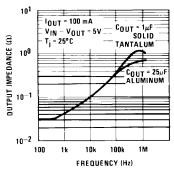
Note 3: Guaranteed parameter, not tested

Note 4: Human body model, 1.5 k Ω in seriew with 100 pF.

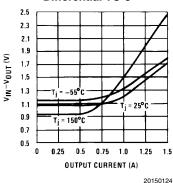
Typical Performance Characteristics



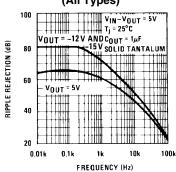
Output Impedance TO-3



Minimum Input-Output Differential TO-3

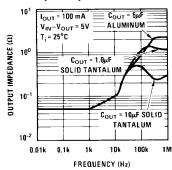


Ripple Rejection (All Types)



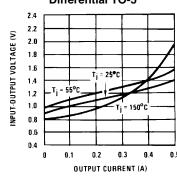
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Output Impedance TO-5



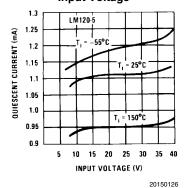
20150123

Minimum Input-Output Differential TO-5

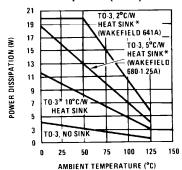


20150125

Quiescent Current vs Input Voltage



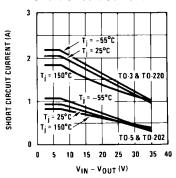
Maximum Average Power Dissipation (TO-3)



20150128

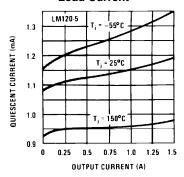
*These curves for LM120. Derate 25°C further for LM320.

Short Circuit Current



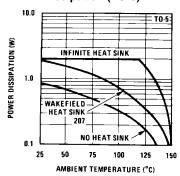
20150132

Quiescent Current vs Load Current



20150127

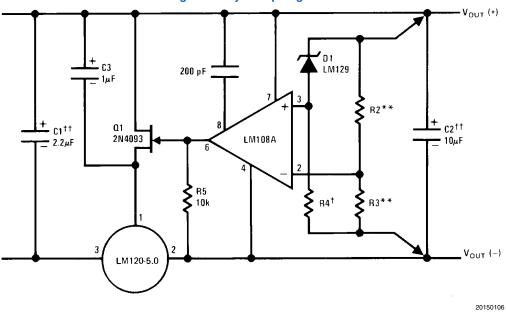
Maximum Average Power Dissipation (TO-5)



20150129

Typical Applications

High Stability 1 Amp Regulator



Lead and line regulation — 0.01% temperature stability — 0.2%

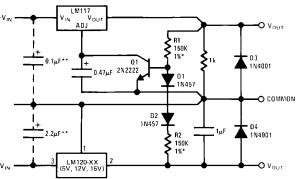
†Determines Zener current.

††Solid tantalum.

An LM120-12 or LM120-15 may be used to permit higher input voltages, but the regulated output voltage must be at least –15V when using the LM120-12 and –18V for the LM120-15.

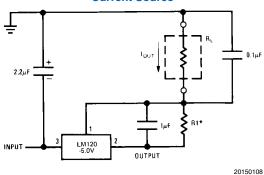
**Select resistors to set output voltage. 2 ppm/°C tracking suggested.

Wide Range Tracking Regulator



20150107

Current Source



.0V

$$*I_{OUT} = 1 \text{ mA} + \frac{5.0 \text{ M}}{1.0 \text{ M}}$$

^{*} Resistor tolerance of R1 and R2 determine matching of (+) and (-) inputs.

^{**}Necessary only if raw supply capacitors are more than 3 from regulators An LM3086N array may substitute for Q1, D1 and D2 for better stability and tracking. In the array diode transistors Q5 and Q4 (in parallel) make up D2; similarly, Q1 and Q2 become D1 and Q3 replaces the 2N2222.

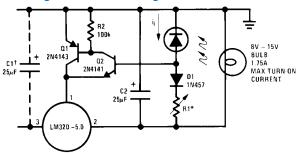
*Optional. Improves transient response and ripple rejection.

$$V_{OUT} = V_{SET} \frac{R1 + R2}{R2}$$

SELECT R2 AS FOLLOWS:

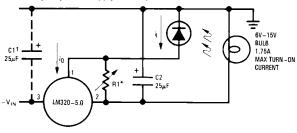
 $\begin{array}{lll} \text{LM120-5} & -300\Omega \\ \text{LM120-12} & -750\Omega \\ \text{LM120-15} & -1k \end{array}$

Light Controllers Using Silicon Photo Cells



201501

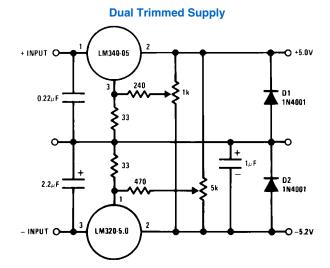
*Lamp brightness increases until i_l = 5V/R1 (i_l can be set as low as 1 μ A). †Necessary only if raw supply filter capacitor is more than 2 from LM320MP.



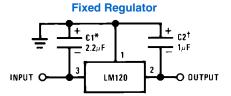
20150111

*Lamp brightness increases until $i_l = i_Q (1 \text{ mA}) + 5 \text{V/R1}$.

†Necessary only if raw supply filter capacitor is more than 2 from LM320.



20150103



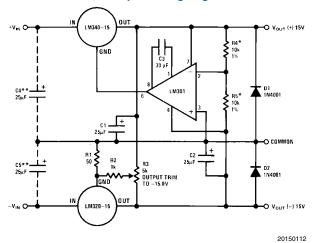
20150102

*Required if regulator is separated from filter capacitor by more than 3 . For value given, capacitor must be solid tantalum. 25 μF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25 μ F aluminum electrolytic may be substituted. Values given may be increased without limit

For output capacitance in excess of 100 $\mu F,$ a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

±15V, 1 Amp Tracking Regulators



Performance (Typical)

Load Regulation at ΔI_L = 1A 10 mV 1 mV Output Ripple, C_{IN} = 3000 μF , 100 $\mu VRMS$ 100 $\mu VRMS$ I_L = 1A

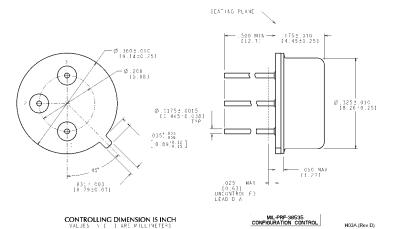
Temperature Stability +50 mV +50 mV Output Noise 10 Hz \leq f \leq 10 kHz 150 μ VRMS 150 μ VRMS

^{*}Resistor tolerance of R4 and R5 determine matching of (+) and (-) outputs. **Necessary only if raw supply filter capacitors are more than 2 from regulators.

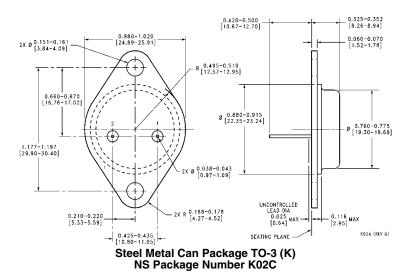
Revision History

Date Released	Revision	Section	Changes
12/15/2010	A	New release to the corporate format	6 MDS datasheets were converted and merged into one datasheet compliant to corporate format. Drift endpoints removed since note used on 883 product. MDS MNLM120-5.0-K Rev OBL, MNLM120-5.0-H Rev OBL, MNLM120-12-K Rev OBL, MNLM120-12-H Rev OBL, MNLM120-15-K Rev OBL, & MNLM120-15-H Rev OBL will be archived.

Physical Dimensions inches (millimeters) unless otherwise noted



Metal Can Package (TO-39) (H) NS Package Number H03A



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Notes

For more National Semiconductor product information and proven design tools, visit the following Web sites at: www.national.com

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