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Specifications:

Operating Supply Voltage: 4VDC ~ 18VDC

Max. Input Signal Frequency: 50MHz

Input Impedance: 120KΩ

TTL: Logic "1" (HI LED) >3.0V ±.25V

Logic "0" (LO LED) <.75V ±.25V

CMOS: Logic "1" (HI LED) >60% V_{CC} ±5%

Logic "0" (LO LED) <15% V_{CC} ±5%, <40% V_{CC} ±5% for 7~18 V_{CC}

Pulser: Sync input impedance 1Megohm

Pulse Width: 10µsec

Pulse Rate: Switchable - .5 / 400Hz

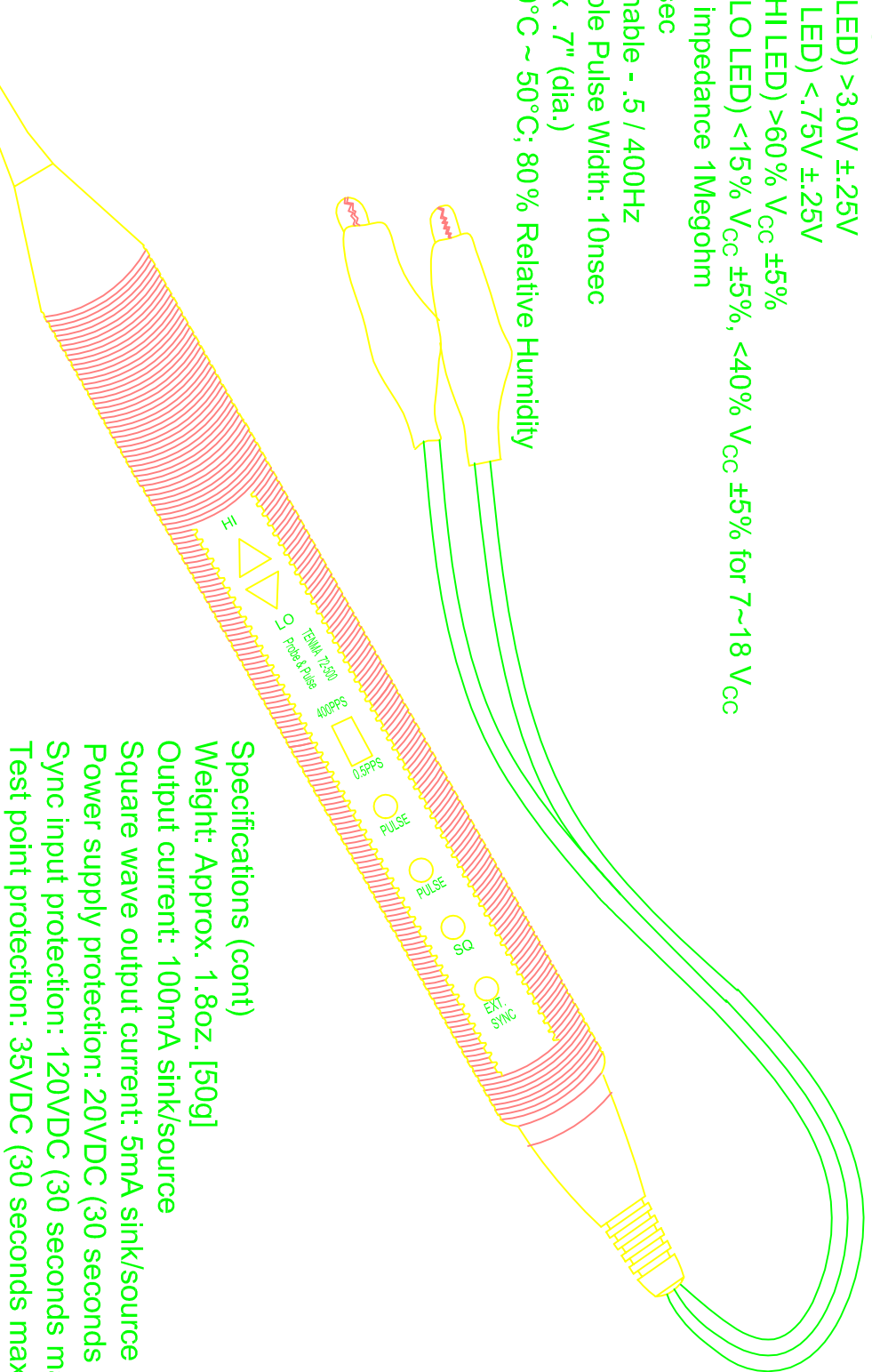
Minimum Detectable Pulse Width: 10nsec

Dimensions: 8.2" x .7" (dia.)

Operating Temp: 0°C ~ 50°C; 80% Relative Humidity



REVOLUTIONS		DOC. NO.	SPC-F005	Effective:	12/21/98	DCP No.	680	
DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRD	DATE
430	A	RELEASED	JWM	1/18/01	JC	2/8/01	DJC	2/8/01



Specifications (cont)

- Weight: Approx. 1.8oz. [50g]
- Output current: 100mA sink/source
- Square wave output current: 5mA sink/source
- Power supply protection: 20VDC (30 seconds max.)
- Sync input protection: 120VDC (30 seconds max.)
- Test point protection: 35VDC (30 seconds max.)



DISCLAIMER:
ALL STATEMENTS AND TECHNICAL INFORMATION CONTAINED HEREIN ARE BASED UPON INFORMATION AND/OR TESTS WE BELIEVE TO BE ACCURATE AND RELIABLE SINCE CONDITIONS OF USE ARE BEYOND OUR CONTROL. THE USER ASSUMES ALL RESPONSIBILITY FOR THE PROPER USE AND THE PROPER USE AND ASSUME ALL RISK AND LIABILITY WHATSOEVER IN CONNECTION THEREWITH.

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.		DRAWING TITLE: 50MHz Combination Logic Probe/Pulser	
DRAWN BY:	DATE:	SIZE	DWG. NO.
Jeff McVicker	1/18/01	A	72-500
CHECKED BY:	DATE:	ELECTRONIC FILE	REV
JOHN COLE	2/8/01	52F7960.dwg	A
APPROVED BY:	DATE:	SCALE:	SHEET:
Daniel Corey	2/8/01	NTS	1 OF 1