FAIRCHILD February 2009 SEMICONDUCTOR 2N5484/5485/5486 MMBF5484/5485/5486 TO-92 G

SOT-23 Mark: 6B / 6M / 6H

NOTE: Source & Drain are interchangeable

# **N-Channel RF Amplifier**

This device is designed primarily for electronic switching applications such as low On Resistance analog switching. Sourced from Process 50.

### Absolute Maximum Ratings\* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>DG</sub>	Drain-Gate Voltage	25	V
V <sub>GS</sub>	Gate-Source Voltage	- 25	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> ,T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES: 1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Мах		Units
		2N5484-5486	*MMBF5484-5486	
PD	Total Device Dissipation	350	225	mW
	Derate above 25°C	2.8	1.8	mW/°C
R <sub>eJC</sub>	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

\*Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

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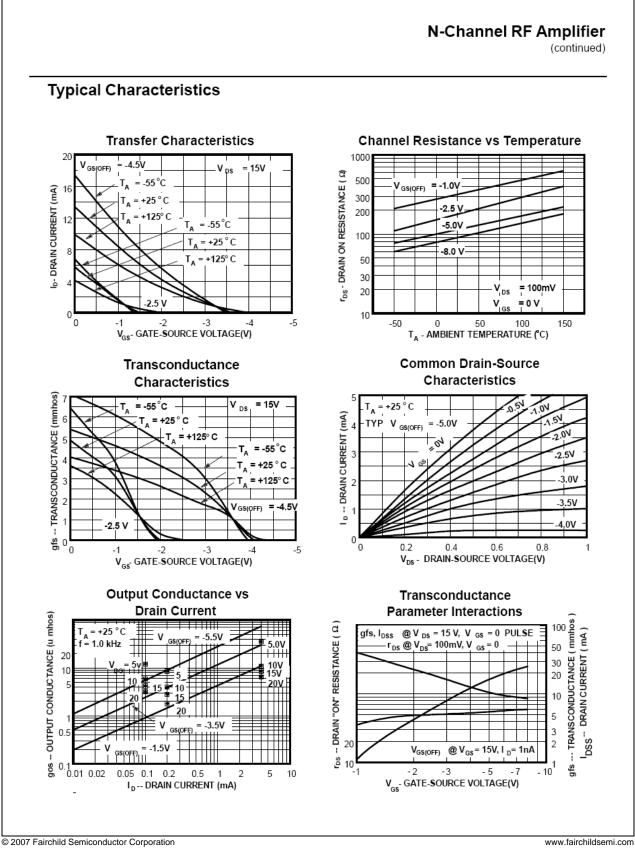
# N-Channel RF Amplifier (continued)

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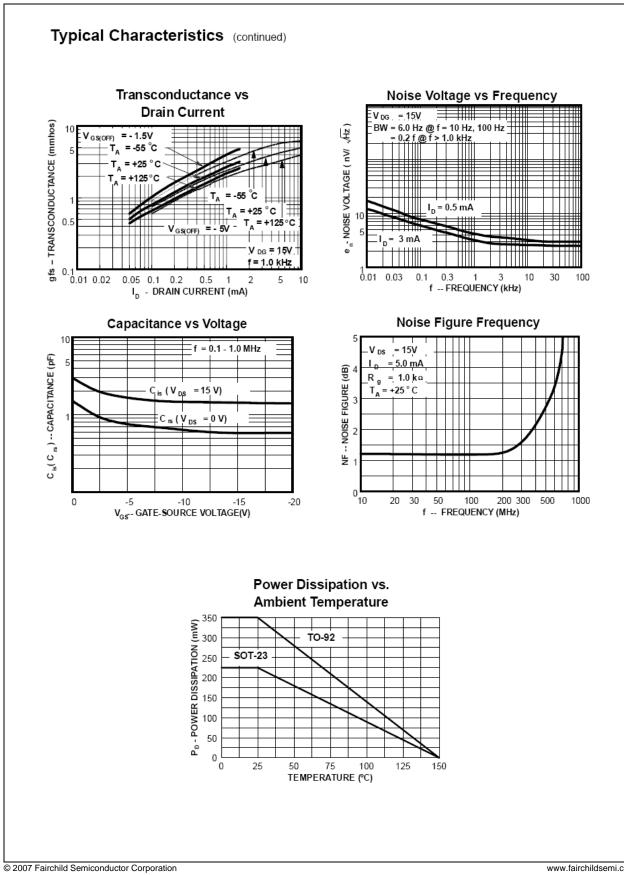
Symbol	Parameter	Test Conditions	Min	Тур	Мах	Units
OFF CHA	RACTERISTICS					
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	I <sub>G</sub> = - 1.0 μA, V <sub>DS</sub> = 0	- 25			V
I <sub>GSS</sub>	Gate Reverse Current	V <sub>GS</sub> = - 20 V, V <sub>DS</sub> = 0			- 1.0 - 0.2	nA
V <sub>GS(off)</sub>	Gate-Source Cutoff Voltage	V <sub>GS</sub> = - 20 V, V <sub>DS</sub> = 0, T <sub>A</sub> = 100°C V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 nA <b>5484</b>	- 0.3		- 3.0	μA V
* 00(0II)	Sale Source Saler Follage	5485	- 0.5		- 4.0	V
		5486	- 2.0		- 6.0	V
ON CHAR	ACTERISTICS					
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current*	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 5484	1.0		5.0	mA
		5485 5486	4.0 8.0		10 20	mA mA
		5460	0.0		20	
SMALL SI	GNAL CHARACTERISTICS					
9fs	Forward Transfer Conductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 kHz 5484	3000		6000	μmho
		5484	3500		7000	μmhc
		5486	4000		8000	μmhc
Re(yis)	Input Conductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 100 MHz			100	
		<b>5484</b> V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 400 MHz			100	μmho
		5485 / 5486			1000	μmho
gos	Output Conductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 kHz			50	
		5484 5485			50 60	μmho μmho
		5486			75	μmhc
Re(yos)	Output Conductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 100 MHz				
(2 )		<b>5484</b> V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 400 MHz			75	μmho
		5485 / 5486			100	μmho
Re(Yfs)	Forward Transconductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 100 MHz				
0		<b>5484</b> V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 400 MHz	2500			μmho
		VDS = 15 V, VGS = 0, 1 = 400 MHZ 5485	3000			μmho
		5486	3500			μmhc
Ciss	Input Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz			5.0	pF
Crss	Reverse Transfer Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz			1.0	pF
Coss	Output Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz			2.0	pF
NF	Noise Figure	$V_{DS}$ = 15 V, R <sub>G</sub> = 1.0 kΩ, f = 100 MHz 5484			3.0	dB
		V <sub>DS</sub> = 15 V, R <sub>G</sub> = 1.0 kΩ, f = 400 MHz <b>5484</b>		4.0		dB
		$\begin{array}{ll} V_{\text{DS}}\text{=}~15~\text{V}~,~R_{\text{G}}\text{=}~1.0~\text{k}\Omega,\\ \text{f}\text{=}~100~\text{MHz}~~\textbf{5485}~\textbf{/}~\textbf{5486} \end{array}$			2.0	dB
		V <sub>DS</sub> = 15 V, R <sub>G</sub> = 1.0 kΩ, f = 400 MHz <b>5485 / 5486</b>			4.0	dB

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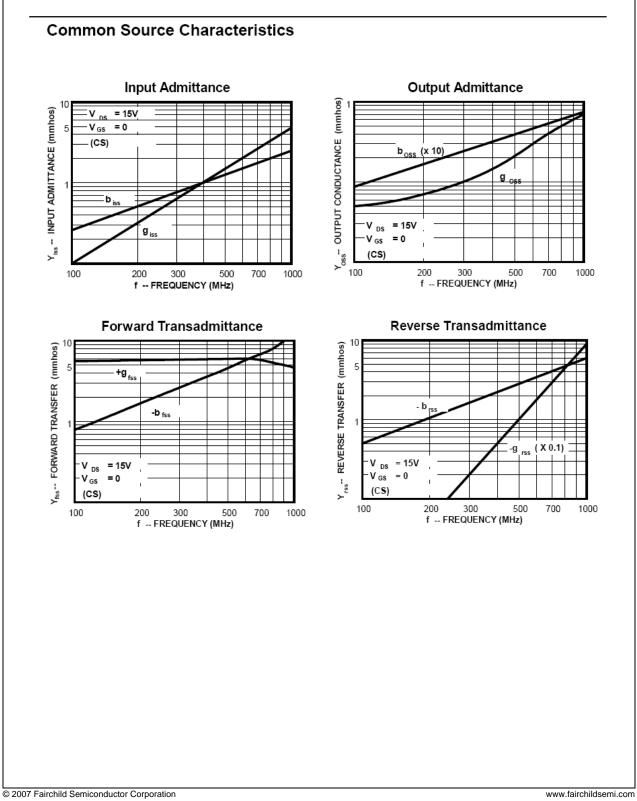


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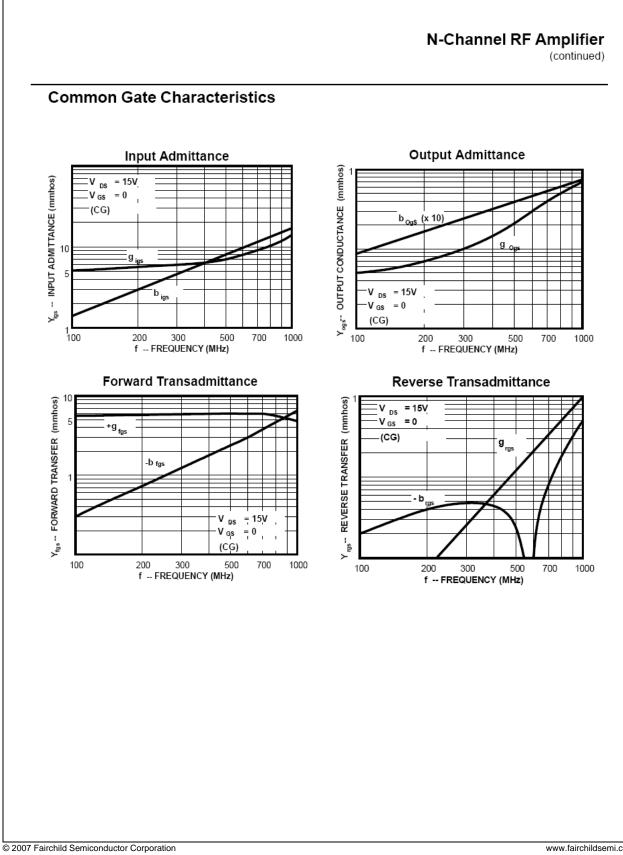
**N-Channel RF Amplifier** 

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