

Ruttonsha International Rectifier Ltd.

SILICON CONTROLLED RECTIFIERS

41RIA, 51RIA SERIES Power Silicon Controlled Rectifiers 64, 80 Amp RMS SCRs

Types : 41RIA10-41RIA140, 51RIA10-51RIA140

FEATURES

- ∉ All diffused series.
- ∉ High di/dt and dv/dt capabilities.
- *∉* Reliable blocking at elevated temperature.
- ∉ High surge current rating.
- ∉ High I²t capability.
- *∉* Excellent dynamic characteristics.

THERMAL MECHANICAL SPECIFICATIONS

R _{thjc}	Maximum thermal resistance	41RIA	51RIA
	DC operation	0.4ºC/W	0.35°C/W
R _{thcs}	Contact thermal resistance case-to-sink	0.25ºC/W	
TJ	Junction operating temp. range	-40°C to +125°C	
T _{stg}	Storage temperature range	-40°C to +	+125ºC
	Mounting torque (Non-lubricated threads)	0.4 M-Kg min. 0.6 M-Kg max.	
	Approximate weight	30 gms.	



ELECTRICAL RATINGS

TYPE	41RIA / 51RIA	10	20	40	60	80	100	120	140
V	Max. repetitive peak off state voltage (V)	100	200	400	600	800	1000	1200	1400
V _{RRM}	Max. repetitive peak reverse voltage (V)	100	200	400	600	800	1000	1200	1400
V _{RSM}	Max. non-repetitive peak reverse voltage (V)	150	300	500	700	900	1100	1300	1500
I _{RM} & I _{DM}	Max. peak reverse & off state current @ rated V _{DRM} & V _{RRM} 125ºC -mA	15	15	15	15	15	15	15	15

41 RIA, 51 RIA SERIES

ELECTRICAL SPECIFICATIONS

	ON-STATE	41RIA	51RIA	Units	Conditions		
I _{T(RMS)}	Max. RMS on-state current	65	80	А			
I _{T(AV)}	Max. average on-state current	40	50	А	T _c = 94°C max., 180°C sinusoidal conduction.		
I _{tsm}	Max. peak one cycle non-repetitive surge current	1050	1200	A	50 Hz half cycle sine wave or 6 ms rectangular	Following any rated load conditions and with rated $V_{\rm RRM}$ applied following surge.	
		1250	1430		pulse.	Following any rated load condition and with no voltage reapplied following surge.	
l²t	Max. I ² t capability for fusing	5700	7200	A²s	t = 10 ms R ir	ated V _{RRM} applied following surge, itial T _J = 125°C	
l²t	Max. I ² t capability for individual (1) device fusing	8060	10180	A²s	t = 10 ms V ir	_{RRM} following surge = 0, itial T _J = 125ºC	
l²√t	Max. I²√t capability for individual device fusing	80600	101800	A²√s	t = 0.1 to 10 ms V ir	_{RRM} following surge = 0,, itial T _J = 125ºC	
V _{TM}	Max. peak on-state voltage	1.65	1.6	V	$T_{J} = 25^{\circ}C, I_{TM} = \pi \times I_{T(AV)}$		
I _H	Max. holding current	20)0	mA	$T_{J} = 25^{\circ}C$, anode supply = 22V, initial $I_{T} = 2.0A$		
I _L	Max. latching current	40	00	mA	Anode supply = 6V, resistive load.		
BLOCKING (1) I ² t for time t = $I^2\sqrt{t} * \sqrt{t}$							
dv/dt	Min. critical rate-of-rise of off-state voltage	20	00	V/µs	T _j = 125 ^o C. Exponential Zero gate bias voltage gate open circuited.		
500 For 67% rated V					For 67% rated V _{DRM}		
t _d	Typical delay time	0.9		μs	$T_c = 25^{\circ}C$, $V_{DM} = rated V_{DRM}$, $I_{TM} = 10A$ dc resistive circuit, Gate pulse 10V, 15 Ω source $t_p = 20 \ \mu$ S		
di/dt	di/dt Max non-repetitive rate of rise of turned-on current V _{RRM} = 700- 1400 V 50 - 600 V		100 200		$T_c = 125^{\circ}C$, $V_{DM} = rated V_{DRM}$, $I_{TM} = 2 x rated di/dt$. Gate pulse 20V 150, $t = 6 \mu S$, $t = 0.1 \mu S$ max.		
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t	Typical turn-off time	110		μs	$T_{c} = 125^{\circ}C$, $I_{TM} = 50A$, di/dt = 10 A/µS, V_{R} during turn-off		
	ERING				interval = 50 V min., reap rated V _{DRM} Gate bias : 0V	ollied dv/dt = 20 V/μS linear to /, 100Ω	
PGM	Max. peak gate power	10		W	tp ≤ 5ms		
P _{G(AV)}	Max. average gate power	2.	.5	W			
I _{GM}	Max. peak positive gate current	2.	.5	А			
+V _{GM}	Max. peak positive gate voltage	20	0	V			
-V _{GM}	Max. peak negative gate voltage	1	0	V			
I _{GT}	Max. required DC gate current 250		50	mA	T ₁ = -40°C Max. required gate trigger current is th		
	to trigger	100			T _J = 25°C lowes with	st value which will trigger all units + 6V anode-to-cathode.	
	Max required DC gate voltage	50			$T_{\rm J} = 125^{\circ}C$ T = -40°C Max required gate trigger voltage is		
V GT	to trigger	3.5		V	$\frac{T_{1} - 25^{\circ}C}{T_{1} - 25^{\circ}C}$ with + 6V and to extra the set of the		
V _{GD}	Max. DC gate voltage not to trigger	0.	2	V	$T_{\rm I} = 125^{\circ}C$ Max.	gate current or voltage not to trigger is	
I _{GD}	Max. DC gate current not to trigger	5.	0	mA	T _J = 125°C unit w	aximum value which will not trigger any ith rated V _{DRM} anode-to-cathode.	

 V_{DRM} = rated voltage



ORDER INFORMATION TABLE











Fig. 5 - Maximum Low-Level On-state Power Loss Vs. Current (Sinusoidal Current Waveform)



Fig. 2 - On-state Current Vs. Case Temperature (Rectangular Current Waveform, 50 to 400 Hz)



Fig. 4 - On-state Current Vs. Case Temperature (Rectangular Current Waveform, 50 to 400 Hz)



41 RIA, 51 RIA SERIES











Fig. 11 - Maximum High-Level On-state Power Loss Vs. Current (Sinusoidal Current Waveform)



Fig. 8 - Maximum Low-Level On-state Power Loss Vs. Current (Rectangular Current Waveform)



Fig. 10 - Maximum High-Level On-state Power Loss Vs. Current (Rectangular Current Waveform)



Fig. 12 - Maximum High-Level On-state Power Loss Vs. Current (Rectangular Current Waveform)



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