SWITCHMODE[™] **Power Rectifier** D²PAK Surface Mount Power Package

The D²PAK Power Rectifier is a state-of-the-art device that employs the use of the Schottky Barrier principle with a platinum barrier metal.

Features

- Package Designed for Power Surface Mount Applications
- Center-Tap Configuration
- · Guardring for Stress Protection
- Low Forward Voltage
- 175°C Operating Junction Temperature
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Short Heat Sink Tab Manufactured Not Sheared!
- Similar in Size to Industry Standard TO-220 Package
- Pb-Free Packages are Available

Mechanical Characteristics

- Case: Epoxy, Molded, Epoxy Meets UL 94 V-0
- Weight: 1.4 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL1 Requirements
- ESD Ratings: Machine Model, C >400 V
 - Human Body Model, 3B >8000 V

MAXIMUM RATINGS (Per Leg)

Rating	Symbol	Value	Unit			
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V			
Average Rectified Forward Current (Rated V _R , T _C = 110°C) Total Device	I _{F(AV)}	10 20	A			
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz, T _C = 100°C)	I _{FRM}	20	A			
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	150	A			
Peak Repetitive Reverse Surge Current (2.0 μs, 1.0 kHz)	I _{RRM}	0.5	A			
Storage Temperature Range	T _{stg}	-65 to +175	°C			
Operating Junction Temperature (Note 1)	TJ	-65 to +175	°C			
Voltage Rate of Change (Rated V _R)	dv/dt	10,000	V/μs			

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

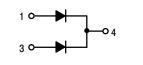
1. The heat generated must be less than the thermal conductivity from Junction–to–Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

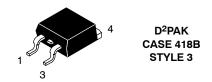


ON Semiconductor®

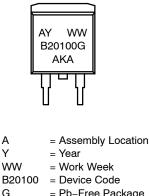
http://onsemi.com







MARKING DIAGRAM



= Pb-Free Package

AKA = Diode Polarity

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

THERMAL CHARACTERISTICS (Per Leg)

Characteristic		Symbol	Value	Unit
Thermal Resistance, Junction-to-Case Junction-to-Ambient (Note 2)		${\sf R}_{ heta {\sf JC}} {\sf R}_{ heta {\sf JA}}$	2.0 50	°C/W
ELECTRICAL CHARACTERISTICS (Per Leg)				
(ir (ir		٧F	0.75 0.85 0.85 0.95	V
	Rated dc Voltage, $T_J = 125^{\circ}C$) Rated dc Voltage, $T_J = 25^{\circ}C$)	i _R	6.0 0.1	mA

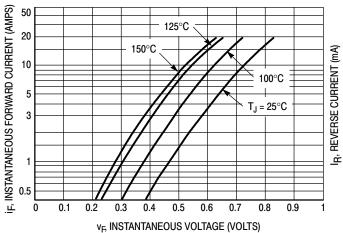
2. When mounted using minimum recommended pad size on FR-4 board.

3. Pulse Test: Pulse Width = 300 $\mu s,$ Duty Cycle \leq 2.0%.

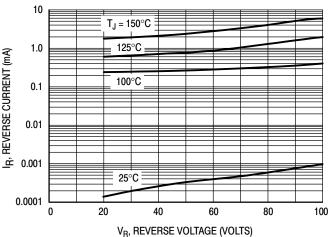
ORDERING INFORMATION

Device	Package	Shipping [†]
MBRB20100CT	D ² PAK	50 Units / Rail
MBRB20100CTG	D ² PAK (Pb–Free)	50 Units / Rail
MBRB20100CTT4	D ² PAK	800 Units / Tape & Reel
MBRB20100CTT4G	D ² PAK (Pb-Free)	800 Units / Tape & Reel

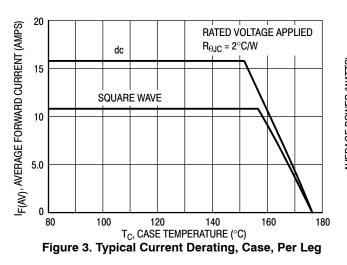
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.











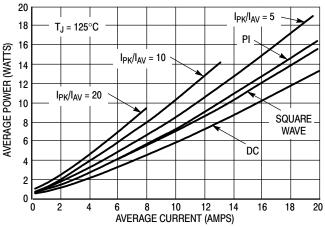
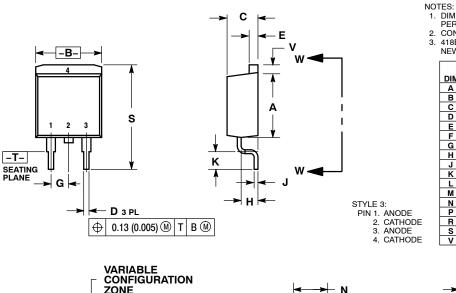


Figure 4. Average Power Dissipation & Average Current

http://onsemi.com

PACKAGE DIMENSIONS

D²PAK CASE 418B-04 ISSUE K



NOTES:
DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
CONTROLLING DIMENSION: INCH.
418B-01 THRU 418B-03 OBSOLETE, NEW STANDARD 418B-04. MILLIMETERS INCHES DIM MIN MAX MIN MAX A 0.340 0.380 8.64 9.65
 B
 0.380
 0.405

 C
 0.160
 0.190
 9.65 10.29 4.06 4.83 **D** 0.020 0.035 0.51 0.89 0.045 0.055 1.14 1.40 F 0.310 0.350 7.87 8.89
 F
 0.310
 0.330

 G
 0.100
 BSC

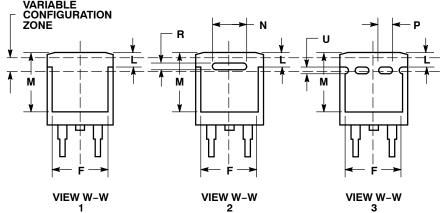
 H
 0.080
 0.110

 J
 0.018
 0.025

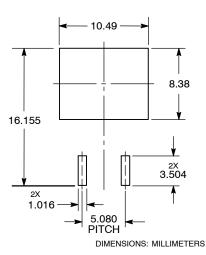
 K
 0.0900
 0.110
 2.54 BSC 2.03 2.79 0.46 0.64 2.29 2.79 L 0.052 0.072 M 0.280 0.320 1.321.837.118.13 N P 0.197 REF 0.079 REF 5.00 REF 2.00 REF
 R
 0.039 REF
 0.99 REF

 S
 0.575
 0.625
 14.60
 15.88

 V
 0.045
 0.055
 1.14
 1.40
 R



SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

SWITCHMODE is a trademark of Semiconductor Components Industries, LLC.

ON Semiconductor and IIII are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death application Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

Japan Customer Focus Center

Phone: 81-3-5773-3850

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative