# MMBD352WT1G

# **Dual Schottky Barrier Diode**

These devices are designed primarily for UHF mixer applications but are suitable also for use in detector and ultra-fast switching circuits.

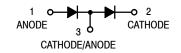
## Features

- Very Low Capacitance Less Than 1.0 pF @ 0 V
- Low Forward Voltage 0.5 V (Typ) @  $I_F = 10 \text{ mA}$
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant



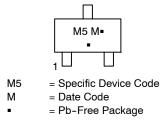
# **ON Semiconductor®**

http://onsemi.com





MARKING DIAGRAM



(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MMBD352WT1G	SOT-323 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V <sub>R</sub>	7.0	V <sub>CC</sub>

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR- 5 Board (Note 1) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	200	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	625	°C/W
Total Device Dissipation Alumina Substrate (Note 2) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	- 55 to +150	°C

1. FR-5 = 1.0  $\times$  0.75  $\times$  0.062 in.

2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in. 99.5% alumina.

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# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Forward Voltage (I <sub>F</sub> = 10 mAdc)	V <sub>F</sub>	-	0.60	V	
Reverse Voltage Leakage Current $(V_R = 3.0 V)$ $(V_R = 7.0 V)$	I <sub>R</sub>	-	0.25 10	μΑ	
Capacitance (V <sub>R</sub> = 0 V, f = 1.0 MHz)	С	-	1.0	pF	



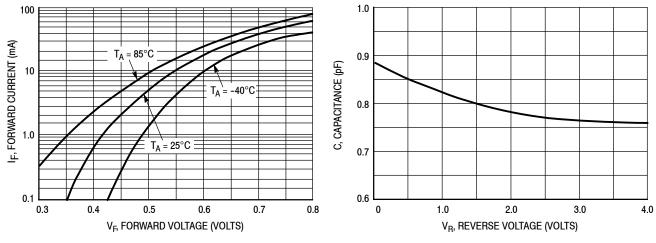


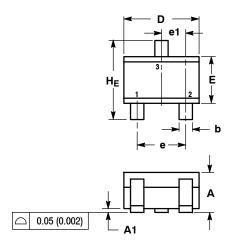
Figure 1. Forward Voltage

Figure 2. Capacitance

## MMBD352WT1G

### PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 **ISSUE N** 



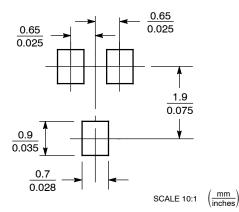
NOTES: DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.80	0.90	1.00	0.032	0.035	0.040	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
A2		0.70 REF 0.028 REF			-		
b	0.30	0.35	0.40	0.012	0.014	0.016	
С	0.10	0.18	0.25	0.004	0.007	0.010	
D	1.80	2.10	2.20	0.071	0.083	0.087	
Е	1.15	1.24	1.35	0.045	0.049	0.053	
е	1.20	1.30	1.40	0.047	0.051	0.055	
e1	0.65 BSC			0.026 BSC			
L	0.20	0.38	0.56	0.008	0.015	0.022	
HE	2.00	2.10	2.40	0.079	0.083	0.095	

STYLE 9: PIN 1. ANODE

CATHODE 3 CATHODE-ANODE

#### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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