

### ST1CC40

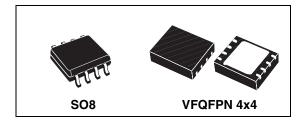
3 A, 850 kHz monolithic synchronous step down constant current LED driver

#### **Features**

- Step down current mode PWM regulator
- 3.5 V to 18 V input voltage
- Current sensing 100 mV Typ. with 5% tolerance
- Stand-by supply current: 6 μA
- Cycle by cycle current limit with frequency foldback
- Integrated 95 m $\Omega$  and 69 m $\Omega$  power MOSFETs
- INH input pin
- All ceramic capacitor
- Internal compensate current mode control
- VFQFPN4x4-8L and SO-8 and packages

#### **Applications**

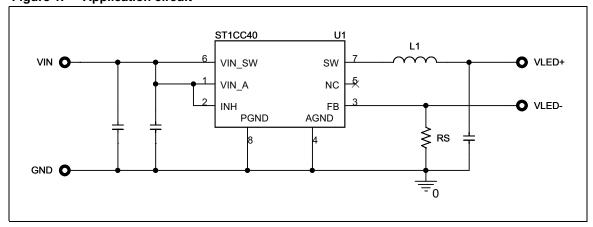
- LED driving, MR16 bulb replacement
- General lighting, architectural lighting



#### **Description**

The ST1CC40 is a fixed frequency step-down DC-DC converter designed to operate as constant current source. The device operates with an input supply range from 3.5 V to 18 V and is able to provide up to 3 A output current with accurate regulation which makes it suitable to drive high power LEDs. Synchronous rectification improves overall system performance. The current is set by an external resistor through 100 mV Typ feedback voltage thus minimizing the power loss on the sensing resistor. 850 kHz switching frequency combined with low impedance ceramic capacitors minimize possible output ripple. The device is protected against thermal overheating, switching over current and output short circuit. The ST1CC40 is available in VFQFPN 4mm x 4mm 8 leads package, and standard SO-8.





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Content ST1CC40

# Content

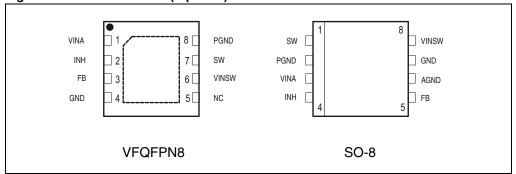
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ST1CC40 Pin settings

# 1 Pin settings

#### 1.1 Pin connection

Figure 2. Pin connection (top view)



## 1.2 Pin description

Table 1. Pin description

N	N.		N. Type		Description	
VFQFPN	S08-BW	Турс	Description			
1	3	VINA	Unregulated DC input voltage			
2	4	INH	Inhibit control input. Logic low disables operation. Toggling this pin with a periodic logic square wave of varying duty cycle at different frequencies controls the brightness of LEDs. Do leave this pin floating.			
3	5	FB	Feedback input. The control loop regulates the current in such a way that the average voltage at the FB input is 100 mV (nominal). The cathode of the LED and the sense resistor ( $R_{sns}$ ) to ground to set the LED current should be connected at this point. The LED current is given by $I_{LED} = V_{FB}/R_{sns}$			
4	6	AGND	Ground			
5	1	NC				
6	8	VINSW	Power input voltage			
7	1	SW	Regulator output switching pin			
8	2	PGND	Power ground			
-	7		Ground			

Maximum ratings ST1CC40

# 2 Maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>INSW</sub>	Power input voltage	-0.3 to 20	
V <sub>INA</sub>	Input voltage	-0.3 to 20	
V <sub>EN</sub>	Enable voltage	-0.3 to V <sub>INA</sub>	V
V <sub>SW</sub>	Output switching voltage	-1 to V <sub>IN</sub>	v
V <sub>PG</sub>	Power Good	-0.3 to V <sub>IN</sub>	
V <sub>FB</sub>	Feedback voltage	-0.3 to 2.5	
I <sub>FB</sub>	FB current	-1 to +1	mA
P <sub>TOT</sub>	Power dissipation at T <sub>A</sub> < 60°C		W
T <sub>OP</sub>	Operating junction temperature range	-40 to 125	°C
T <sub>stg</sub>	Storage temperature range	-55 to 150	°C

## 3 Thermal data

Table 3. Thermal data

Symbol	Parameter	Value	Unit	
В	Maximum thermal resistance	VFQFPN	40	°C/W
R <sub>thJA</sub>	junction-ambient (1)	SO8-BW	65	C/ VV

<sup>1.</sup> Package mounted on demonstration board.

## 4 Electrical characteristics

 $T_J \!\!=\!\! 25~^{\circ}\text{C},\, V_{CC} \!\!=\!\! 12~\text{V},\, \text{unless otherwise specified}.$ 

Table 4. Electrical characteristics

Complete	Parameter	Toot condition		11		
Symbol	Parameter	Test condition	Min	Тур	Max	Unit
V <sub>IN</sub>	Operating input voltage range	(1)	3.5		18	V
$V_{FB}$	Feedback voltage		90	97	104	mV
I <sub>FB</sub>	V <sub>FB</sub> pin bias current	(1)		50		nA
R <sub>DSON</sub> -P	High side switch on resistance	I <sub>SW</sub> =750mA		95		mΩ
R <sub>DSON</sub> -N	Low side switch on resistance	I <sub>SW</sub> =750mA		69		mΩ
I <sub>LIM</sub>	Maximum limiting current	(2)	4.0		6.0	Α
Oscillator						
F <sub>SW</sub>	Switching frequency		0.7	0.850	1	MHz
D	Duty Cycle	(2)	0		100	%
DC characte	eristics					
IQ	Quiescent current	V <sub>INH</sub> >1.2V		1.5	2.5	mA
I <sub>QST-BY</sub>	Total stand-by quiescent current	V <sub>INH</sub> <0.4V		2	15	μА
Enable						
V	INII I dhe wa aha ahal a salda as a	Device ON level	1.2			
V <sub>INH</sub>	INH threshold voltage	Device OFF level			0.4	V
I <sub>INH</sub>	INH current			2		μΑ
Soft start				•		•
T <sub>SS</sub>	Soft start duration			1		ms
Protection			•	•		,
_	Thermal shutdown			150		00
T <sub>SHDN</sub>	Hysteresis			15		°C

Specification referred to T<sub>J</sub> from -40 to +125°C. Specification in the -40 to +125°C temperature range are assured by design, characterization and statistical correlation.

<sup>2.</sup> Guaranteed by design.

### 5 Functional description

The ST1CC40 is a constant frequency, current mode regulator with an internal power switch capable of generating 3 A constant current output. The architecture of the device is showed in the block diagram here below. The synchronous rectification saves the use of an external schottky diode and allows higher efficiency. The ST1CC40 is suitable to drive high power brightness LEDs.

0.85 MHz Typ switching frequency allows using tiny surface-mount components. The typical application requires only few external components 3 capacitors and the inductor. The LED brightness can be controlled by applying a periodic pulse signal to the INH/DIM pin and varying its duty cycle. This method controls the average light output by pulsing the LED current between the set value and zero and is called PWM dimming. Inhibit pin is used also to disable (logic low < 0.4 V) or enable (logic high >1.2 V) the device. It can be connected to  $V_{\rm inA}$  if not used. The ST1CC40 is provided with thermal shut down protection activated at 150 °C typ.Cycle-by-cycle over-current protection, protect the application and the regulator against over load outputs. An Internal soft start for start-up current limitation and power on delay helps to reduce the inrush current during start-up. The ST1CC40 offer 0.1 V Typ voltage reference with 5% max spread thus minimizes the power loss on the current sensing resistor.

The output current flowing through the LED string is determined by the value of the resistor  $R_{SNS}$  and is calculated by the following Equation:

R<sub>SNS</sub>=V<sub>FB</sub>/I<sub>OUT</sub>

#### Where:

- R<sub>SNS</sub> is the value of the sensing resistor
- V<sub>ER</sub> is the reference voltage for the ST1CC40 in Volt (0.1 V typ)
- I<sub>OUT</sub> is the desired DC current in the LED string in Ampere

Figure 3. **Block diagram** VINSW\_ OSC SENSE  $(\mp$ COMP REGULATOR UVLO MOSFET CONTROL COMP OTP E/A SHUT-DOWN SOFTSTART  $O_{\overline{\mathsf{GNDA}}}$ INH GNDP

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## 6 Package mechanical data

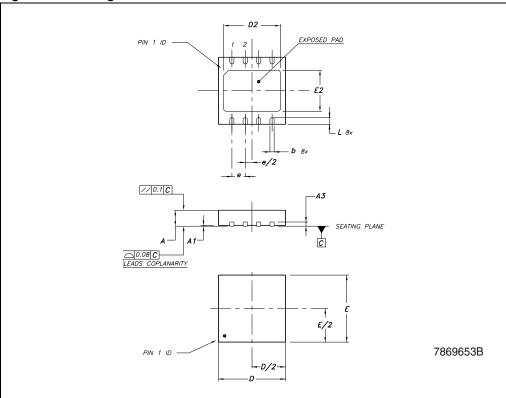
In order to meet environmental requirements, ST offers these devices in different grades of  $\mathsf{ECOPACK}^{\mathbb{B}}$  packages, depending on their level of environmental compliance.  $\mathsf{ECOPACK}^{\mathbb{B}}$  specifications, grade definitions and product status are available at:  $\mathit{www.st.com}$ .  $\mathsf{ECOPACK}^{\mathbb{B}}$  is an ST trademark.

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Table 5. VFQFPN8 (4x4x1.08 mm) mechanical data

Dim.		mm			inch	
Dilli.	Min	Тур	Max	Min	Тур	Max
Α	0.80	0.90	1.00	0.0315	0.0354	0.0394
A1		0.02	0.05		0.0008	0.0020
A3		0.20			0.0079	
b	0.23	0.30	0.38	0.009	0.0117	0.0149
D	3.90	4.00	4.10	0.153	0.157	0.161
D2	2.82	3.00	3.23	0.111	0.118	0.127
E	3.90	4.00	4.10	0.153	0.157	0.161
E2	2.05	2.20	2.30	0.081	0.087	0.091
е	_	0.80	_	_	0.031	
L	0.40	0.50	0.60	0.016	0.020	0.024

Figure 4. Package dimensions



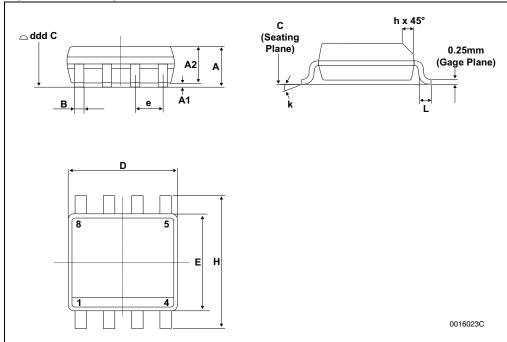
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Table 6. SO8-BW mechanical data

Dim	mm			inch		
Dim	Min	Тур	Max	Min	Тур	Max
Α	135		1.75	0.053		0.069
A1	0.10		0.25	0.004		0.001
A2	1.10		1.65	0.043		0.065
В	0.33		0.51	0.013		0.020
С	0.19		0.25	0.007		0.01
D <sup>(1)</sup>	4.80		5.00	0.1890	0.1929	0.1969
E	3.80		4.00	0.15		0.157
е		1.27			0.050	
Н	5.80		6.20	0.228		0.244
h	0.25		0.50	0.0098		0.0197
L	0.40		1.27	0.0157		0.0500
k			0°(min.),	8° (max.)		
ddd			0.10			0.0039

Dimensions D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15mm (.006inch) in total (both side).

Figure 5. Package dimensions



Order codes ST1CC40

## 7 Order codes

Table 7. Ordering information

Order codes	Package
ST1CC40PUR	VFQFPN 4x4 8L
ST1CC40DR	SO8-BW

ST1CC40 Revision history

# 8 Revision history

Table 8. Document revision history

Date	Revision	Changes
04-Mar-2011	1	First release

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