

MUR1020CT MURB1020CT MURB1020CT-1

Ultrafast Rectifier

Features

- · Ultrafast Recovery Time
- Low Forward Voltage Drop
- Low Leakage Current
- 175°C Operating Junction Temperature

 $t_{rr} = 25ns$

 $I_{F(AV)} = 10Amp$

 $V_{R} = 200V$

Description/Applications

International Rectifier's MUR.. series are the state of the art Ultra fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultra fast recovery time.

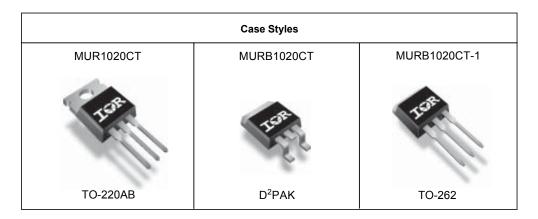
The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC-DC converters as well as free-wheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

Absolute Maximum Ratings

	•			
	Parameters		Max	Units
V _{RRM}	Peak Repetitive Peak Reverse Voltage		200	V
I _{F(AV)}	Average Rectified Forward Current	Per Leg	5	А
	Total Device, (Rated V _R), T _C = 149°C	Total Device	10	
I _{FSM}	Non Repetitive Peak Surge Current	PerLeg	50	
I _{FM}	Peak Repetitive Forward Current	PerLeg	10	
	(Rated V_R , Square wave, 20 KHz), T_C = 149°C			
T _{.I} , T _{STG}	Operating Junction and Storage Temperatures		- 65 to 175	°C



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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameters	Min	Тур	Max	Units	Test Conditions
V _{BR} , V _r	Breakdown Voltage, Blocking Voltage	200	-	-	٧	I _R = 100μA
V _F	Forward Voltage	-	0.87	0.99	V	I _F = 5A, T _J = 125°C
		-	1.02	1.20	V	I _F = 10A, T _J = 125°C
		-	1.12	1.25	V	I _F = 10A, T _J = 25°C
I _R	Reverse Leakage Current	-	-	10	μA	$V_R = V_R$ Rated
		-	-	250	μA	$T_J = 150$ °C, $V_R = V_R$ Rated
Ст	Junction Capacitance	-	8	-	pF	V _R = 200V
Ls	Series Inductance	-	8.0	-	nΗ	Measured lead to lead 5mm from package body

Dynamic Recovery Characteristics @ T_J = 25°C (unless otherwise specified)

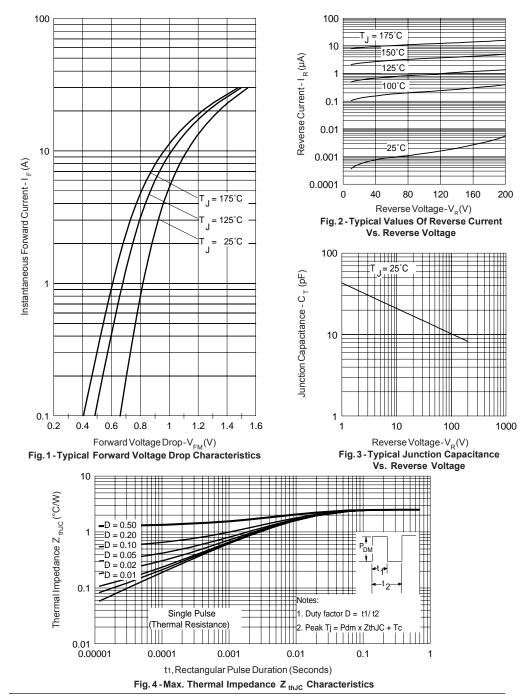
	Parameters	Min	Тур	Max	Units	Test Condition	s
t _{rr}	Reverse Recovery Time	-	-	35	ns	I _F = 1.0A, di _F /dt = 50A/μs, V _R = 30V	
		-	-	25		I _F = 0.5A, I _R = 1.0A	A, I _{REC} = 0.25A
		-	24	-		T _J = 25°C	I _F = 5A
			35			T _J = 125°C	$V_R = 160V$
I _{RRM}	Peak Recovery Current	-	3.3	-	Α	T _J = 25°C	di _F /dt = 200A/μs
		-	5.0	-		T _J = 125°C	
Q _{rr}	Reverse Recovery Charge	-	33	-	nC	T _J = 25°C	
		-	76	-		T _J = 125°C	

Thermal - Mechanical Characteristics

	Parameters	Min	Тур	Max	Units
TJ	Max. Junction Temperature Range	-	-	- 65 to 175	°C
T _{Stg}	Max. Storage Temperature Range	-	-	- 65 to 175	
R _{thJC}	Thermal Resistance, Junction to Case Per Leg	-	-	5	°C/W
R _{thJA}	Thermal Resistance, Junction to Ambient Per Leg	-	-	50	
R _{thCS} ^①	Thermal Resistance, Case to Heatsink	-	0.5	-	
Wt	Weight	-	2.0	-	g
		-	0.07	-	(oz)
	Mounting Torque	6.0	-	12	Kg-cm
		5.0	-	10	lbf.in

① Mounting Surface, Flat, Smooth and Greased

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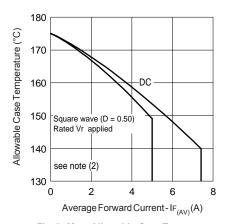


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current

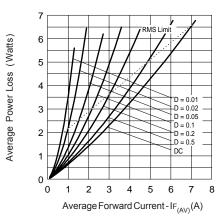


Fig. 6-Forward Power Loss Characteristics

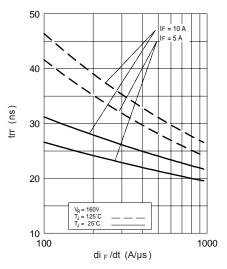


Fig. 7 - Typical Reverse Recovery vs. di $_{\rm F}$ /dt

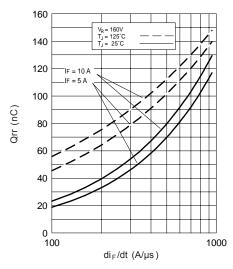


Fig. 8 - Typical Stored Charge vs. di $_{\text{F}}$ /dt

 $\begin{aligned} &\text{(2) Formula used: } T_{\text{C}} = T_{\text{J}} - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ &\text{Pd} = \text{Forward Power Loss} = I_{\text{F(AV)}} \times \text{V}_{\text{FM}} \textcircled{0} (I_{\text{F(AV)}} / D) \quad (\text{see Fig. 6}); \\ &\text{Pd}_{\text{REV}} = \text{Inverse Power Loss} = \text{V}_{\text{R1}} \times I_{\text{R2}} (1 - D); \ I_{\text{R2}} \textcircled{0} \times \text{V}_{\text{R1}} = \text{rated V}_{\text{R2}} \end{aligned}$

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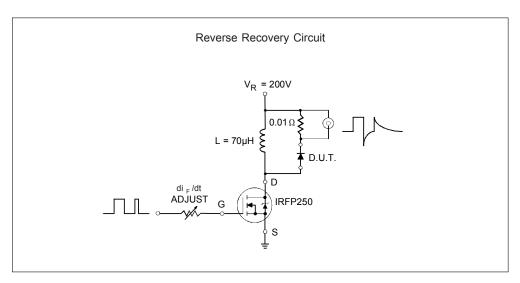


Fig. 9- Reverse Recovery Parameter Test Circuit

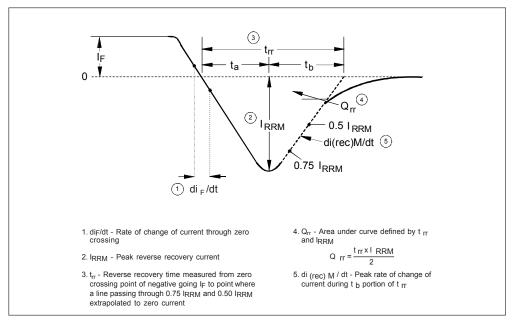
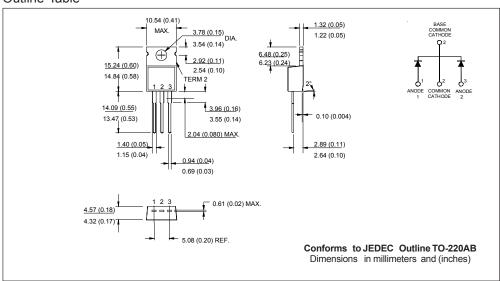
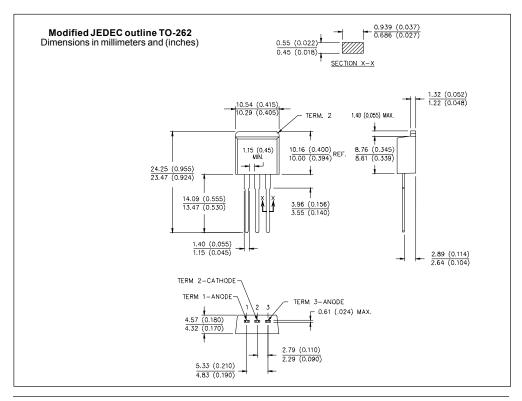


Fig. 10 - Reverse Recovery Waveform and Definitions

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Outline Table

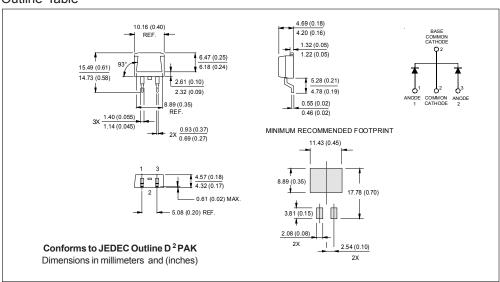




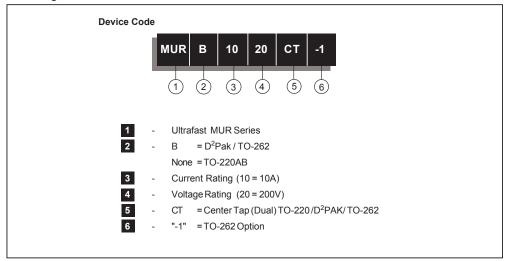
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Outline Table



Ordering Information Table



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