

HEXFRED® Ultrafast Soft Recovery Diode, 80 A



SOT-227

PRODUCT SUMMARY				
V_R	1200 V			
V _F (typical)	2.6 V			
t _{rr} (typical)	25 ns			
I _{F(DC)} at T _C	40 A at 78 °C			

FEATURES

- Fast recovery time characteristic
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- UL approved file E78996
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level



The dual diode series configuration (HFA80FA120P) is used for output rectification or freewheeling/clamping operation and high voltage application.

The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as HV power supplies, electronic welders, motor control and inverters.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		1200	V	
Continuous forward current	I _F	T _C = 78 °C	40		
Single pulse forward current	I _{FSM}	T _J = 25 °C	400	Α	
Maximum repetitive forward current	I _{FRM}	Rated V _R , square wave, 20 kHz, T _C = 60 °C	72		
Maximum navva dissination	р	T _C = 25 °C	178	10/	
Maximum power dissipation P _D		T _C = 100 °C	71	W	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	
Operating junction and storage temperature range	T _J , T _{Stg}		- 55 to + 150	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	Ι _R = 100 μΑ		1200	-	-	
Forward voltage V		I _F = 25 A	See fig. 1	-	2.6	3.0	V
	V_{FM}	I _F = 40 A		-	2.9	3.3	
		I _F = 80 A, T _J = 125 °C		-	3.4	-	
Develope legister of the legis		V _R = V _R rated	Cooffe O	-	2.0	-	μΑ
Reverse leakage current	I _{RM}	$T_J = 125$ °C, $V_R = 0.8 \times V_R$ rated	See fig. 2	-	0.5	2	mA
Junction capacitance	C _T	V _R = 200 V See fig. 3		ı	43	-	pF

HFA80FA120P

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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	25	-	
Reverse recovery time	ecovery time $t_{rr} \qquad T_{J} = 25 ^{\circ}\text{C}$ $T_{J} = 125 ^{\circ}\text{C}$	T _J = 25 °C		-	52	-	ns
		T _J = 125 °C		-	110	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	5.9	-	Α
	IRRM	T _J = 125 °C		-	10.8	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	160	-	nC
		T _J = 125 °C		-	630	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	Б		-	-	0.7	
Junction to case, both legs conducting	- R _{thJC}		-	-	0.35	°C/W
Case to heatsink	R _{thCS}	Flat, greased and surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	Nm

Document Number: 94075



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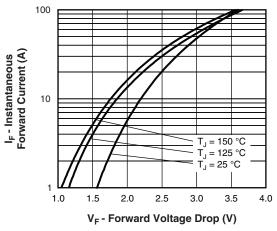


Fig. 1 - Typical Forward Voltage Drop Characteristics

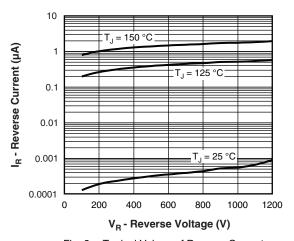


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

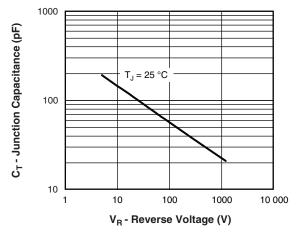


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

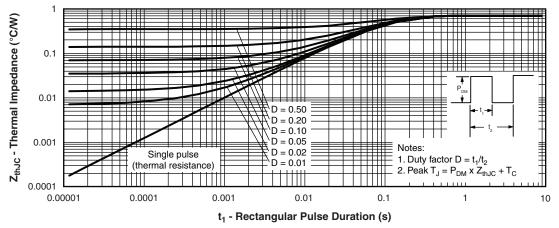


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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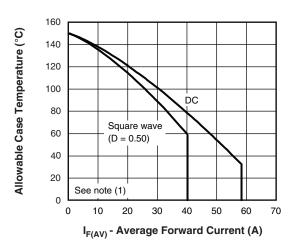


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

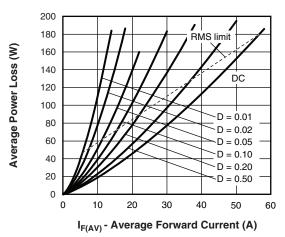


Fig. 6 - Forward Power Loss Characteristics

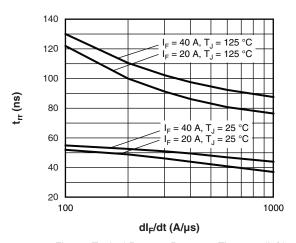


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

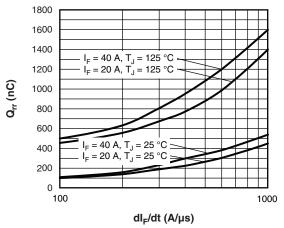


Fig. 8 - Typical Stored Charge vs. dI_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$



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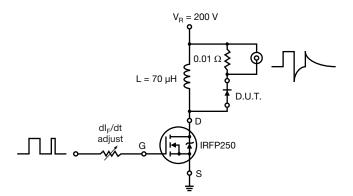
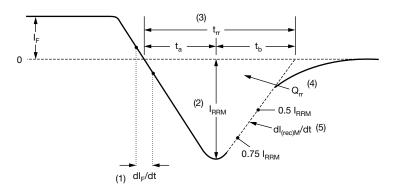


Fig. 9 - Reverse Recovery Parameter Test Circuit



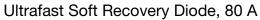
- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RBM} and 0.50 I_{RBM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dl_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

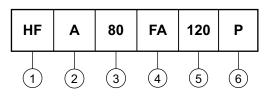
$\mathsf{HEXFRED}^{\circledR}$





ORDERING INFORMATION TABLE

Device code



1 - HEXFRED® family

- Process designator (A = Electron irradiated)

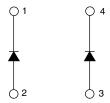
3 - Average current (80 = 80 A)

Package outline (FA = SOT-227)

Voltage rating (120 = 1200 V)

6 - P = Lead (Pb)-free

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95036</u>					
Packaging information	www.vishay.com/doc?95037				

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