

Vishay Semiconductors

Insulated Ultrafast Rectifier Module, 60 A



SOT-227

PRODUCT SUMMARY				
V_{R}	200 V			
I _{F(AV)} at T _C = 100 °C	60 A			
t _{rr}	27 ns			

FEATURES

- Two fully independent diodes
- · Ceramic fully insulated package $(V_{ISOL} = 2500 V_{AC})$
- · Ultrafast reverse recovery
- Ultrasoft reverse recovery current shape
- · Low forward voltage
- Optimized for power conversion: welding and industrial SMPS applications
- Industry standard outline
- Plug-in compatible with other SOT-227 packages
- · Easy to assemble
- · Direct mounting to heatsink
- UL approved file E78996



- Compliant to RoHS directive 2002/95/EC
- · Designed and qualified for industrial level

DESCRIPTION

The UFB60FA20P insulated modules integrate two state of the art Vishay Semiconductors ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The planar structure of the diodes, and the platinum doping life time control, provide a ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, dc-to-dc converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V_R		200	V	
Continuous forward current per diode	l _F	T _C = 100 °C	30	А	
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	250		
Maximum power dissipation per module	P_{D}	T _C = 100 °C	53	W	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 150	°C	

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ELECTRICAL SPECIFICATIONS PER DIODE (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	$I_R = 100 \mu A$	200	-	-	
Farmend with an	W	I _F = 30 A	-	0.96	1.08	V
Forward voltage	orward voltage V _{FM}	I _F = 30 A, T _J = 150 °C	-	0.78	0.86	
Reverse leakage current I _{RM}		$V_R = V_R$ rated	-	-	100	μΑ
	T _J = 150 °C, V _R = V _R rated	-	-	1.0	mA	
Junction capacitance	C _T	V _R = 200 V	-	119	-	pF

DYNAMIC RECOVERY CHARACTERISTICS PER DIODE (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	27	
Reverse recovery time	t _{rr}	T _J = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 100 \text{ V}$	-	31	-	ns
		T _J = 125 °C		-	51	-	
Peak recovery current I _{RI}	I _{RRM}	T _J = 25 °C		-	2.7	-	А .
		T _J = 125 °C		-	6.8	-	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	41	-	nC
	T _J = 125 °C		-	174	-	iiC	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	В		-	-	1.9	°C/W
Junction to case, both leg conducting	R _{thJC}		-	-	0.95	K/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	Nm



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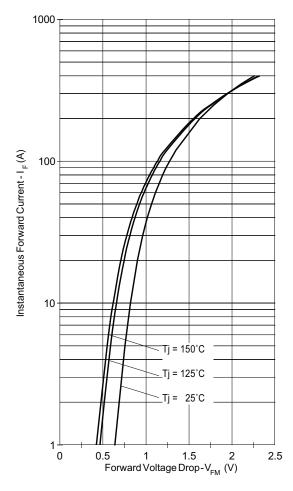


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Diode)

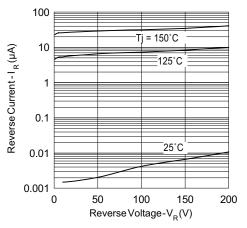


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

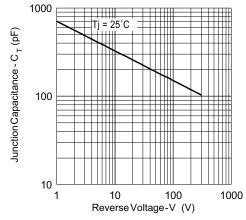


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

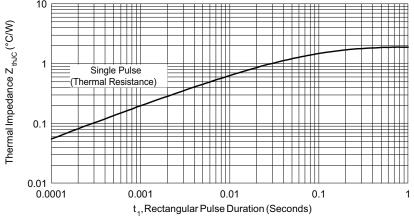


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Diode)

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70

If = 30A



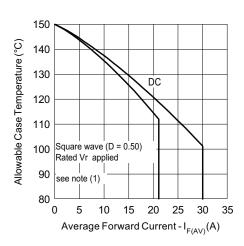


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

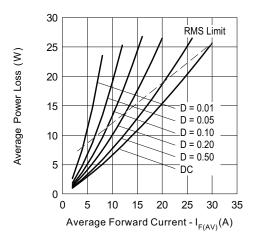
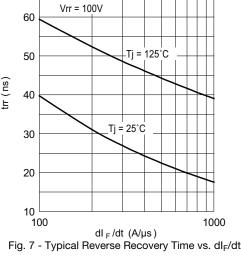


Fig. 6 - Forward Power Loss Characteristics (Per Leg)



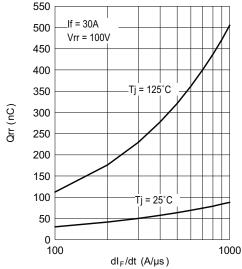


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = Rated V_R



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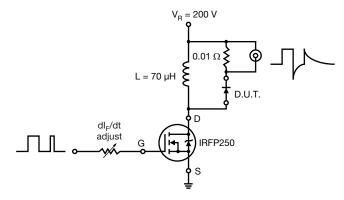
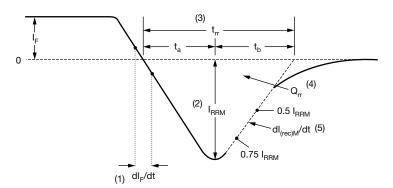


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

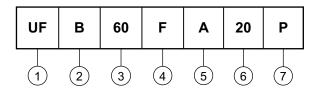
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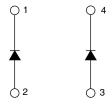
ORDERING INFORMATION TABLE

Device code



- 1 Ultrafast rectifier
- 2 Ultrafast Pt diffused
- 3 Current rating (60 = 60 A)
- Circuit configuration (2 separate diodes, parallel pin-out)
- 5 Package indicator (SOT-227 standard isolated base)
- 6 Voltage rating (20 = 200 V)
- 7 P = Lead (Pb)-free

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95036</u>						
Packaging information	www.vishay.com/doc?95037					

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