Unit: mm

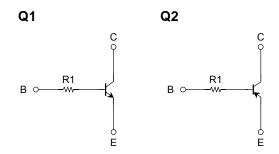
TOSHIBA Transistor Silicon NPN · PNP Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

RN4991FS

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

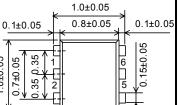
- Two devices are incorporated into a fine pitch small mold (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

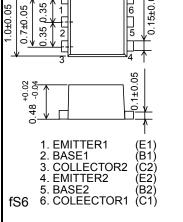
Equivalent Circuit and Bias Resistor Values



R1: $10k\Omega$

(Q1, Q2 common)





2-1F1D

Weight: 0.001g (typ.)

JEDEC JEITA

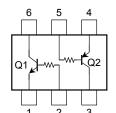
TOSHIBA

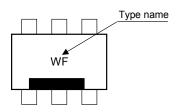
Equivalent Marking

Circuit

(top

view)





Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	20	٧
Collector-emitter voltage	V _{CEO}	20	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	I _C	50	mA

Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-20	V
Collector-emitter voltage	V _{CEO}	-20	٧
Emitter-base voltage	V _{EBO}	-5	V
Collector current	Ic	-50	mA

Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P _C (Note)	50	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Total rating

Electrical Characteristics (Ta = 25°C) (Q1)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = 20 \text{ V}, I_E = 0$	_	_	100	nA
Emitter cut-off current	I _{EBO}	$V_{EB} = 5 \text{ V}, I_{C} = 0$	_	_	100	nA
DC current gain	h _{FE}	$V_{CE} = 5 \text{ V}, I_{C} = 1 \text{ mA}$	300	_	_	
Collector-emitter saturation voltage	V _{CE} (sat)	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$	_	_	0.15	V
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$	_	1.2	_	pF

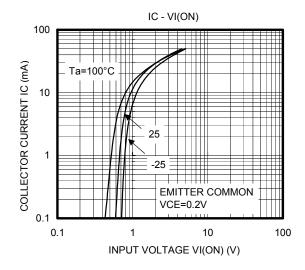
Electrical Characteristics (Ta = 25°C) (Q2)

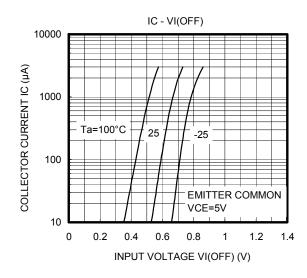
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$	_	_	-100	nA
Emitter cut-off current	I _{EBO}	$V_{EB} = -5 \text{ V}, I_C = 0$	_	_	-100	nA
DC current gain	h _{FE}	$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$	300	_	_	
Collector-emitter saturation voltage	V _{CE} (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	_	-0.15	V
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	1.2	_	pF

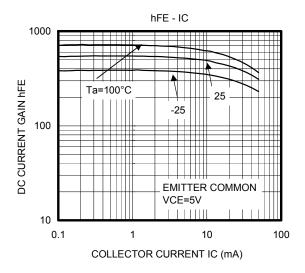
Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

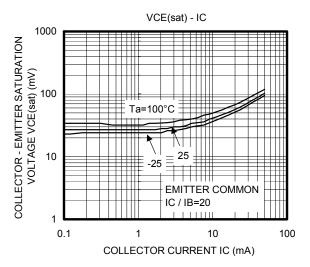
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1		8	10	12	kΩ

Q1



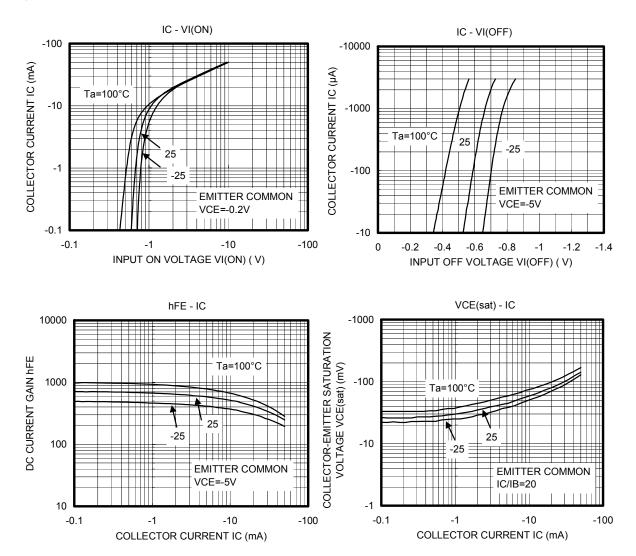






4

Q2



5 2004-06-30

HANDLING PRECAUTION

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

6 2004-06-30

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030619EAA

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