Unit: mm

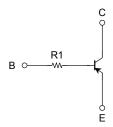
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

RN2972FS, **RN2973FS**

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine pitch small mold (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.
- Complementary to RN1972FS, RN1973FS

Equivalent Circuit and Bias Resistor Values



Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

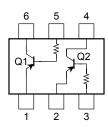
Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-20	V
Collector-emitter voltage	V_{CEO}	-20	V
Emitter-base voltage	V_{EBO}	-5	V
Collector current	IC	-50	mA
Collector power dissipation	P _C (Note)	50	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Total rating

1.0±0.05 0.8±0.05 0.1±0.05 0.1±0.05 0.15±0.05 0.7 ± 0.05 1±0.05 1.EMIITTER1 (E1) 2.EMITTER2 (E2) 3.BASE2 (B2) 4.COLLECTOR2 (C2)5.BASE1 6.COLLECTOR1 fS6 JEDEC JEITA **TOSHIBA** 2-1F1C

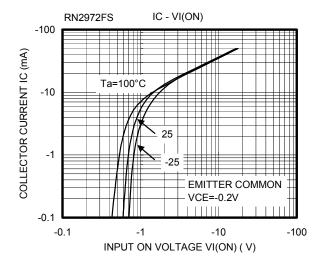
Weight: 0.001g (typ.)

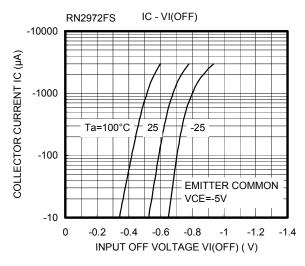
Equivalent Circuit (top view)

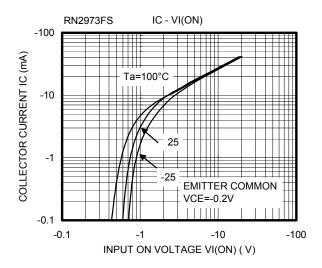


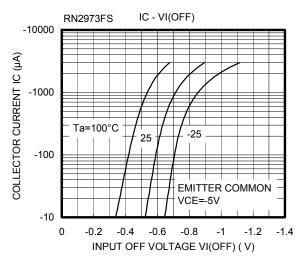
Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

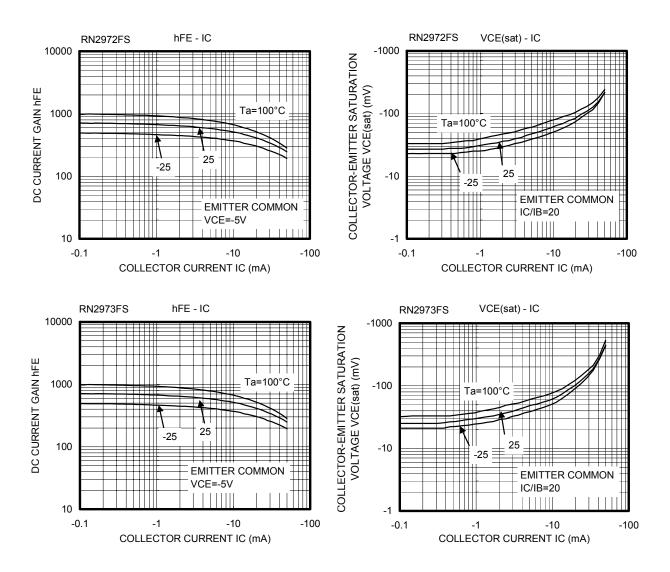
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$	_	_	-100	nA
Emitter cut-off curren	t	I _{EBO}	$V_{EB} = -5 \text{ V}, I_{C} = 0$	_	_	-100	nA
DC current gain		h _{FE}	$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$	300	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_		-0.15	V
Collector output capacitance		C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	1.2	_	pF
Input resistor	RN2972FS	R1	_	17.6	22	26.4	kΩ
	RN2973FS			37.6	47	56.4	











Type Name	Marking	
RN2972FS	6 5 4 Type name KH 1 2 3	
RN2973FS	6 5 4 Type name KJ 1 2 3	

HANDLING PRECAUTION

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

4

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