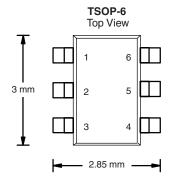




# P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)	
	$0.027$ at $V_{GS} = -4.5 \text{ V}$	- 7		
- 20	$0.035$ at $V_{GS} = -2.5 \text{ V}$	- 6.2	21	
	0.048 at V <sub>GS</sub> = - 1.8 V	- 5.2		



Ordering Information: Si3493DV-T1-E3 (Lead (Pb)-free)

Si3493DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: 93xxx

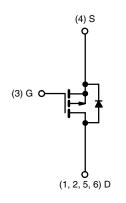
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET: 1.8 V Rated
- Ultra-Low On-Resistance
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

- Load Switch
- PA Switch
- Battery Switch



P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 20		V
Gate-Source Voltage		V <sub>GS</sub>	± 8		
O " D : O . (T . 450.00)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 7	- 5.3	٨
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		- 3.6	- 3.9	
Pulsed Drain Current		I <sub>DM</sub>	- 20		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.7	- 0.9	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.0	1.1	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C		1.0	0.6	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manifestore Longitian to Application	t ≤ 5 s	R <sub>thJA</sub>	45	62.5	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		90	110	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	25	30	

#### Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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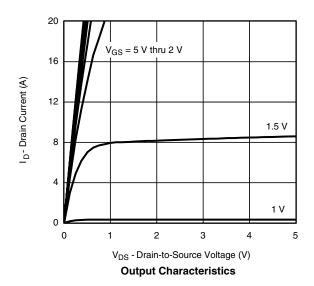
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	- 0.40		- 1	V	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1	μΑ	
		$V_{DS}$ = - 20 V, $V_{GS}$ = 0 V, $T_J$ = 85 °C			- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V$ , $V_{GS} = -4.5 V$	- 20			Α	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 7 A		0.022	0.027		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 6.2 A		0.029	0.035	Ω	
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 3 A		0.039	0.048		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 7 A		25		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V		- 0.7	- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			21	32	nC	
Gate-Source Charge	Q <sub>gs</sub>	$Q_{gs}$ $V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -7 \text{ A}$		2.6			
Gate-Drain Charge	$Q_{gd}$			6			
Turn-On Delay Time	t <sub>d(on)</sub>			20	30		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$		40	60	ns	
Turn-Off Delay Time		$I_D\cong$ - 1 A, $V_{GEN}=$ - 4.5 V, $R_g=$ 6 $\Omega$		125	190		
Fall Time	t <sub>f</sub>			85	130		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = -1.7 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$		64	90		

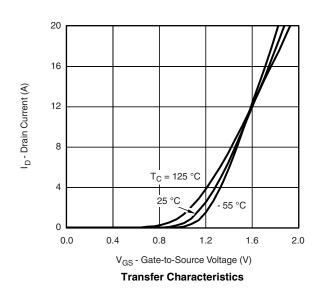
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



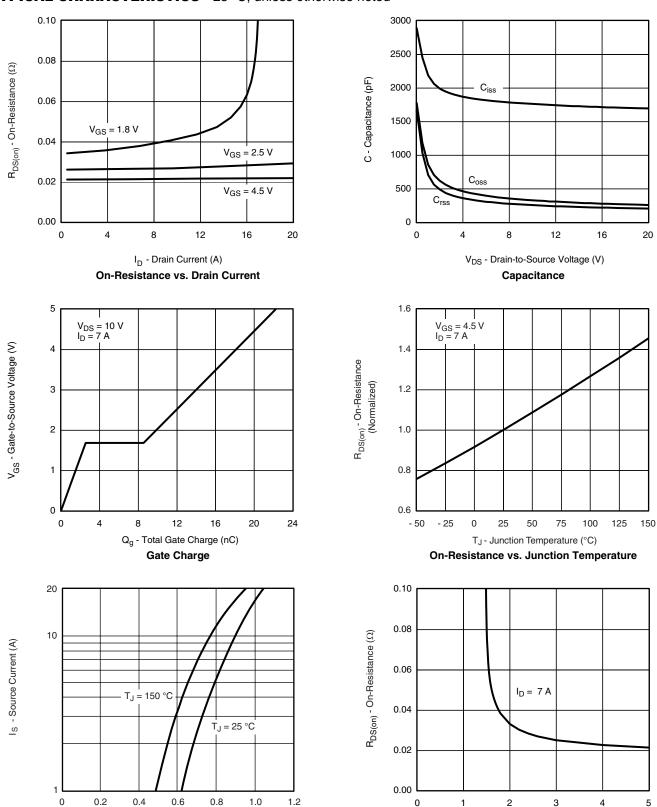








#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



V<sub>SD</sub> - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

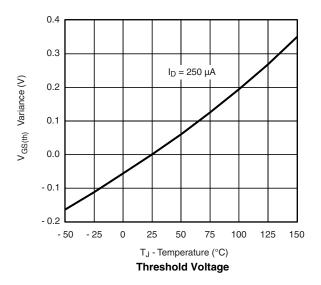
V<sub>GS</sub> - Gate-to-Source Voltage (V)

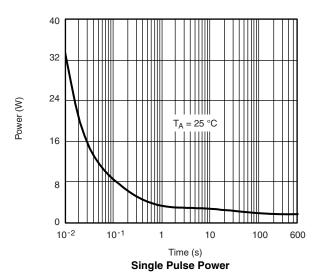
On-Resistance vs. Gate-to-Source Voltage

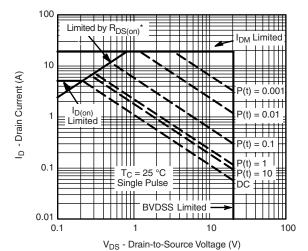
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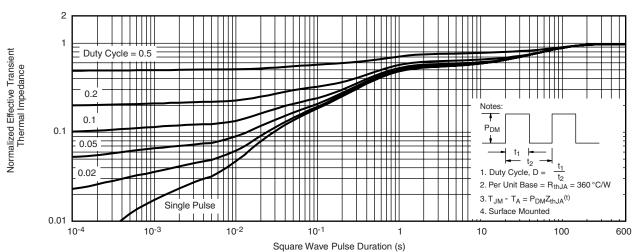
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







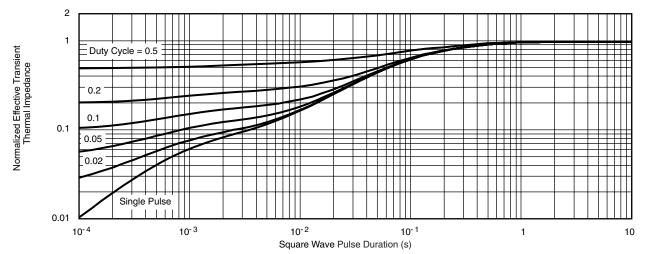
\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified **Safe Operating Area** 



Normalized Thermal Transient Impedance, Junction-to-Ambient



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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Document Number: 91000 www.vishay.com
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