Vishay Dale

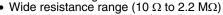


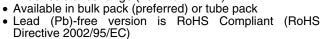
Thick Film Resistor Networks Single-In-Line, Coated SIP 01, 03, 05 Schematics



FEATURES

- Body height: "A" profile = 0.195" [4.95 mm]; "B" profile = 0.295" [7.50 mm]
- "A" profile standard in 4 thru 12 pins
- Thick film resistive elements
- Reduces total assembly costs
- Resistor elements protected by tough epoxy conformal coating











STANDARD ELECTRICAL SPECIFICATIONS							
GLOBAL MODEL/ SCHEMATIC	PACKAGE HEIGHT	RESISTOR POWER RATING max. at 70 °C ¹⁾	$\begin{array}{c} \textbf{RESISTANCE} \\ \textbf{RANGE} \\ \Omega \end{array}$	TEMP. COEFFICIENT (- 55 °C to + 125 °C) ppm/°C	STANDARD TOLERANCE %	TCR TRACKING ¹⁾ (- 55 °C to + 125 °C) ppm/°C	OPERATING VOLTAGE V _{DC} max.
CSCxxx01	Α	0.20 W	10 - 50 50.1 - 2.2M	± 250 ± 100	± 2 (1 %) ²⁾	± 50	100
	В	0.25 W					
CSCxxx03	Α	0.30 W	10 - 50 50.1 - 2.2M	± 250 ± 100	± 2 (1 %) ²⁾	± 50	100
	В	0.40 W					
CSCxxx05	Α	0.20 W	10 - 50 50.1 - 2.2M	± 250 ± 100	± 2 (1 %) ²⁾	± 150	100
	В	0.25 W					

For resistor power ratings at + 25 °C see derating curves.
 See derating curves for Package Power Rating.

2. Contact factory for 1 %.

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GLOBAL PART NUMBER INFORMATION					
New Global Part Numbering: CSC08A03100RGDA (preferred part number format)					
C S C 0 8 A 0 3 1 0 0 R G D A					
GLOBAL MODEL PIN COUNT PACKAGE SCHEMATIC RESISTANCE CODE PACKAGING SPECIAL					
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$					
Historical Part Number example: CSC08A03101G (will continue to be accepted)					
CSC 08 A 03 101 G D03					
HISTORICAL PIN COUNT PACKAGE SCHEMATIC RESISTANCE TOLERANCE CODE PACKAGING					
New Global Part Numbering: CSC08A05131AGPA (preferred part number format)					
C S C 0 8 A 0 5 1 3 1 A G P A					
GLOBAL MODEL PIN COUNT PACKAGE SCHEMATIC RESISTANCE CODE PACKAGING SPECIAL					
CSC 04 = 4 Pin 08 = 8 Pin 12 = 12 Pin					
alpha modifier (see impedance codes table) PA = Tin/Lead, Bulk DA = Tin/Lead, Tube as applicable					
Historical Part Number example: CSC08A05221331G (will continue to be accepted)					
CSC 08 A 05 221 331 G P03					
HISTORICAL PIN COUNT PACKAGE SCHEMATIC RESISTANCE VALUE 1 TOLERANCE CODE PACKAGING					

Pb containing terminations are not RoHS compliant, exemptions may apply. Lead (Pb)-free version meets EIA/ECA-CB23 Rev. G whisker test requirements for Class 1A products.

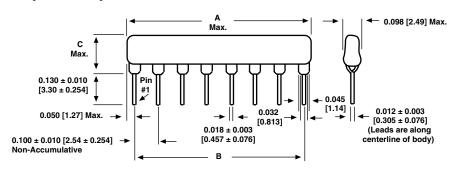


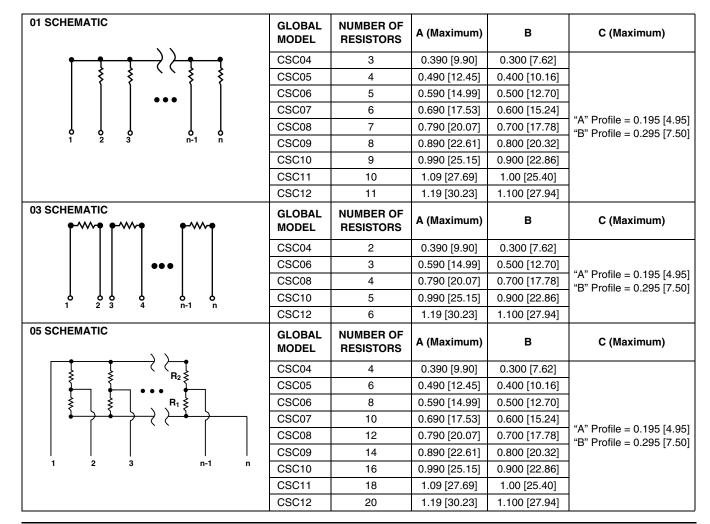
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TECHNICAL SPECIFICATIONS					
PARAMETER	UNIT	CSC SERIES			
Voltage Coefficient of Resistance	V _{eff}	< 50 ppm typical			
Dielectric Strength	V _{AC}	200			
Isolation Resistance (03 Schematic)	Ω	> 100M			
Operating Temperature Range	°C	- 55 to + 125			

DIMENSIONS in inches [millimeters]





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Thick Film Resistor Networks Single-In-Line, Coated SIP 01, 03, 05 Schematics



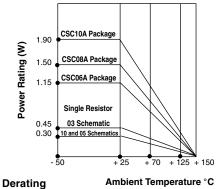
MECHANICAL SPECIFICATIONS				
Marking Resistance to Solvents:	Permanency testing per MIL-STD-202, Method 215			
Solderability:	Per MIL-STD-202, Method 208E, RMA flux			
Body:	High alumina, epoxy coated			
Terminals:	Solder plated leads			

STOCKED RESISTANCE VALUES IN OHMS ("G" TOLERANCE)

Standard E-24 resistance values stocked. Consult factory. Many dual terminator resistance values stocked. Consult factory.

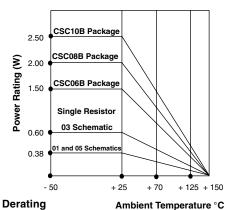
IMPEDANCE CODES					
CODE	R ₁ (Ω)	R ₂ (Ω)	CODE	R ₁ (Ω)	R ₂ (Ω)
500B	82	130	141A	270	270
750B	120	200	181A	330	390
800C	130	210	191A	330	470
990A	160	260	221B	330	680
101C	180	240	281B	560	560
111C	180	270	381B	560	1.2K
121B	180	390	501C	620	2.7K
121C	220	270	102A	1.5K	3.3K
131A	220	330	202B	зК	6.2K

"A" Profile



Ambient Temperature °C





"A" PROFILE + 70 °C	PACKAGE RATINGS
CSC12A	1.5 W
CSC11A	1.37 W
CSC10A	1.25 W
CSC09A	1.12 W
CSC08A	1.00 W
CSC07A	0.87 W
CSC06A	0.75 W
CSC05A	0.62 W
CSC04A	0.40 W

"B" PROFILE + 70 °C PACKAGE RATINGS				
CSC12B	1.90 W			
CSC11B	1.75 W			
CSC10B	1.60 W			
CSC09B	1.45 W			
CSC08B	1.30 W			
CSC07B	1.15 W			
CSC06B	1.00 W			
CSC05B	0.80 W			
CSC04B	0.60 W			

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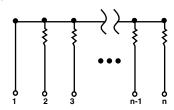


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CIRCUIT APPLICATIONS

01 Schematic

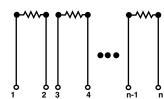


Bussed

The CSCxxx01 single-in-line resistor networks provide the user with nominally equal resistors, each connected to a common pin (Pin No. 1). Commonly used in the following applications:

- "Wired OR" Pull-up • Power Gate Pull-up
- Open Collector Pull-up
- TTL Input Pull-down
- MOS/ROM Pull-up/Pull-down TTL Unused Gate Pull-up
- * "A" profile standard, "B" Profile available.

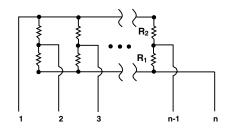
03 Schematic



The CSCxxx03 single-in-line resistor networks provide the user with nominally equal resistors. Each resistor is isolated from all others. Commonly used in the following applications:

- "Wired OR" Pull-up
- Long-Line Impedance Balancing
- Power Driven Pull-up
- LED Current Limiting • ECL Output Pull-down
- Power Gate Pull-up • Line Termination
- TTL Input Pull-down
- * "A" Profile standard, "B" Profile available.

05 Schematic



Dual Terminator

The CSCxxx05 circuits contain series pairs of resistors. Each series pair is connected between two common lines. The junction of these resistor pairs is connected to the input terminals. The 05 circuits are designed for TTL dual-line termination and pulse squaring.

* "A" profile standard, "B" Profile available.

PERFORMANCE				
TEST	CONDITIONS	MAX. ∆R (Typical Test Lots)		
Thermal Shock	5 cycles between - 65 °C and + 125 °C	± 0.50 % ΔR		
Short Time Overload	2.5 x rated working voltage, 5 seconds	± 0.25 % ΔR		
Low Temperature Operation	45 minutes at full rated working voltage at - 65 °C	± 0.25 % ΔR		
Moisture Resistance	240 hours with humidity ranging from 80 % RH to 98 % RH	± 1.00 % ΔR		
Resistance to Soldering Heat	Leads immersed in + 350 °C solder to within 1/16" of body for 3 seconds	± 0.25 % ΔR		
Shock	Total of 18 shocks at 100 g's	± 0.25 % ΔR		
Vibration	12 hours at maximum of 20 g's between 10 and 2000 Hz	± 0.25 % ΔR		
Load Life	1000 hours at + 70 °C, rated power applied 1.5 hours "ON", 0.5 hours "OFF" for full 1000 hours period. Derated according to the curve.	± 1.00 % ΔR		
Terminal Strength	4.5 pound pull for 30 seconds	± 0.25 % ΔR		
Insulation Resistance	10 000 MΩ (minimum)	-		
Dielectric Withstanding Voltage	No evidence of arcing or damage (200 V _{rms} for 1 minute)	-		

Document Number: 31509 Revision: 30-Jul-07





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