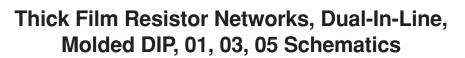
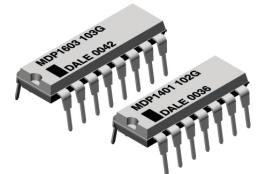
MDP 14, 16

Vishay Dale







FEATURES

- 0.160" [4.06mm] maximum seated height and rugged, molded case construction.
- · Highly stable thick film
- Low temperature coefficient (- 55°C to + 125°C) \pm 100ppm/°C
- · Reduces total assembly costs
- · Compatible with automatic inserting equipment
- Wide resistance range
- Uniform performance characteristics
- · Available in tube pack

STANDARD ELECTRICAL SPECIFICATIONS							
MODEL/ NO. OF PINS	SCHEMATIC	RESISTOR POWER RATING Max. @ 70°C*	RESISTANCE RANGE	STANDARD TOLERANCE		TCR TRACKING** (- 55°C to + 125°C)	WEIGHT
		W	Ω	%	ppm/°C	ppm/°C	g
MDP 14	01 03 05	0.125 0.250 0.125	10 - 2.2M 10 - 2.2M Consult factory	$\begin{array}{c} \pm \ 1, \pm \ 2, \pm \ 5 \\ \pm \ 1, \pm \ 2, \pm \ 5 \\ \pm \ 1, \pm \ 2, \pm \ 5 \end{array}$	± 100	± 50 ± 50 ± 100	1.3
MDP 16	01 03 05	0.125 0.250 0.125	10 - 2.2M 10 - 2.2M Consult factory	\pm 1, \pm 2, \pm 5 \pm 1, \pm 2, \pm 5 \pm 1, \pm 2, \pm 5 \pm 1, \pm 2, \pm 5	± 100	± 50 ± 50 ± 100	1.5

* For resistor power ratings @ + 25°C see derating curves.

** Tighter tracking available

ORDERING	INFORMATION					
01 and 03 Sc	hematics		01			
MDP MODEL	14 NUMBER OF PINS		03 SCHEMATIC	RES	101 ISTANCE VALUE	G TOLERANCE
				tolerand figures.	digits (3 for "F" se) are significant Last digit s number of zeros v.	$F = \pm 1\%$ $G = \pm 2\%$ $J = \pm 5\%$
05 Schemati	ic					
MDP MODEL	14 NUMBER OF PINS	05 SCHEMATIC	221 RESISTANCE VA	LUE R ₁	271 RESISTANCE VALUE R ₂	G TOLERANCE
					tolerance) are significant specifies the number to follow.	$F = \pm 1\%$ $G = \pm 2\%$ $J = \pm 5\%$
with 14 pins on	G = A dual-in-line thick filr 0.100" [2.54mm] centers, 00 ohm and a tolerance of	03 Schematic,	resisto	1-05-221/2 r network v	71G = A 14 pin dual-in-line th with 12 series pair of resistor: pair and a tolerance of $\pm 2\%$	s of 220 ohm

www.vishay.com 40 For Technical Questions, contact: ff2aresistors@vishay.com

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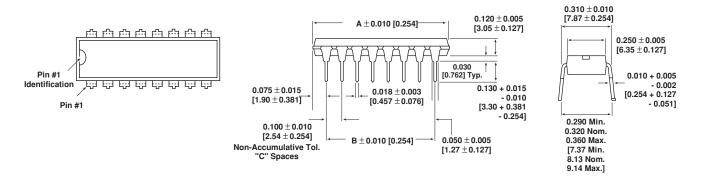


Thick Film Resistor Networks, DIP, Molded

MDP 14, 16

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DIMENSIONS in inches [millimeters]



MODEL	Α	В	С
MDP 14	0.750 [19.05]	0.600 [15.24]	6
MDP 16	0.850 [21.59]	0.700 [17.78]	7

TECHNICAL SPECIFICATIONS					
PARAMETER	UNIT	MDP-14	MDP-16		
Package Power Rating (Maximum at + 70°C)	w	1.73	1.92		
Voltage Coefficient of Resistance V _{eff} < 50ppm typical		om typical			
Dielectric Strength	VAC		200		
Insulation Resistance	Ω	> 10,000	M minimum		
Operating Temperature Range	°C	- 55	to + 125		
Storage Temperature Range:	°C	- 55 to + 150			

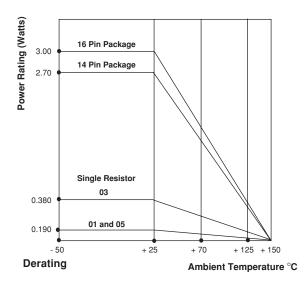
MECHANICAL SPECIFICATIONS				
Marking Resistance to Solvents:	Permanency testing per MIL-STD-202, Method 215.			
Solderability:	Per MIL-STD-202, Method 208E.			
Body:	Molded epoxy.			
Terminals:	Copper alloy, tin-lead plated.			
Weight:	14 pin = 1.3 grams; 16 pin = 1.5 grams			

MDP 14, 16

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CIRCUIT APPLICATIONS 01 Schematic 13 and 15 resistors with one pin common The MDPXX-01 circuit provides a choice of 13 and 15 nominally equal resistors, each connected between a common pin (14 and 16) and a discrete PC board pin. Commonly used in the following applications: MOS/ROM Pull-up/Pull-down • TTL Input Pull-down Open Collector Pull-up Digital Pulse Squaring • "Wired OR" Pull-up • TTL Unused Gate Pull-up Pin #1 MDP16-01 • Power Driven Pull-up • High Speed Parallel Pull-up **03 Schematic** 7 and 8 isolated resistors The MDPXX-03 provides a choice of 7 and 8 nominally equal resistors, each resistor isolated from all others and wired directly across. Commonly used in the following applications: · Long-line Impedance Balancing "Wired OR" Pull-up · Power Driven Pull-up LED Current Limiting Powergate Pull-up • ECL Output Pull-down Pin #1 MDP16-03 Line Termination • TTL Input Pull-down 05 Schematic TTL dual-line terminator; pulse squaring The MDPXX-05 circuit contains 12 and 14 series pair of resistors. Each series pair is connected between ground and a common line. The junction of these resistor pairs is connected to the input terminals. The 05 circuits are designed for TTL dual-line termination and pulse squaring.

MDP14-05, MDP16-05 Standard E-24 resistance values stocked. Consult factory

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PERFORMANCE					
TEST	CONDITIONS	MAX. ∆R (Typical Test Lots)			
Power Conditioning	1.5 rated power, applied 1.5 hours "ON" and 0.5 hour "OFF" for 100 hours \pm 4 hours at + 25°C ambient temperature	± 0.50% ∆R			
Thermal Shock	5 cycles between - 65°C and + 125°C	± 0.50% ΔR			
Short Time Overload	2.5 x rated working voltage 5 seconds	± 0.25% ΔR			
Low Temperature Operation	45 minutes at full rated working voltage at - 65°C	± 0.25% ΔR			
Moisture Resistance	240 hours with humidity ranging from 80% RH to 98% RH	± 0.50% ΔR			
Resistance to Soldering Heat	Leads immersed in + 350°C solder to within 1/16" of device body for 3 seconds	± 0.25% ΔR			
Shock	Total of 18 shocks at 100 G's	± 0.25% ΔR			
Vibration	12 hours at maximum of 20 G's between 10 and 2,000 Hz	± 0.25% ΔR			
Load Life	1000 hours at + 70°C, rated power applied 1.5 hours "ON, 0.5 hour "OFF" for full 1000 hour period. Derated according to the curve.	± 1.00% ∆R			
Terminal Strength	4.5 pound pull for 30 seconds	± 0.25% ΔR			
Insulation Resistance	10,000 Megohm (minimum)	_			
Dielectric Withstanding Voltage	No evidence of arcing or damage (200 VRMS for 1 minute)	_			