



Half Size Clock Oscillator Enable/Disable



The XO-52 series oscillator is half size, has tri-state enable/disable controlled function. The metal package with pin 4 case ground acts as shielding to minimize EMI radiation.

FEATURES

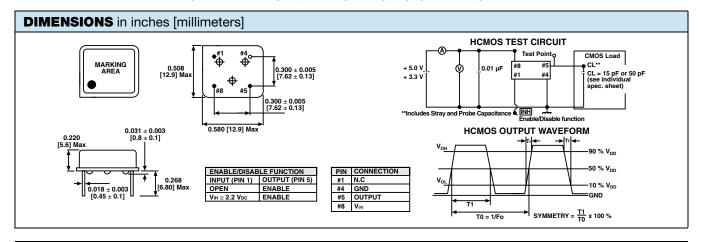
- Size: 8 pin half size
- · Industry standard
- Tri-state enable/disable
- Wide frequency range
- Low cost
- Resistance weld package
- 5 V
- Compliant to RoHS directive 2002/95/EC



STANDARD ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	CONDITION	VALUE				
Frequency range F _O		-	1.000 MHz to 100.000 MHz				
Frequency stability (1)		all conditions	± 25 ppm, ± 50 ppm, ± 100 ppm				
Operating temperature range	т		0 °C to 70 °C				
Operating temperature range	T _{OPR}	-	- 40 °C to + 85 °C (option)				
Storage temperature range	T _{STG}	-	- 55 °C to + 125 °C				
Power supply voltage V _{DD}		-	5.0 V ± 10 %				
Aging (first year)		25 °C ± 3 °C	± 5 ppm				
		1.000 MHz to 23.999 MHz	20 mA max.				
Supply current		24.000 MHz to 49.999 MHz	30 mA max.				
	I _{DD}	50.000 MHz to 69.999 MHz	40 mA max.				
		70.000 MHz to 100.000 MHz	60 mA max.				
Output symmetry	Sym	at ¹ / ₂ V _{DD}	40 %/60 % (45 %/55 % option)				
Rise time t _r		20 % V _{DD} to 80 % V _{DD}	10 ns max.				
Fall time t _f		80 % V _{DD} to 20 % V _{DD}	10 ns max.				
Output voltage	V _{OH}	-	90 % V _{DD} min.				
Output voltage	V _{OL}	-	10 % V _{DD} max.				
	TTL load	-	1 TTL to 10 TTL				
Output load		-	to 50M: 50 pF				
	HCMOS load	-	to 70M: 30 pF				
		-	to 100M: 15 pF				
Start-up time	t _s	-	10 ms max.				
Die 1 tri state franction			pin 1 = H or open (output active at pin 5)				
Pin 1, tri-state function		-	pin 1 = L (high impedance at pin 5)				

Note

(1) Include: 25 °C tolerance, operating temperature range, input voltage change, aging, load change, shock vibration



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Vishay Dale

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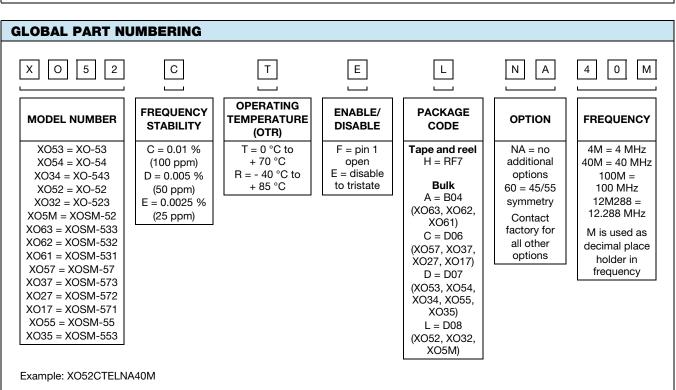


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ORDERING INFORMATION										
XO-52	В	R	E	40 M	e2					
MODEL	FREQUENCY STABILITY AA = 0.0025 % (25 ppm) A = 0.005 % (50 ppm) B = 0.01 % (100 ppm)	OTR blank = 0 °C to + 70 °C R = - 40 °C to + 85 °C	ENABLE/DISABLE blank = pin 1 open E = disable to tri-state	FREQUENCY/MHz	JEDEC LEAD (Pb)-FREE standard					

GLOBAL PART NUMBER									
X O 5 2 MODEL	FREQUENCY STABILITY	T OTR	E ENABLE/ DISABLE	PACKAGE CODE	N A OPTIONS	4 0 M FREQUENCY			



For technical questions, contact: frequency@vishay.com





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