Displaytech Ltd.

Website: www.displaytech-us.com

LCD Module Product Specification

Product: 2.4" TFT Display Module (240RGBx320DOTS)

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15 October 2009.

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REVISION RECORD

VERSION	CHANGES	DATE
1.0	Initial revision	3 August 2007
2.0	Complete new module	9 January 2008
3.0	IC was changed. Data updated on: Introduction, General specifications, Mechanical drawing, Interface description, Absolute maximum ratings, Electrical characteristics, and Optical characteristics	11 April 2008
4.0	Corrected the lifetime info on page 4. Added DT024TFT-TS mechanical drawing (P.6) and touch screen pinout connection (P.7)	6 November 2008
5.0	Delete touch screen option	31 December 2008
6.0	Add touch screen option	15 October 2009

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1. Introduction

SDT024TFT or **SDT024TFT-TS** is a display module that contains a TFT display with a 320 * 240 RGB resolution. The driver used for this project is the Ilitek **ILI9320** or **compatible** and can display 262K colors. The driver is mounted on the glass and the interconnection via FPC including components to drive the display module.

2. General Specifications

Item	Specification	Unit
LCD mode	Transmissive	
Decelotion	240(RGB)	Line
Resolution	320	Line
Viewing ones	39.12	mm
Viewing area	51.36	mm
A ativa anna	36.72	mm
Active area	48.96	mm
Driver IC	ILI9320	
Interface type	System parallel / RGB (1)	
Colours	262K	
Operation temperature range	-20~70	°C
Storage temperature range	-30~80	°C

Remarks:

- (1) Serial interface is available, but not recommendable, as the speed of it is very slow.
- (2) Recommended mating connector: Hirose FH19SC-45S-0.5SH, FH12S-45S-0.5SH; or Molex 0512964593, 0512964594; or equivalent
- (3) Orientation: both Portrait and Landscape mode are available (controlled by software, refer to IC spec)

Component Life Cycle

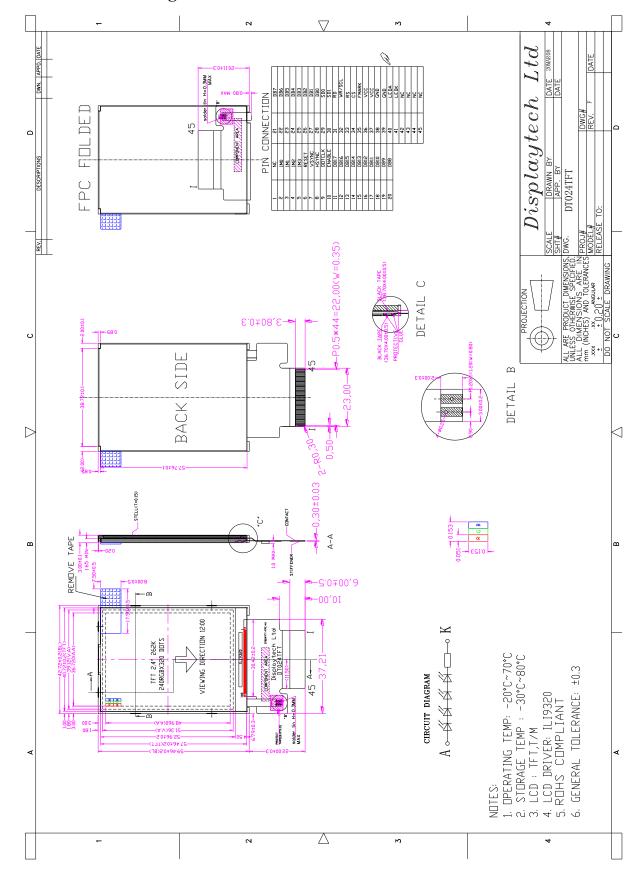
- 1) Storage Life: min. 1 Year
- 2) Operation Life (*1): min. 43 x 10³ h (24h per day x 7 days per week x 52 weeks / year x 5 years) (Not include backlight)
- 3) Storage and Operation Life Times are defined for a temperature of +25°C

Notes:

- *1. Operation life ends when one of the listed faults occurs:
 - The on/off response-times reach 1.5 times of the max. value specified for a new display
 - The contrast is reduced to 0.5 of the original contrast value
 - Loss of function
 - The number of cosmetic defects exceeds the maximum defined

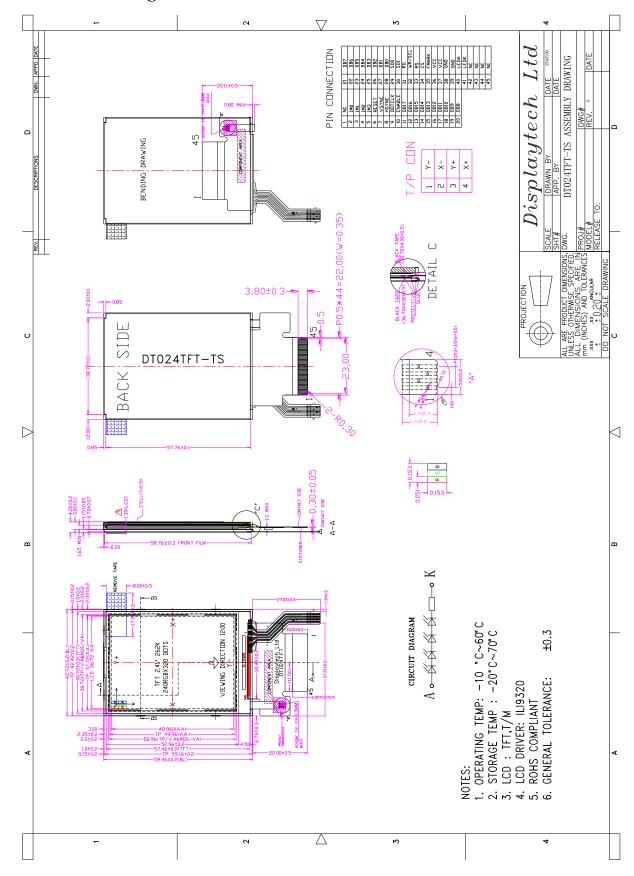
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${\bf 3.\ \ Mechanical\ Drawing-SDT024TFT}$



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Mechanical Drawing – SDT024TFT-TS



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4. Interface Description

Pin no	Symbol	Level	Description								
1	NC		No connection								
2~5	IM0~IM3	H/L	IM3	IM2	IM1	IMO	MPU interface	DB Pin			
			0	0	1	0	80-system, 16 bit	DB[10:17], DB[1:8]			
			0	0	1	1	80-system, 8 bit	DB[10:17]			
			1	0	1	0	80-system, 18 bit	DB[0:17]			
			1	0	1	1	80-system, 9 bit	DB[9:17]			
	DEGET	***		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1							
6	RESET	H/L	Reset sig			1.C. D.C	VD : 4 C 4:	T			
7	VSYNC						B interface operation				
8	HSYNC						interface operation.				
9	DOTCLK					3 interfa	ce operation. The dat	a input timing is on the rising			
10	ENIADLE		edge of			CD :	C				
10	ENABLE DRO	 TT/T			ai for K	JB inter	face operation				
11~28	DB17~DB0	H/L	Data bus		(CDO)		. 1	TD1 1			
29	SDO	H/L	Serial data output (SDO) pin in serial interface operation. The data is outputted on the falling edge of the SCL signal.								
20	CDI	TT /T					11.4.6	TD1 1			
30	SDI	H/L	Serial data input (SDI) pin in serial interface operation. The data is inputted on the								
21	DD.	TT /T	rising edge of the SCL signal. Read strobe signal in 80-system bus interface operation and enables read operation								
31	RD	H/L	when RI			system i	ous interface operatio	n and enables read operation			
32	WR/SCL	H/L				arratam	hua intarface energtic	on and enables write operation			
32	W K/SCL	Π/L						serial interface operation.			
33	RS	H/L	Register			nonous	TIOCK SIGNAL (SCL) III	serial interface operation.			
34	CS	H/L	Chip sel								
35	FMARK		_			which i	s used when writing d	lata to the internal RAM.			
36	VCC	2.8V			c signai,	WIIICII I	s used when writing d	ata to the internal KAWI.			
37	VCC	2.8V	Power supply Power supply								
38	GND	0V	Ground	^ ·							
39	GND	0V	Ground								
40	LEDA			LED Backlight Anode							
41	LEDK		LED Backlight Cathode								
42~45	NC		No connection								
42~43	NC	-	NO CONNECTION								

Touch Screen FPC (only for SDT024TFT-TS)

	_ (-)
Pin no	Symbol
1	Y-
2	X-
3	Y+
4	X+

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5. Absolute Maximum Ratings

Item	Symbol	Rating	Unit
Supply voltage range	Vcc - GND	-0.3 to + 4.6	V
Input voltage range	Vin	-0.3 to Vcc + 0.3	V
Operating Ambient Temperature	Тор	-20 ~ +70	°C
Operating Ambient Humidity	Нор	10 ~ 90 (Max 60°C)	% RH
Storage Temperature	Tstg	-30 ~ +80	°C
Storage Humidity	Нѕтс	10 ~ 90 (Max 60°C)	% RH

6. Electrical Characteristics

DC Characteristics

Item	Symbol	Rating	Unit
Power supply	Vcc	2.7 to 2.9	V
Input current	Idd	22.5 max	mA
Input voltage "H"	Vih	0.8Vcc to Vcc	V
Input voltage "L"	VIL	-0.3 to 0.2Vcc	V
Output voltage "H"	Voh	0.8Vcc to Vcc	V
Output voltage "L"	Vol	Vss to 0.2Vcc	V

7. Display Controller /Power Supply Timing

See Display Controller Specification: ILITEK ILI9320

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8. Operational EMC Requirements

The operational EMC immunity requirements and emission limits for DISPLAYTECH modules are provided in table 1: EMC specification for operational modules.

Table 1. EMC specification for operational modules

EMC phenomena	REFERENCE standard	Frequency range	Level/ Limit	Test specification	Performance criteria
Electromagnetic field	IEC 61000-4-3	30MHz- 1000MHz 3 V/m		1kHz sine, 80% AM	С
EFT/Burst	EFT/Burst IEC 61000-4-4 n.a. 10 V		-8us/50us	C	
				-10ns/100ns	C
Electrostatic Discharge*	IEC61000-4-2	n.a. 4 kV/ 8 kV		Contact/ Air	С
Conducted RF signals	IEC 61000-4-6	150kHz- 30MHz	1 V	1kHz sine, 80% AM	С
Radiated emission	IEC 61000-6-4	C 61000-6-4 30 MHz- 1000MHz 47 6		d = 10 m	n.a.

After a charge of 4kV, the display module is allowed to go down for 2 seconds and need to comeback again. With 8kV the display module is allowed to go down and has to comeback after a reset.

9. Optical Characteristics

Ite	m	Symbol	Condition	Min	Тур	Max	Unit	Remark	Note
Response Time		Tr + Tf	0-00		36.8	55.2	ms	Fig 2	4
Contrast ratio		Cr	Ø=0° Θ=0°	314	628				1
Luminance Un	iformity	δ White	Ta=25°C	74	82		%		3
Surface Lumin	ance	Lv	1 a=25 C	207	259		cd/m²		2
			Ø=90°	70	80				
Viewing Angle		θ	Ø=270°	49	59		daa	Ein 1	6
viewing Angle	Viewing Angle range		Ø=0°	70	80		deg	Fig 1	U
			Ø=180°	70	80				
	D 1			0.543	0.593	0.643			
	Red	у	θ=0°	0.300	0.350	0.400			
	Green	X		0.268	0.318	0.368			
CIE (x,y) Chromaticity	Green	y	Ø=0°	0.468	0.514	0.568			5
	Blue x y	X	0=0° Ta=25°C	0.097	0.147	0.197			3
		1 a=25 C	0.068	0.118	0.168				
	White	X		0.233	0.293	0.353			
	wnite	v		0.256	0.316	0.376			

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Note 1: Contrast Ratio = Average Surface Luminance with all white pixels (P₁,P₂, P₃,P₄,P₅) Average Surface Luminance with all black pixels (P₁, P₂, P₃,P₄,P₅)

Note 2: Surface luminance is the LCD surface from the surface with all pixels displaying white. Lv = Average Surface Luminance with all white pixels (P1, P2, P3,P4,P5)

Note 3: The uniformity in surface luminance, δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance.

δ WHITE = Minimum Surface Luminance with all white pixels (P₁, P₂, P₃,P₄, P₅)
Maximum Surface Luminance with all white pixels (P₁, P₂, P₃,P₄, P₅)

Note 4: Response time is the time required for the display to transition from White to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see FIG 2.

Note 5: CIE (x, y) chromaticity: The x,y value is determined by measuring luminance at each test position 1 through 5, and then taking average value

Note 6: Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For additional information see Fig 1.

Fig.1 (Definition of Viewing Angle)

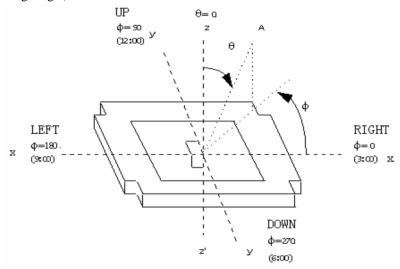
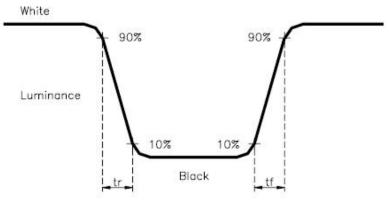


Fig. 2 (The response time is defined as the time interval between the 10% and 90% amplitudes. Refer to figure below.)



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10.Backlight Specification

ITEM	PARA	UNIT	
COLOR	WHITE		
CHROMATICITY COORDINATE	X=0.283-0.330 Y=0.276-0.339		
AVERAGE LUMINOUS INTENSITY (LV)	3300 to 450	cd/m ²	
NO.OF LED SMT			
FORWARD VOLTAGE (VF)	12.7 to 13.	V	

11. Safety Precaution

Handling precautions:

This device is susceptible to Electro-Static Discharge (ESD) damage. Observe Anti-Static precautions.

Power supply precautions:

- Identify and, at all times, observe absolute maximum ratings for both logic and LC some variance between models.
- Prevent the application of reverse polarity to VCC and GND, however briefly.
- Use a clean power source free from transients. Power up conditions are occasionally "jolting" and may exceed the maximum ratings of the modules.
- The VCC power of the module should also supply the power to all devices that may access the display. Don't allow the data bus to be driven when the logic supply to the module is turned off.

Operating precautions:

- DO NOT plug or unplug the module when the system is powered up.
- Minimize the cable length between the module and host MPU.
- Operate the module within the limits of the modules temperature specifications.

Mechanical/Environmental precautions:

- Improper soldering is the major cause of module difficulty. Use of flux cleaner is not recommended as they may seep under the elastomeric connection and cause display failure.
- Mount the module so that it is free from torque and mechanical stress.
- Surface of the LCD panel should not be touched or scratched. The display front surface is an easily scratched, plastic polarizer. Avoid contact and clean only when necessary with soft, absorbent cotton dampened with petroleum benzene.
- Always employ anti-static procedure while handling the module.
- Prevent moisture build-up upon the module and observe the environmental constraints for storage temperature and humidity.
- Do not store in direct sunlight
- If leakage of the liquid crystal material should occur, avoid contact with this material, particularly ingestion. If the body or clothing becomes contaminated by the liquid crystal material, wash thoroughly with water and soap