



# Dual N-Channel 30-V (D-S) MOSFET with Schottky Diode

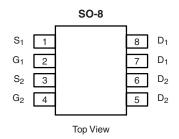
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)			
30	0.022 at V <sub>GS</sub> = 10 V	7.5			
30	0.030 at V <sub>GS</sub> = 4.5 V	6.5			

SCHOTTKY PRODUCT SUMMARY					
V <sub>DS</sub> (V)	V <sub>SD</sub> (V) Diode Forward Voltage	I <sub>F</sub> (A)			
30	0.50 V at 1.0 A	2.0			

### **FEATURES**

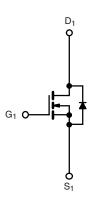
- Halogen-free According to IEC 61249-2-21 Definition
- LITTLE FOOT® Plus
- Compliant to RoHS directive 2002/95/EC



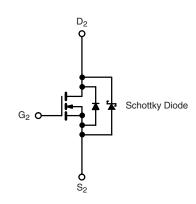


Ordering Information: Si4808DY-T1-E3 (Lead (Pb)-free)

Si4808DY-T1-GE3 (Lead (Pb)-free and Halogen-free)







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	10 s	Steady State	Unit				
Drain-Source Voltage		$V_{DS}$	30		V			
Gate-Source Voltage		$V_{GS}$	±					
0 "	T <sub>A</sub> = 25 °C		7.5	5.7				
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C	- I <sub>D</sub>	6.0	4.6				
Pulsed Drain Current		I <sub>DM</sub>	30		Α			
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.7	0.9				
	T <sub>A</sub> = 25 °C	D.	2.0	1.1	w			
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	P <sub>D</sub>	1.3	0.7	] vv			
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C			

THERMAL RESISTANCE RATINGS									
			MOSFET		Schottky				
Parameter		Symbol	Тур.	Max.	Тур.	Max.	Unit		
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	R <sub>thJA</sub>	52	62.5	53	62.5			
	Steady-State	' 'thJA	93	110	93	110	°C/W		
Maximum Junction-to-Foot (Drain)	Steady-State	$R_{thJC}$	35	40	35	40			

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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MOSFET SPECIFICATION	NS T <sub>J</sub> = 2	25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit			
Static	•				•				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		0.8			V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$				± 100	nA		
		V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V	Ch-1			1			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	VDS = 24 V, VGS = 0 V	Ch-2			100	μΑ		
2010 Gate Voltage Diam Current	.033	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	Ch-1			15	μΑ		
			Ch-2			2000			
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$		20			Α		
Drain-Source On-State Resistance <sup>b</sup>	Book )	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.5 A			0.018	0.022	0		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 6.5 \text{ A}$			0.024	0.030	Ω		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 7.5 \text{ A}$			22		S		
Diada Famuard Valtarah	V <sub>SD</sub>	I <sub>S</sub> = 1 A, V <sub>GS</sub> = 0 V	Ch-1		0.8	1.2	V		
Diode Forward Voltage <sup>b</sup>	V SD	ig - i A, v <sub>G</sub> g - v	Ch-2		0.47	0.5			
Dynamic <sup>a</sup>									
Total Gate Charge	$Q_g$				13	20			
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 7$	.5 A		2		nC		
Gate-Drain Charge	$Q_{gd}$				2.7				
Gate Resistance	$R_g$			0.5		3.2	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>				8	16			
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$ $I_D \cong$ 1 A, $V_{GEN}$ = 10 V, $R_g$ = 6 $\Omega$			10	20	ns		
Turn-Off Delay Time	t <sub>d(off)</sub>				21	40			
Fall Time	t <sub>f</sub>				10	20			
Source-Drain Reverse Recovery	t <sub>rr</sub>	I <sub>E</sub> = 1.7 A, dI/dt = 100 A/μs	Ch-1		40	80			
Time	۲rr	1- 1.7 Α, αι/αι – 100 Α/μο	Ch-2		32	70			

#### Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.

SCHOTTKY SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Forward Voltage Drop	V <sub>F</sub>	I <sub>F</sub> = 1.0 A		0.47	0.50	V		
		I <sub>F</sub> = 1.0 A, T <sub>J</sub> = 125 °C		0.36	0.42			
Maximum Reverse Leakage Current	I <sub>rm</sub>	V <sub>R</sub> = 30 V		0.004	0.100			
		V <sub>R</sub> = 30 V, T <sub>J</sub> = 100 °C		0.7	10	mA		
		V <sub>R</sub> = - 30 V, T <sub>J</sub> = 125 °C		3.0	20	1		
Junction Capacitance	C <sub>T</sub>	V <sub>R</sub> = 10 V		50		pF		

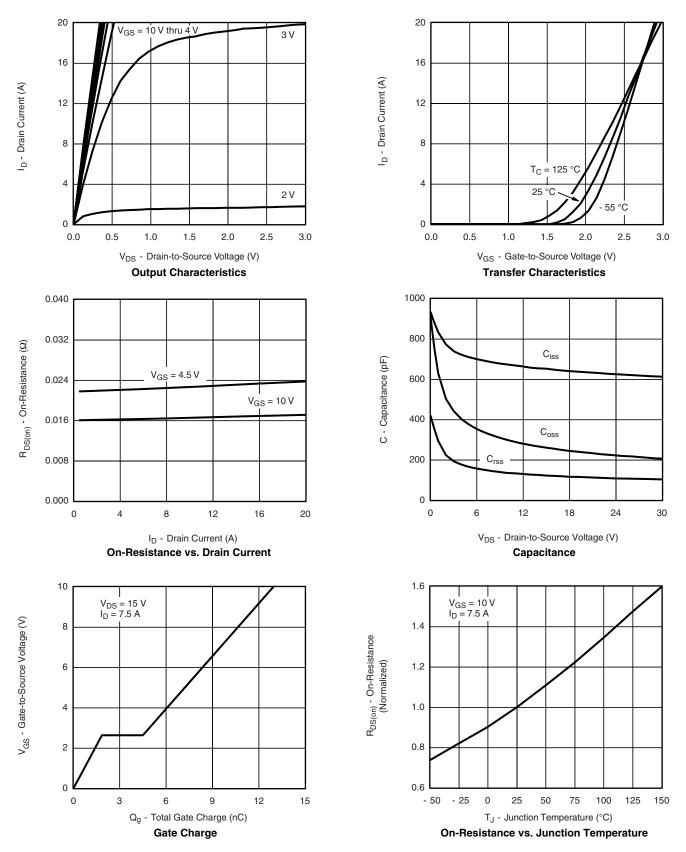
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







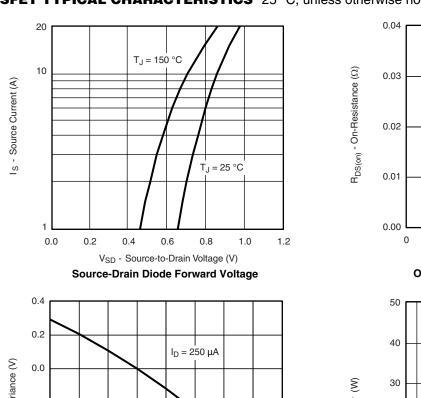
### MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

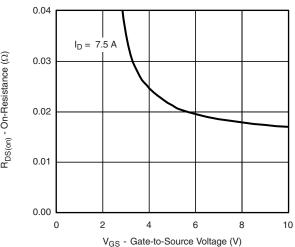


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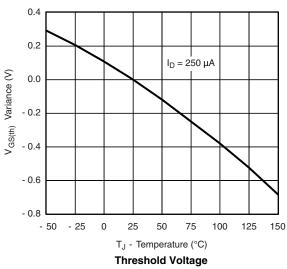
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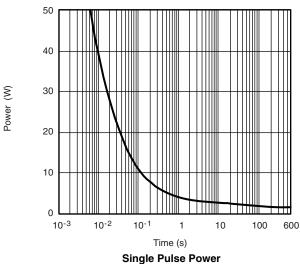
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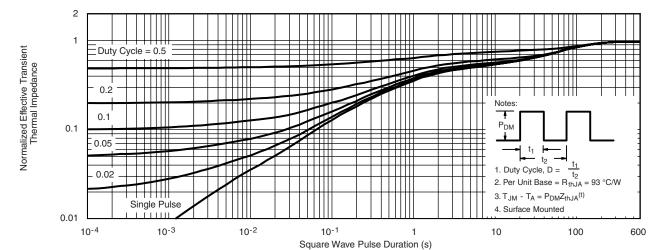




On-Resistance vs. Gate-to-Source Voltage





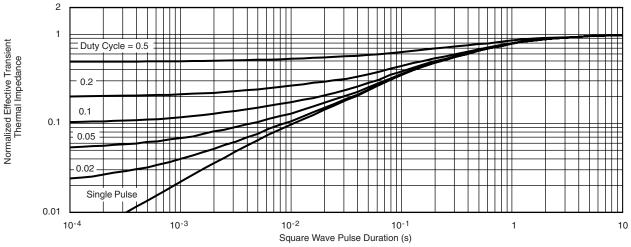


Normalized Thermal Transient Impedance, Junction-to-Ambient



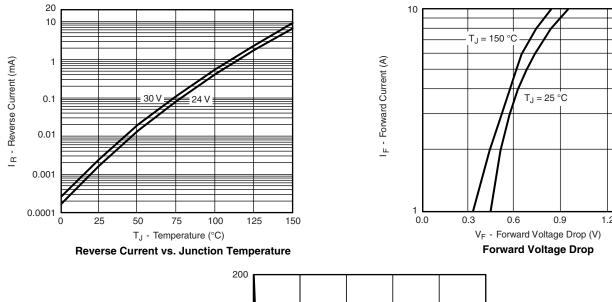


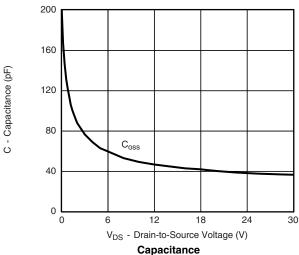
### MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

### SCHOTTKY TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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